xLogic User Manual








Easy Electronic Co.,Ltd

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Introduction

Congratulations with your xLogic SuperRelay provided by Easy Electronic Co., Ltd.

The xLogic SuperRelay is a compact and expandable CPU replacing mini PLCs, multiple timers, relays and counters.

The xLogic SuperRelay perfectly fits in the space between timing relays and low-end PLCs. Each CPU incorporates not only a real-time clock and calendar, but also provides support for optional expansion I/O modules to enhance control and monitoring applications. Data adjustments can easily be performed via the keypad, the LCD display, or through the easy-to-use xLogic soft. DIN-rail and panel-mounted options are both are available, offering full flexibility to the various installation needs of your application.

The xLogic SuperRelay is available in 120V/240V AC or 12V and 24V DC versions, making it the ideal solution for relay replacement, or simple control applications as building and parking lot lighting, managing automatic lighting, access control, watering systems, pump control, ventilation systems, home automation and a wide field of other applications demanding low cost to be a primary design issue.

We strongly recommended taking the time to read this manual, before putting the xLogic SuperRelay to work. Installation, programming and use of the unit are detailed in this manual. The feature-rich xLogic SuperRelay provides a for off-line operation mode, allowing full configuration and testing prior to in-field service commissioning. In reviewing this manual you will discover many additional advantageous product properties, it will greatly simplify and optimize the use of your xLogic SuperRelay.

Valid range of this manual

The manual applies to devices of ELC series modules . For more information about SMS module or Ethernet module ,please refer to the SMS module or Ethernet module user's manual.

Safety Guideline

This manual contains notices you have to observe in order to ensure your personal safety, as well as to prevent damage to property. The notices referring to your personal safety are highlighted in the manual by a safety alert symbol; notices referring to property damage only have no safety alert symbol. The notices shown below are graded according to the degree of danger.

Caution
Indicates that death or severe personal injury may result if proper precautions are not taken

Caution
With a safety alert symbol indicates that minor personal injury can result if proper precautions are not taken.

Caution
Without a safety alert symbol indicates that property damage can result if proper precautions are not taken.

Attention
Indicates that an unintended result or situation can occur if the corresponding notice is not taken into account.

If more than one degree of danger is present, the warning notice representing the highest degree of danger will be used. A notice warning of injury to persons with a safety alert symbol may also include a warning relating to property damage.

Qualified Personnel

The device/system may only be set up and used in conjunction with this documentation. Commissioning and operation of a device/system may only be performed by qualified personnel. Within the context of the safety notices in this documentation qualified persons are defined as persons who are authorized to commission, ground and label devices, systems and circuits in accordance with established safety practices and standards.

Prescribed Usage

Note the following:

Warning

This device and its components may only be used for the applications described in the catalog or the technical description, and only in connection with devices or components from other manufacturers which have been approved or recommended by EASY. Correct, reliable operation of the product requires proper transport, storage, positioning and assembly as well as careful operation and maintenance.

Trademarks

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Disclaim of Liability

We have reviewed the contents of this publication to ensure consistency with the hardware and software described. Since variance cannot be precluded entirely, we cannot guarantee full consistency. However, the information in this publication is reviewed regularly and any necessary corrections are included in subsequent editions.

Additional support

We take pride in answering your question as soon as we can:
Please consult our website at www.xLogic-relay.com for your closest point of contact or email us at sales@xlogic-relay.com

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Chapter 1 General Introduction to xLogic

Overview

xLogic SuperRelay is a universal logic module made by Easy.

xLogic SuperRelay, a compact, expandable CPU that can replace mini PLC, multiple timers, relays and counters, Splitting the difference between a timing relay and a low-end PLC, Each CPU houses a real-time clock and calendar, and supports optional expansion I/O modules to enhance your control and monitoring applications . Data adjustments can be done via the on-board keypad and LCD display, or with xLogic soft. It can be either DIN-rail or panel mounted, depending upon the needs of your application, and it is available in 120V/240V ac as well as 12V and 24V dc versions, and it is the ideal solution for relay replacement applications, simple control applications such as building and parking lot lighting, managing automatic lighting, access control, watering systems, pump control, or ventilation systems in factory, and home automation and applications in which cost is a primary design issue.

Highlight features:

- 4-lines, 10-character per line, backlight display.
- Multiple value display and input via keypad and LCD display.
- Keypanel programming feature (optional)
- Function Block Diagram
- Standard Modbus RTU/ASCII communication protocol supported.
- It's optional for xLogic to act as slave or master in certain Modbus RTU communication network.
- CAN BUS protocol based expansion modules
- Expandable up to 9 linked IO expansion modules reaching 162 I/O points in maximum
- 1 RS232 port and 2 RS485 ports built-in (merely applied to standard ELC-12 Series); 1 RS232 port and 1 RS485 port built-in (merely applied to ELC-18 Series)
- Optional Ethernet connectivity
- SMS/GSM module for remote control, monitoring and alarm
- Multiple channels analog inputs available with DC 0-10V signal ,PT100 signal& 0/4….20mA.
- Default Real Time Clock (RTC)
- Backup at Real Time Clock (RTC) at 25 °C:72 hours(ELC-6&economy ELC-12);100 hours(Standard ELC-12&Upgraded ELC-18)
- Two channels high-speed counting (ELC-18 Series CPU :14KHz; Upgraded ELC-18&ELC-12 Series CPU:60KHz)
- Pre-configured standard functions, e.g. on/ off-delays, pulse relay and softkey
- 2 PWM channels(333Hz)
- Retentive memory capability (Not applied to ELC-6&economic ELC-12)
- RS232 communication download cable with photo-electricity isolation
- USB communication download cable with photo-electricity isolation
- Programmable capability up to 256 function blocks(ELC-18) ,512 function blocks(ELC-12)
- Mounting via modular 35mm DIN rail or screw fixed mounting plate
- On-line monitor capability
- Compact design
- HMI(LCD) separate installation available,e.g. xLogic can be installed inside cabinet and HMI mounted in it's front panel
- Datalogging
- Low cost

Some of the things xLogic can do for you?

The xLogic SuperRelay provides solutions for commercial, industrial, building and domestic applications such as lighting, pumping, ventilation, shutter operations or in switching cabinets. The application field is widespread and these are just a few to mention.
Using the RS485 bus and Ethernet connectivity allows the user to realize various extensive (real-time) monitoring and control applications. Utilizing the optional SMS module the systems can furthermore be remotely controlled via (GSM) cell phone. SMS Alarms, status updates and any other desirable messages can be provided at set triggers.

Special versions without operator panel and display unit are available for series production applications in small machine, installation and cabinet building environments to further slash cost.

xLogic devices:
xLogic Basic is available in two voltage classes:
*Classes 1: DC12-24V: i.e.: ELC-18DC Series and ELC-12DC Series.

*Classes2:AC110-240V: i.e.: ELC-18AC Series and ELC-12AC Series

In the versions:

* With Display: ELC-18 Series (12 inputs and 6 outputs)
* Optional (With/without) Display: ELC-12 Series (8 inputs and 4 outputs)

ELC-18 Series is equipped with an expansion bus (Can Bus)
Each Version is provides 44 pre-configured standard and special function blocks for the creation of your circuit program.

Expansion modules:
ELC-E (applied to ELC-18 CPU)

* xLogic digital modules are available for operation with 12…24V DC, and 110.. .240 V AC, and are equipped with eight inputs and eight outputs.
* xLogic analog modules are available for operation with 12…24 V DC and are equipped with six digital and two analog inputs.

ELC12-E(applied to ELC-12 CPU)

* xLogic digital modules are available for operation with 12…24V DC, and 110.. .240 V AC, and are equipped with four inputs and four outputs.
* xLogic analog modules are available for operation with 12…24 V DC and are equipped with four digital/analog inputs.

Communication modules:

- **xLogic:RS232 communication cable (type:ELC-RS232)**

It is kind of universal cable with photoelectricity isolation which can be directly connected to standard 9-pin port of PC, also kind of interface module which can enable user's program to be downloaded into xLogic main module through xLogicsoft for running.It also is the connection cable between CPU and third party device with the RS232 port(just as HMI) in modbus communication system.

- **xLogic: USB communication cable (type: ELC-USB).**

It is kind of communication cable with photoelectricity isolation through which PC with USB port only can be connected to xLogic main module, moreover, it has same features as ELC-RS232 module, so it is quite convenient for user whose computer has no standard serial port.

- **xLogic: Ethernet module（type：ELC-Ethernet）**

It is called Ethernet module, used to connect xLogic main modules in different places to enormous Ethernet to buildup a huge monitoring and control system. It contains DC and AC two types.

- **xLogic:SMS module（type：ELC-SMS）**

ELC-SMS is kind of SMS module, through which SMS can be regarded as expansion input by user to realize wireless remote control and it can send alarm messages to user cell phones.

Communication / Network

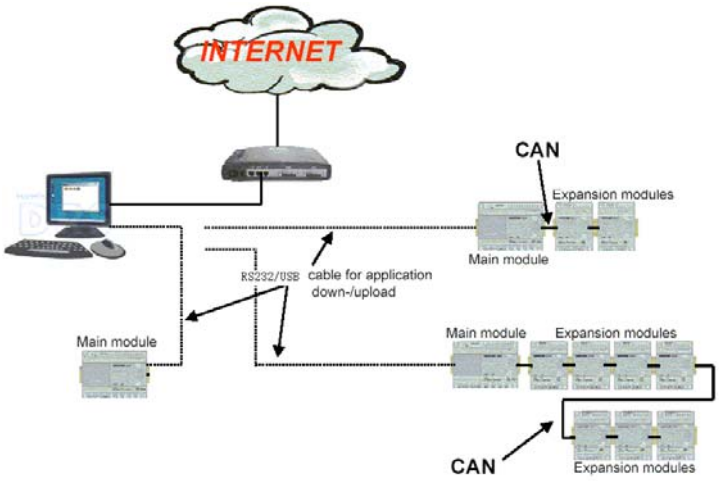
xLogic offers different ways to communicate within the system.

RS485 port

The RS485 port is used for communication between the main module and various devices or equipments which have the standard RS485 port. Communicate using Modbus RTU/ASCII protocol.

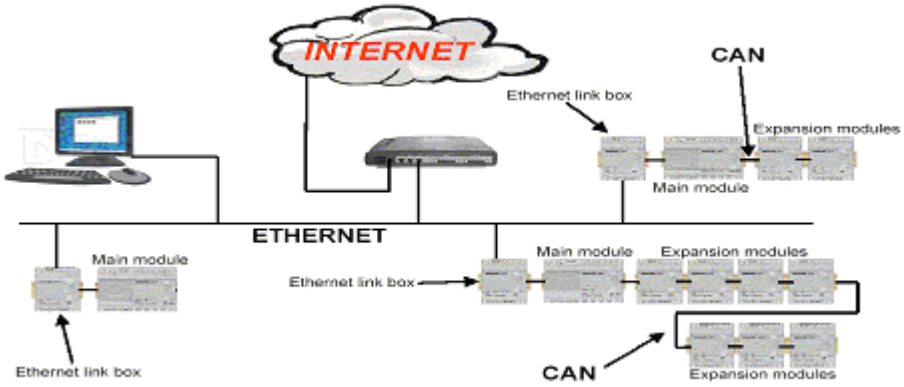
RS232 or USB port

If there is no network required and only one main module with some expansion modules is needed for the application, the down- and upload of the project to and from the main module happens over the standard RS232 or USB port. It allows system maintenance like monitoring too.



Ethernet network

If the application requires a system where more than one main module is needed and these main modules have to communicate, each main module will be connected over an Ethernet Module box to the Ethernet. The project down- and upload to and from the main modules and the communication between the main modules happens over the Ethernet network. Furthermore the visualization of the whole system is possible and easy to realize a personal computer.



CAN bus

The communication between the main module and the expansion modules or the remote I/O happens over a CAN bus. CAN is a widely used bus system（only ELC-18 Series available, not applied to ELC-12 series）

Note

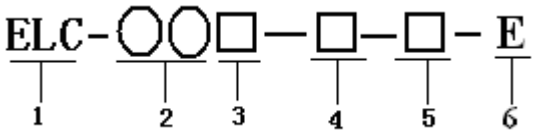
xLogic Main Module may be equipped with expansion modules of the different voltage class, but expansion module must be supplied the correct power corresponding to its type.

Each xLogic Main Module provides the following connections for the creation of the circuit program, regardless of the number of connected blocks:

- Digital inputs I1 to IC（ELC-18），I1 to I8(ELC-12).
- Analog inputs AI1 to AI8
- Digital outputs Q1 to Q6（ELC-18），Q1 to Q4(ELC-12)
- Digital flag blocks F1-F64(applied to standard ELC-12&Upgraded ELC-18 CPU) ; F1-F32(applied to other ELC series CPU)
  -F8 : Startup flag
- Analog flag blocks AF1 to AF64(applied to standard ELC-12&Upgraded ELC-18 CPU); AF1-AF32(applied to other ELC series CPU)
- Shift register bits S1 to S8
- 4 cursor keys

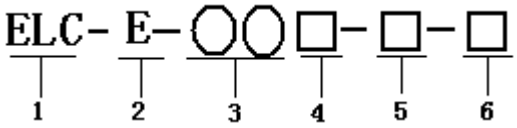
Naming Rules of ELC Series

Model name（main module）:



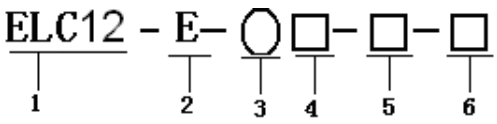
1. Series name
2. Points of input and output
3. Supply power AC or DC
4. Digital/Analog D: digital DA: digital/analog L: with photoelectricity isolation
5. Output type R: relay T: transistor
   TN = "PNP" transistor; TP= "NPN" transistor
6. E: economic mode

Model name (expansion module ,plus with ELC-18 CPU together to use):



1. Series name
2. E: expansion module
3. Points of input and output
4. Supply power AC or DC
5. Digital/Analog D: digital DA: digital/analog
6. Output type R: relay TP: "NPN" transistor；TN :"PNP" transistor

Model name (expansion module ,plus with ELC-12 CPU together to use):



1. Series name
2.E: expansion module
3.Points of input and output
4.Supply power AC or DC
5.Digital/Analog DA: digital/analog
6.Output type R: relay TP: "NPN" transistor；TN :"PNP" transistor

Standard ELC-12 Series CPU Units(new version)

Model	Expansion	Brief Description	Supply voltage	Inputs	Outputs	High-speed count	PWM	HMI	RTC
ELC-12AC-R-CAP	YES	CPU with 13-LED-indicators COVER	AC 110~240V	8 digital	4 relays (10A)	NO	NO	optional	yes
ELC-12DC-DA-R-CAP	YES	CPU with 13-LED-indicators COVER	DC12V-DC24V	4 digital/analog(I1-I4) + 4 digital (I5-I8)	4 relays (10A)	I5,I6(Max.14kHz)I7 ,I8(Max.60k Hz)	NO	optional	yes
ELC-12DC-DA-TN-CAP	YES	CPU with 13-LED-indicators COVER	DC12V-DC24V	4 digital/analog(I1-I4) + 4 digital (I5-I8)	4 transistors(PNP)	I5,I6(Max.14kHz)I7 ,I8(Max.60k Hz)	2ch(Q 3,Q4)	optional	yes
ELC-12DC-DA-TP-CAP	YES	CPU with 13-LED-indicators COVER	DC12V-DC24V	4 digital/analog(I1-I4) + 4 digital (I5-I8)	4 transistors(NPN)	I5,I6(Max.14kHz)I7 ,I8(Max.60k Hz)	2ch(Q 3,Q4)	optional	yes
ELC-12AC-R-HMI	YES	CPU with keypad panel/LCD	AC 110~240V	8 digital	4 relays(10A)	NO	NO	optional	yes
ELC-12DC-DA-R-HMI	YES	CPU with keypad panel/LCD	DC12V-DC24V	4 digital/analog(I1-I4) + 4 digital (I5-I8)	4 relays (10A)	I5,I6(Max.14kHz)I7 ,I8(Max.60k Hz)	NO	optional	yes
ELC-12DC-DA-TN-HMI	YES	CPU with keypad panel/LCD	DC12V-DC24V	4 digital/analog(I1-I4) + 4 digital (I5-I8)	4 transistors (PNP)	I5,I6(Max.14kHz)I7 ,I8(Max.60k Hz)	2ch(Q 3,Q4)	optional	yes
ELC-12DC-DA-TP-HMI	YES	CPU with keypad panel/LCD	DC12V-DC24V	4 digital/analog(I1-I4) + 4 digital (I5-I8)	4 transistors (NPN)	I5,I6(Max.14kHz)I7 ,I8(Max.60k Hz)	2ch(Q 3,Q4)	optional	yes

Economic ELC-12 Series CPU Units

ELC-12AC-R-E-CAP	NO	CPU with 13-LED-indicators COVER	AC 110~240V	8 digital	4 relays (10A)	NO	NO	optional	yes
ELC-12DC-D-R-E-CAP	NO	CPU with 13-LED-indicators COVER	DC12V-DC24V	4 digital/analog(I1-I4) + 4 digital (I5-I8)	4 relays (10A)	NO	NO	optional	yes
ELC-12DC-D-TN-E -CAP	NO	CPU with 13-LED-indicators COVER	DC12V-DC24V	4 digital/analog(I1-I4) + 4 digital (I5-I8)	4 transistors (PNP)	NO	NO	optional	yes
ELC-12DC-D-TP-CAP	NO	CPU with 13-LED-indicators COVER	DC12V-DC24V	4 digital/analog(I1-I4) + 4 digital (I5-I8)	4 transistors (NPN)	NO	NO	optional	yes

Standard ELC-12 Series Expansion Modules

Model	Supply voltage	Inputs	Outputs
ELC12-E-8AC-R	AC 110~240V	4 Digital	2 Relays(3A,Q1-Q2) +2 Relays(10A,Q3-Q4)
ELC12-E-8DC-DA-R	DC12V – DC24V	4 Digital / analog	2 Relays(3A,Q1-Q2) +2 Relays(10A,Q3-Q4)
ELC12-E-8DC-DA-TN	DC12V – DC24V	4 Digital / analog	4 Transistors（PNP）
ELC12-E-8DC-DA-TP	DC12V – DC24V	4 Digital / analog	4 Transistors（NPN）
ELC12-E-PT100	DC12V – DC24V	2 Channels PT100, resolution: 0.1°(12bits), temperature range : -50℃- 200℃	none
ELC12-E-AQ-V	DC15V – DC24V	None	2 Channels (DC 0…10V), Voltage Signal
ELC12-E-AQ-I	DC12V – DC24V	None	2 Channels (0…20mA) , Current Signal
ELC12-E-AI(I)	DC12V – DC24V	4 Channels (0/4…..20 mA), Current Signal	none
ELC12-E-RS485	DC12V – DC24V	isolated 485 converter,used to bring out the terminals of RS485 port built-in ELC-12 series CPU for connection with third party devices.	
ELC12-E-ETHERNET-AC	AC 110-240V	Ethernet module	
ELC12-E-ETHERNET-DC	DC12V – DC24V	Ethernet module	

Accessories

ELC-HMI	Displaying and program-making keypad panel for ELC-12 series CPU , optional,
ELC-COVER-CABLE	Connection cable between ELC-12 CPU and ELC-HMI-FP (Faceplate) for long-distance application purpose, one and half meters standard length (customizable)
ELC-HMI-FP	Faceplate (ELC-HMI's installation unit), making it possible for ELC-HMI to be externally installed in the front door of cabinet for easy observation and operation while ELC-12 CPU is required to be installed inside.
ELC12-CB-A	A type connection bridge between ELC-12 CPU & Extension module. Free of charge !
ELC12-CB-B	connection cable between ELC-12 CPU & Extension module, it can be used to remotely connect ELC-12 CPU to its extension units, 3-meter standard length (customizable)
ELC-COVER	CPU'S cover with 13 LED indicators indicating IO status, if LCD not required, Optional
ELC-MEMORY	Real time datalogging device with a mini-SD card slot for ELC-12 CPUs. The history data(IO status , analog value, current value of registers) of ELC-12 CPU can be recorded, retrieved and viewed via it

Standard ELC-6 Series CPU Units								
Model	**Expansion**	**Supply voltage**	**Inputs**	**Outputs**	**High-speed count**	**PWM**	**HMI**	**RTC**
ELC-6AC-R	no	AC110~ AC240V	4 digital	2 relays (10A)	no	no	no	yes
ELC-6DC-D-R	no	DC12-24V	4 digital	2 relays (10A)	no	no	no	yes
ELC-6DC-D-TN	no	DC12-24V	4 digital	2 transistors (PNP)	no	no	no	yes

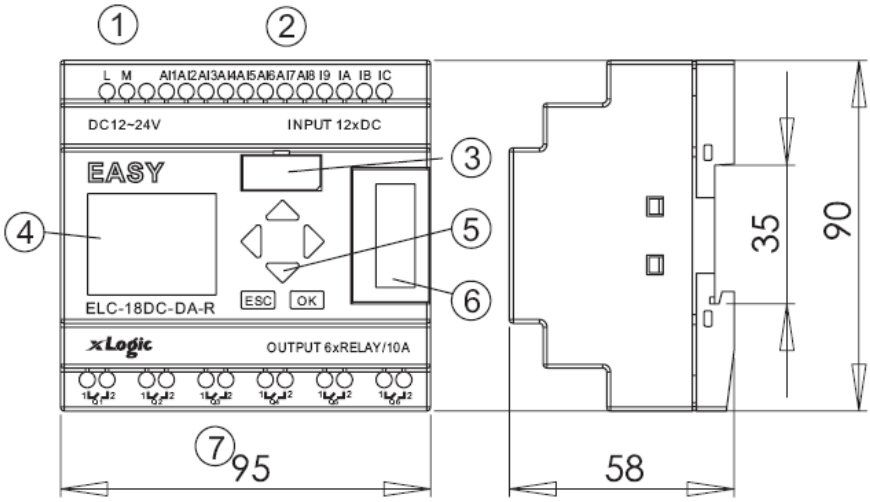
MODEL	**DESCRIPTION**							
Standard ELC-18 CPU UNITS								
	Expansion	Supply voltage	Inputs	Outputs	PWM	HMI	RTC	High-speed count
ELC-18AC-R	available	AC110~ AC240V	12 digital	6 relays（10A）	no	yes	yes	No
ELC-18DC-D-R	available	DC12-24V	12 digital	6 relays（10A）	no	yes	yes	2 Routes(14KHZ)
ELC-18DC-D-TP(NPN)	available	DC12-24V	12 digital	6 transistor (0.3A)	2 ch(Q5,Q6)	yes	yes	2 Routes(14KHZ)
ELC-18DC-D-TN(PNP)	available	DC12-24V	12 digital	6 transistor (0.3A)	2 ch(Q5,Q6)	yes	yes	2 Routes(14KHZ)
ELC-18DC-DA-R	available	DC12-24V	8analog/ digital+ 4digital	6 relays（10A）	no	yes	yes	2 Routes(14KHZ)
ELC-18DC-DA-TP(NPN)	available	DC12-24V	8analog/ digital+ 4digital	6 transistor (0.3A)	2 ch(Q5,Q6)	yes	yes	2 Routes(14KHZ)
ELC-18DC-DA-TN(PNP)	available	DC12-24V	8analog/ digital+ 4digital	6 transistor (0.3A)	2 ch(Q5,Q6)	yes	yes	2 Routes(14KHZ)
Economic ELC-18CPU UNITS								
	Expansion	Supply voltage	Inputs	Outputs	PWM	HMI	RTC	High-speed count
ELC-18AC-R-E	No	AC110~ AC240V	12 digital	6 relays（10A）	no	yes	yes	No
ELC-18DC-D-R-E	No	DC12-24V	12 digital	6 relays（10A）	no	yes	yes	2 Routes(14KHZ)
ELC-18DC-D-TP-E(NPN)	No	DC12-24V	12 digital	6 transistor (0.3A)	no	yes	yes	2 Routes(14KHZ)
ELC-18DC-D-TN-E(PNP)	No	DC12-24V	12 digital	6 transistor (0.3A)	no	yes	yes	2 Routes(14KHZ)
ELC-18DC-DA-R-E	No	DC12-24V	8analog/ digital+ 4digital	6 relays（10A）	no	yes	yes	2 Routes(14KHZ)
ELC-18DC-DA-TP-E(NPN)	No	DC12-24V	8analog/ digital+ 4digital	6 transistor (0.3A)	no	yes	yes	2 Routes(14KHZ)
ELC-18DC-DA-TN-E(PNP)	No	DC12-24V	8analog/ digital+ 4digital	6 transistor (0.3A)	no	yes	yes	2 Routes(14KHZ)
Upgraded ELC-18 CPU UNITS								
	Expansion	Supply voltage	Inputs	Outputs	PWM	HMI	RTC	High-speed count
ELC-18AC-R-U	Yes	AC110~ AC240V	12 digital	6 relays（10A）	no	yes	yes	No
ELC-18DC-D-R-U	Yes	DC12-24V	12 digital	6 relays（10A）	no	yes	yes	2 Routes(60KHZ)
ELC-18DC-D-TP-U(NPN)	Yes	DC12-24V	12 digital	6 transistor (0.3A)	2 ch(Q5,Q6)	yes	yes	2 Routes(60KHZ)
ELC-18DC-D-TN-U(PNP)	Yes	DC12-24V	12 digital	6 transistor (0.3A)	2 ch(Q5,Q6)	yes	yes	2 Routes(60KHZ)
ELC-18DC-DA-R-U	Yes	DC12-24V	12 digital	6 relays（10A）	no	yes	yes	2 Routes(60KHZ)
ELC-18DC-DA-TP-U(NPN)	Yes	DC12-24V	12 digital	6 transistor (0.3A)	2 ch(Q5,Q6)	yes	yes	2 Routes(60KHZ)
ELC-18DC-DA-TN-U(PNP)	Yes	DC12-24V	12 digital	6 transistor (0.3A)	2 ch(Q5,Q6)	yes	yes	2 Routes(60KHZ)

Standard ELC-18 Series Expansion Modules			
	Supply voltage	Inputs	Outputs
ELC-E-16AC-R	AC110~ AC240V	8 digital	4 relays（10A）+4 relays(3A)
ELC-E-16DC-D-R	DC12-24V	8digital	4 relays（10A）+4 relays(3A)
ELC-E-16DC-DA-R	DC12-24V	6digital+2analog/digital	4 relays（10A）+4 relays(3A)
ELC-E-16DC-D-TN	DC12-24V	8digital	8 transistors(PNP)(0.3A)
ELC-E-16DC-DA-TN	DC12-24V	6digital+2analog/digital	8 transistors(PNP)(0.3A)
ELC-E-PT100	DC12-24V	3 Channels PT100, resolution: 0.1°(12bits), temperature range : -50℃-200℃	none
ELC-E-AQ-V	DC15V – DC24V	none	2 Channels (DC 0…10V), Voltage Signal
ELC-E-AI(I)	DC12-24V	4 Channels (0/4…..20 mA), Current Signal	none
ELC-RS485	DC12-24V	isolated 485 converter,used to bring out the terminals of RS485 port built-in ELC-12 series CPU for connection with third party devices.	

Accessories	
ELC-RS232	RS232 communication module /download cable between PC and xLogic CPU units
ELC-USB	USB communication module /download cable between PC and xLogic CPU units
ELC-Ethernet-DC/AC	Ethernet module connecting to ELC-18 CPU units
ELC-SMS-D-R	SMS module can be connected to ELC-18 CPU units.(DC 24V power supply,6 digital inputs,4 relay outputs)
ELC-COPIER	ELC-COPIER can be used to save user program and download program into xLogics.

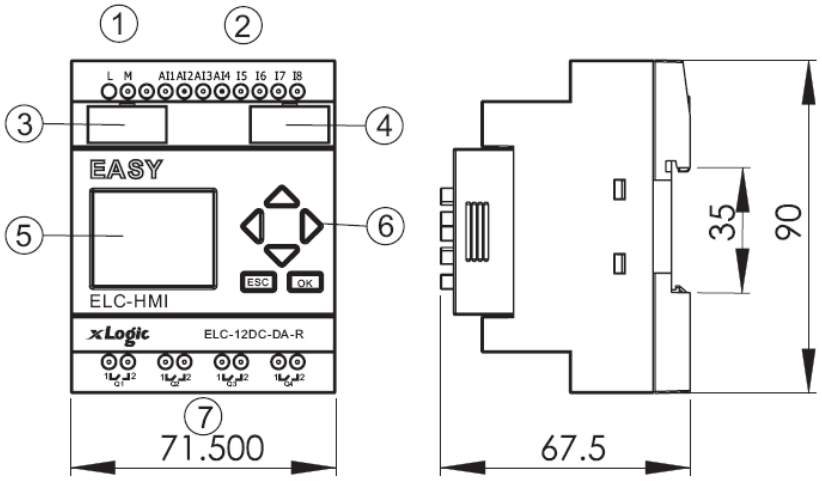
Structure

1. ELC-18 Series CPU



1.Power supply 2. Input 3. Program/RS232 port 4. HMI/LCD panel 5. keypad 6.Expansion+RS485 port 7.Output

2. ELC-12 Series CPU

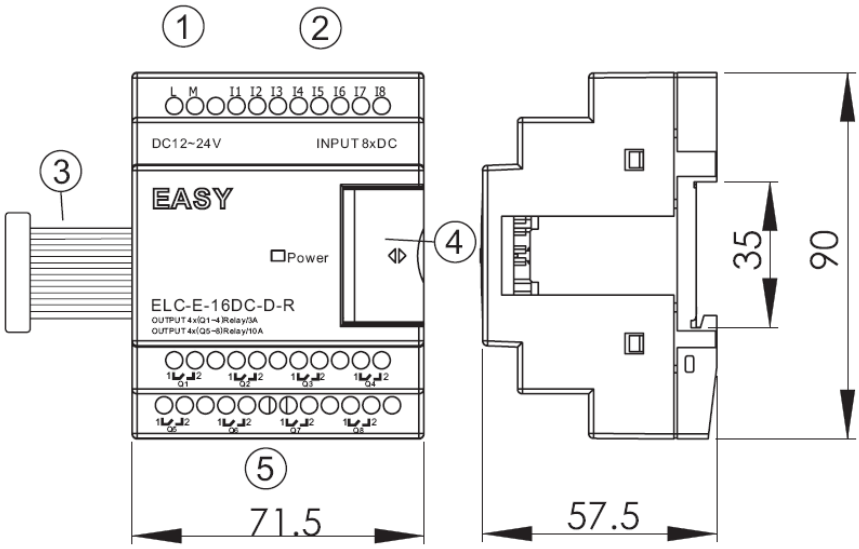


1. Power supply 2.Input 3. Program/RS232 port 4.Extension/RS485 port 5..HMI/LCD panel 6.keypad 7.Output

Notes:

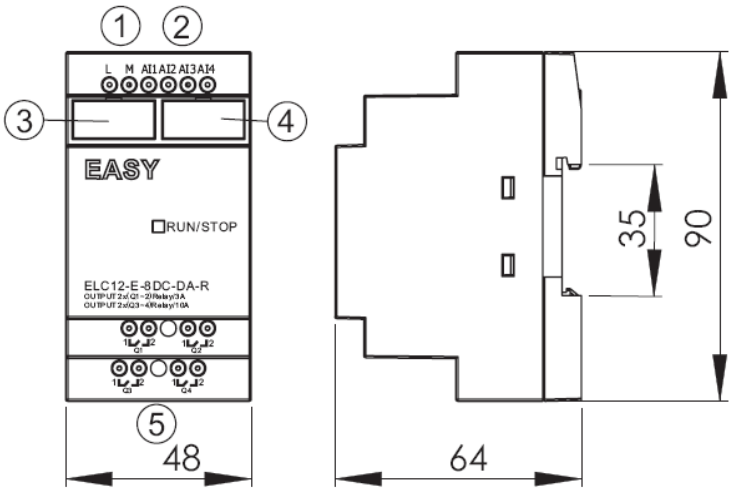
1. Program port/RS232 port(ELC-RS232 ,ELC-USB,ELC-Copier,ELC-MEMORY should be inserted in this port)
2. Extension port(it can be used as 2 RS485 ports ,ELC12-CB-A, ELC12-CB-B should be inserted in this port)

3. ELC-E Series Expansion Module(only use with ELC-18 CPUs)



1. Power supply 2. Input 3. Connection cable 4. Extension port 5. Output

4. ELC12-E Series Expansion Module (also apply to ELC-6 Series CPU)



1.Power supply 2. Input 3. extension port (left) 4.expansion port(Right)(Program port for applied to ELC-6 CPU)

Chapter 2 Installing/removing xLogic

Dimensions

The xLogic installation dimensions are compliant with DIN 43880.

xLogic can be snap-mounted to 35 mm DIN rails to EN 50022 or on the wall.

xLogic width:

- ELC-12 Series Main Module has a width of 72mm.
- ELC12-E expansion mddule have a width of 48mm
- ELC-18 Series Main Module has a width of 95mm.
- ELC-E expansion modules have a width of 72mm .

Note

The figure below shows you an example of the installation and removal of an ELC-12 CPU and one expansion module ELC-12 CPU. The measures shown apply to all other ELC-12 Series versions and ELC-18 Series versions and expansion modules.

W a r n i n g

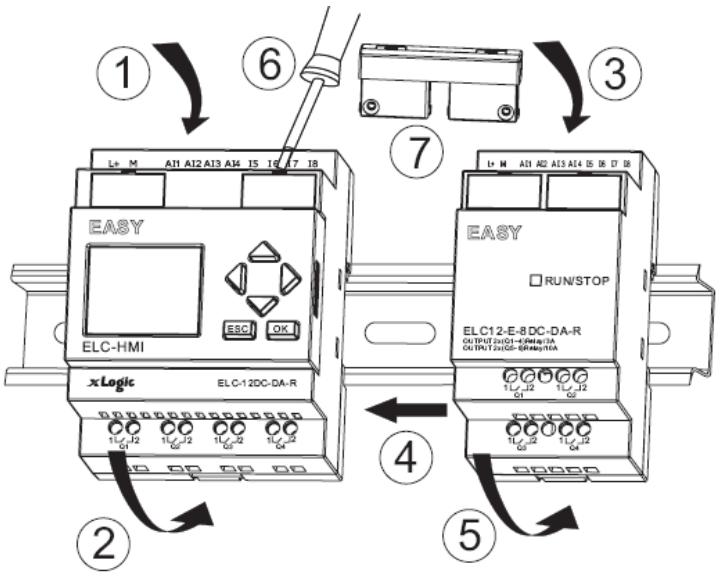
Always switch off power before you "remove" and "insert" an expansion module.

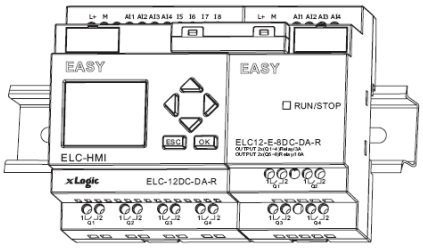
2.1.1 DIN rail mounting

Mounting

How to mount a xLogic Basic module and a expansion module onto a DIN rail:

1. Hook the xLogic Basic module onto the rail.
2. Push down the lower end to snap it on. The mounting interlock at the rear must engage.
3. Hook the xLogic expansion module onto the rail
4. Slide the digital module towards the left until it contacts the xLogic Basic.
5. Push down the lower end to snap it on. The mounting interlock at the rear must engage.
6. Take the plastic cover in the expansion port of CPU and expansion module.
7. Plus the connection bridge





Repeat the expansion module steps to mount further expansion modules.

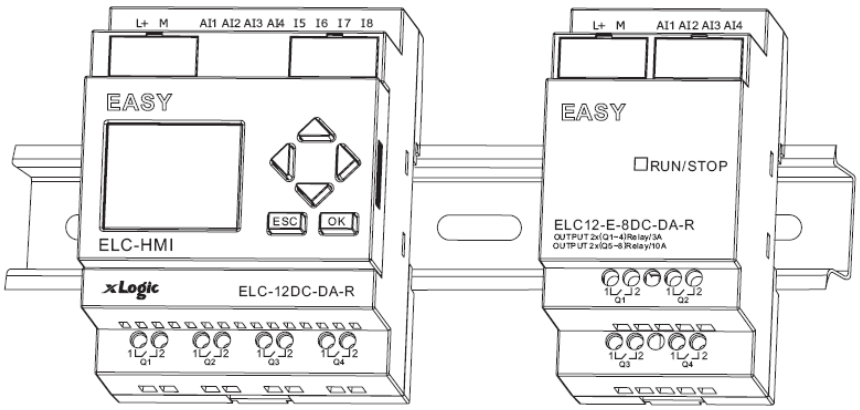
Notes: 1. ELC12-E extensions connect with ELC-12 CPU by ELC12-CB-A bridge or ELC12-CB-B connection cable (3 meters)
2. ELC-E extensions connect with ELC-18 CPU directly by the connector of the expansion module

Removal

To remove xLogic:

..... if you have installed only one xLogic Basic:

1. Insert a screwdriver into the eyelet at the bottom of the slide interlock and move the latch downward.
2. Swing the xLogic Basic off the DIN rail.

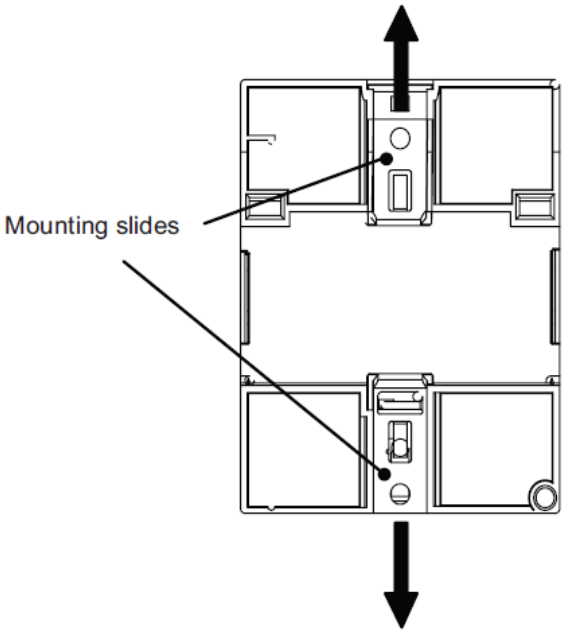


..... if you have connected at least one expansion module to xLogic Basic:

1.Remove the connection bridge
2. Slide the expansion module off towards the right.
3. Insert a screwdriver into the eyelet at the bottom of the slide interlock and lever it downward.
4. Swing the expansion module off the profile rail.
   Repeat steps 1 to 4 for all other expansion modules.

2.1.2 Wall-mounting

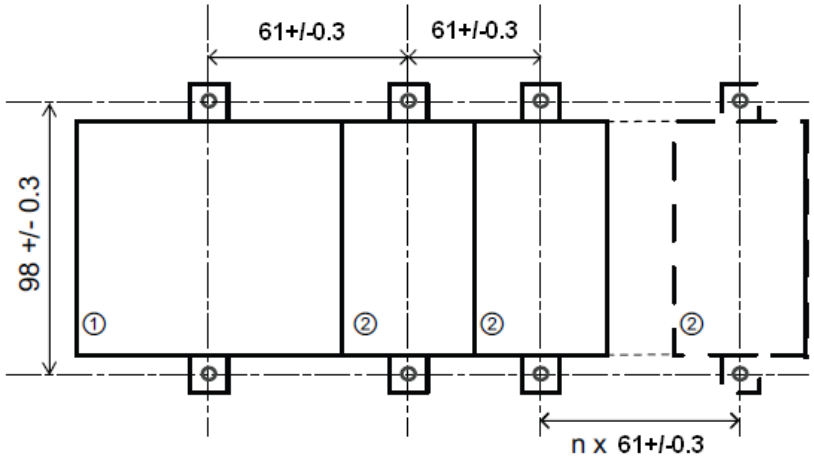
For wall-mounting, first slide the mounting slides on the rear side of the devices towards the outside. You can now wall-mount xLogic by means of two mounting slides and two Ø M4 screws (tightening torque 0.8 to 1.2 Nm).



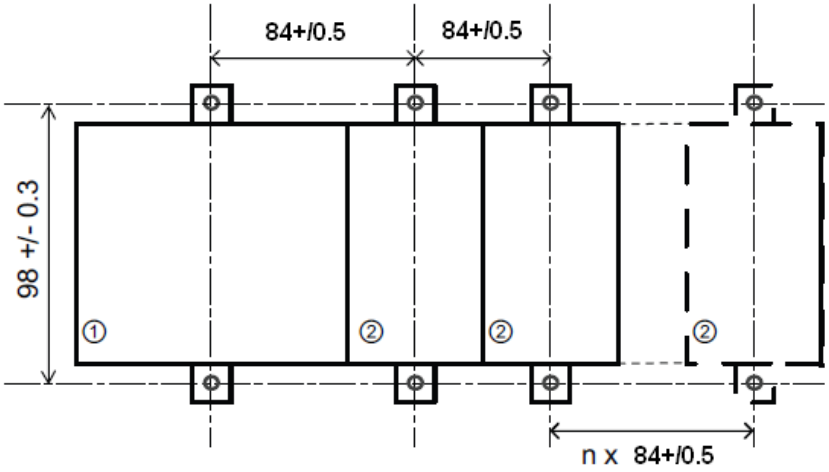
Drilling template for wall-mounting

Before you can wall-mount xLogic, you need to drill holes using the template shown below.

ELC-12 series:



ELC-18 series:



All dimensions in mm

Bore hole for Ø M4 screw, tightening torque 0.8 to 1.2 Nm

1. xLogic CPU

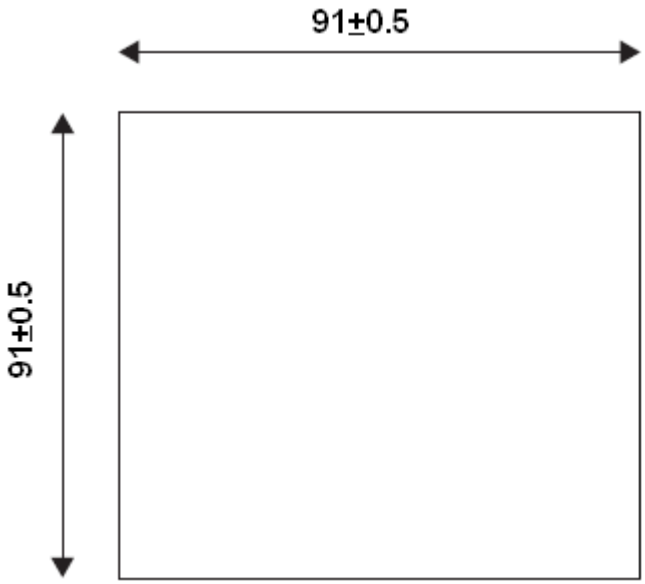
2. xLogic extensions

2.1.3 Mouting ELC-HMI-FP

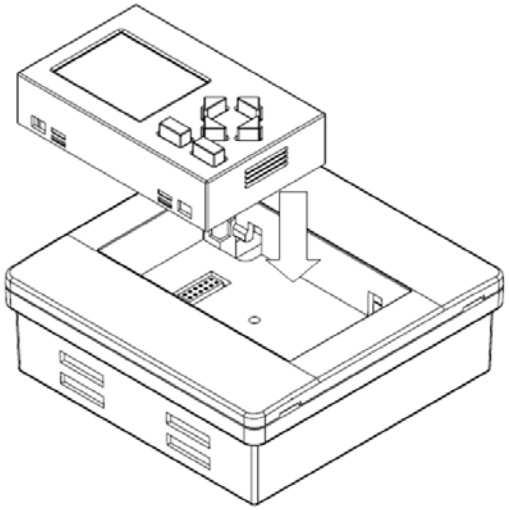
ELC-HMI-FP ， Faceplate (ELC-HMI's installation unit), making it possible for ELC-HMI to be externally installed in the front door of cabinet for easy observation and operation while ELC-12 CPU is required to be installed inside.

To prepare the mounting surface for the optional **ELC-HMI-FP** TD and mount it, follow these steps:

1. Cut a 91 mm x 91 mm (tolerance: +0.5 mm) hole in the mounting surface.

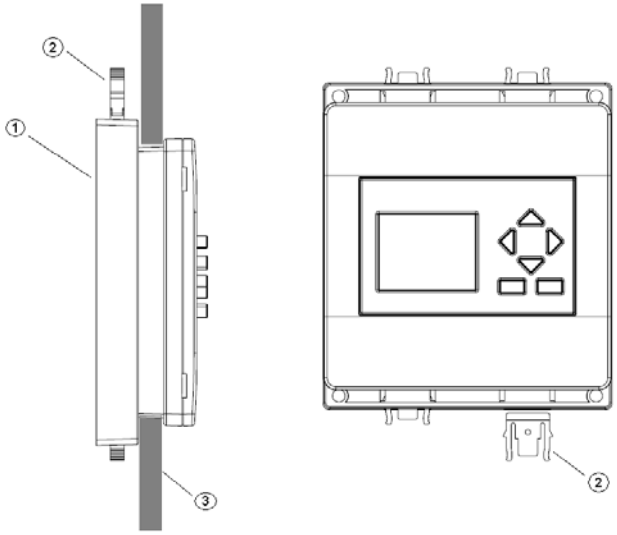


2. Put the ELC-HMI into ELC-HMI-FP module.



3. Fit the **ELC-HMI-FP** (as the above figure ,not include the lock part) into the cutout you made in the mounting surface.

4. Attach the mounting brackets (included) to the **ELC-HMI-FP** .



1. Mounting brackets

2. Mounting lock
3. Cabinet door or control panel (Thickness:1.5 to 8.5 mm)

5. You can then use the **ELC-COVER-CABLE** to connect the **ELC-HMI-FP** to the ELC-12 Basic module up to a distance of 1.5 meters. This distance can be extended to up to ten meters by using a standard Sub-D cable together with the **ELC-COVER-CABLE** .

2.2 Wiring the xLogic

Wire the xLogic using a screwdriver with a 3-mm blade.

You do not need wire ferrules for the terminals. You can use conductors with cross-sections of up to the following thicknesses:

- 1 x 2.5 mm^2
- 2 x 1.5 mm^2 for each second terminal chamber
- Tightening torque: 0.4.. .0.5 N/m or 3. ..4 lbs/in

Note

Always cover the terminals after you have completed the installation. To protect xLogic adequately from impermissible contact to live parts, local standards must be complied with.

2.2.1 Connecting the power supply

The ELC-18AC and ELC-12AC versions of xLogic are suitable for operation with rated voltages of 110 V AC and 240 V AC. The ELC-18DC and ELC-12DC versions can be operated with a 12 or 24 VDC power supply.

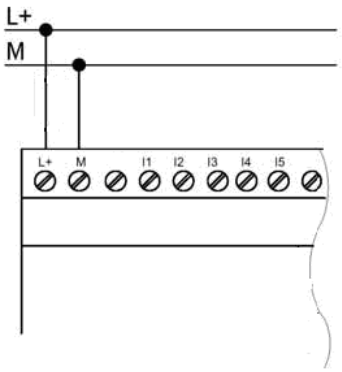
Note

A power failure may cause an additional edge triggering signal.

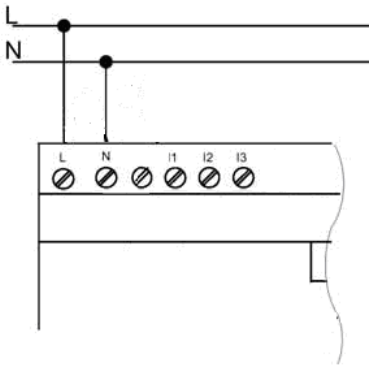
Data of the last uninterrupted cycle are stored in xLogic.

To connect xLogic to the power supply:

With DC power supply



With AC power supply



2.2.2. Connecting xLogic inputs

1. Requirements

At the inputs you connect sensor elements such as: momentary switches, switches, light barriers, daylight control switches etc.

	ELC-18AC ELC-12AC ELC-E-16AC	ELC-18DC ELC-12DC ELC-E-16DC
Signal status 0 Input current	<40VAC <0.24mA	<3VDC <1.5mA
Signal status 1 Input current	>85VAC Typical 0.24mA	>8VDC Typical 3mA
Analogue input	NO	AI1-AI8(0-10V DC)

Note:

1. For ELC-18DC-DA Series and ELC-12DC-DA Series versions. That can receive analog input. They can be set to analog input or digital input as either may be used in the program. They will be recognized as analog inputs when the input terminal is connected with an analog function block, and they will be recognized as switching inputs when the input terminal is not connected with an analog function block.

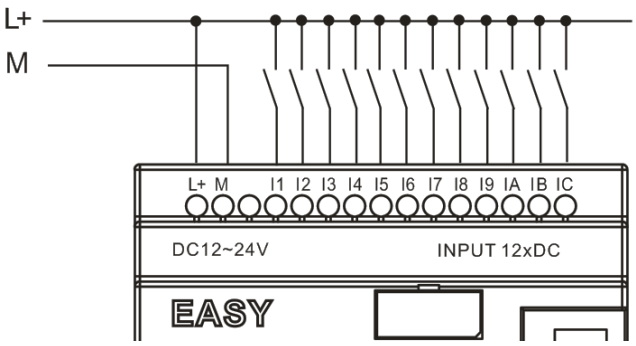
2. The analog inputs require DC 0V ~ +10V voltage signals. These are divided equally in 0.02V increments. In programming, all the block parameters related to the analog inputs are based on the minimum increment of 0.02V.

3. They can be recognized as switching input when the input voltage is more than 10.0V and cannot be recognized as an analog input.

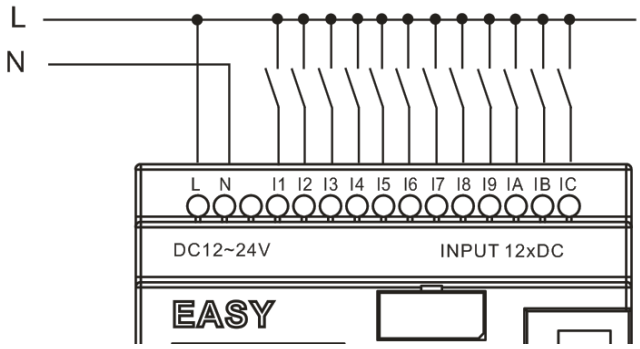
4. For the switching input off, when the switch status changes from 0 to 1, the time of Status 1 must be greater than 50ms, and when the switch status changes from 1 to 0, the time of Status 0 also must be greater than 50ms.

2.Connecting xLogic is shown as in the following figures:

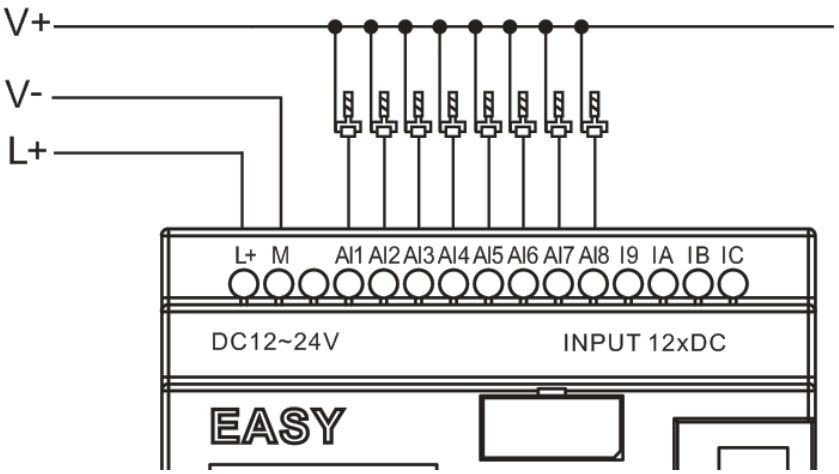
*** ELC-18DC and ELC-12DC Series inputs**



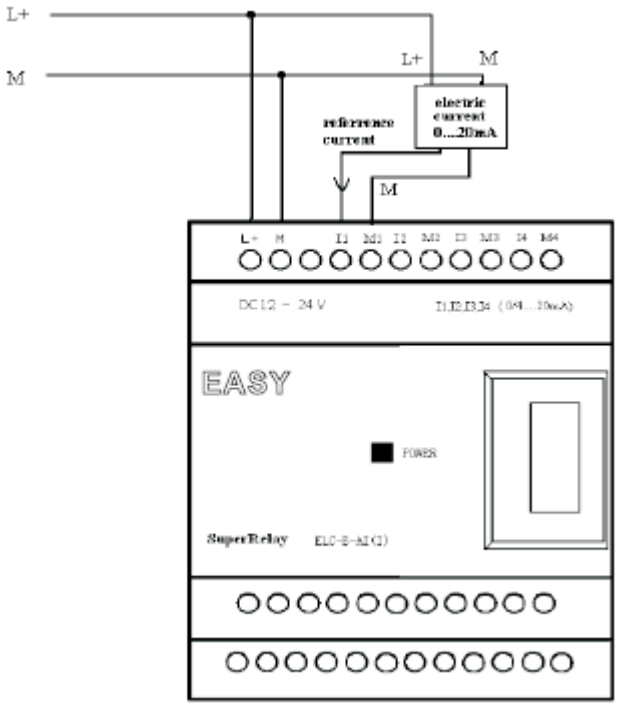
*** ELC-18AC Series and ELC-12AC inputs**



*** ELC-Analog Inputs(DC 0…10V)**



ELC-E-AI(I) current Inputs



The above figure shows how to make a four-wire current measurement.
Connect two-wire sensor to ELC-E-AI(I).
Two-wire sensor wiring is as follows:

1. Connect the output of the sensor to the "I" terminal (0…20mA current measurement) of ELC-E-AI(I) module.
2. Connect the attached connector of the sensor to the +24V(L+) of power supply.
3. Connect the current output terminal M to the corresponding M terminals(M1,M2,M3) of ELC-E-AI(I).

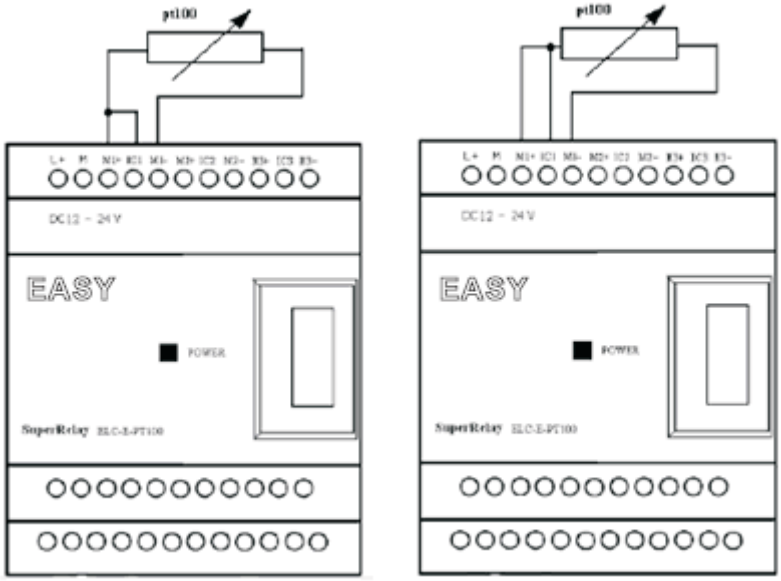
ELC-E-PT100

It can be connected with one two-wire or three-wire resistance-type thermocouple.
When two-wire technology applied, the terminals "M1+ and IC1" (this rule also shall be applied to" M2+ and IC2", "M3+ and IC3") would be short connected . Such connection can not compensate error/tolerance caused by the resistance in measurement loop. The measurement error of 1 Ω impedance of power cord is proportional to +2.5 °C

The three-wire technology can inhibit the influence of measurement results caused by cable length (ohmic resistance).

Two-wire technology

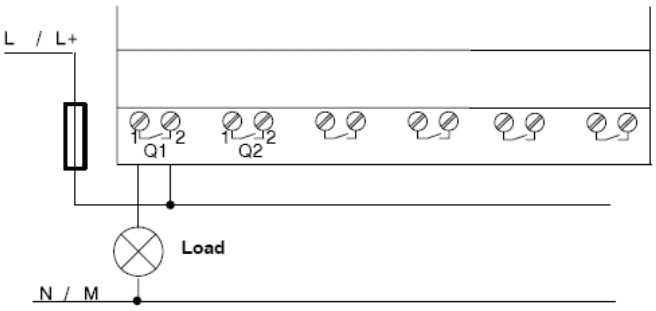
three-wire technology



2.2.3 Connecting xLogic Outputs

1. Requirement for the relay output

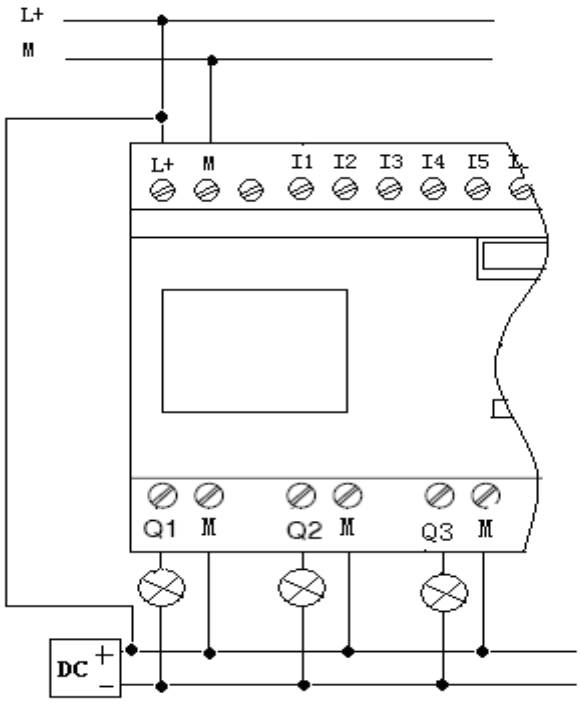
Various loads such as lamp, fluorescent tube, motor, contact, etc., can be connected to the outputs of xLogic. The maximum ON output current that can be supplied by xLogic is 10A for the resistance load and 2A for the inductive load. The connection is in accordance with the following figure:



Relay Output

2. Requirement for the electronic transistor output:

The load connected to xLogic must have the following characteristics:
* The maximum switch current cannot exceed 2A.
* When the switch is ON (Q=1), the maximum current is 2A.

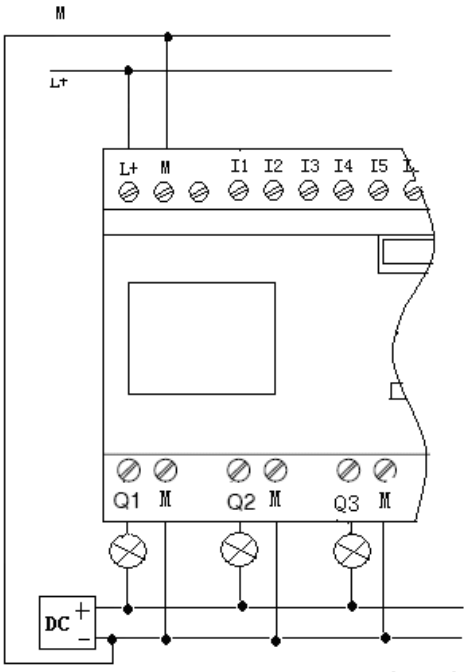


Transistor Output (PNP)

Notes (PNP):

* The load connecting voltage must be ≤80VDC and it must be DC.

* The "+" terminal of the output wiring must be connected with the DC positive voltage, and it must be connected with the "L+" terminal of the xLogic power ， a load must be connected with the "-" terminal of the DC negative voltage.



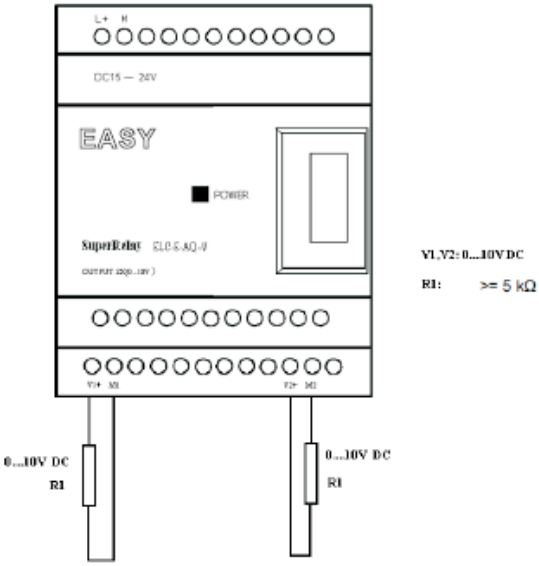
Transistor Output (NPN)

Notes (NPN):

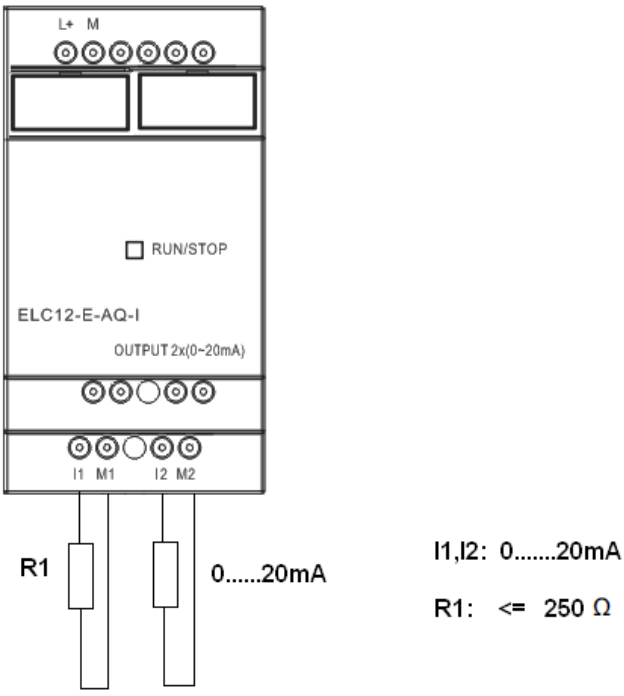
* The load connecting voltage must be ≤80VDC and it must be DC.

* The "-" terminal of the output wiring must be connected with the DC negative voltage, and it must be connected with the "M" terminal of the xLogic power ， a load must be connected with the "+" terminal of the DC positive voltage.

ELC-E-AQ-V

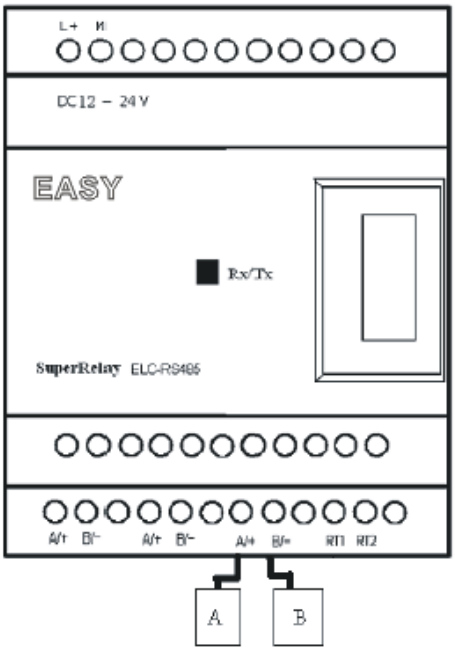


ELC12-E-AQ-I



ELC-RS485

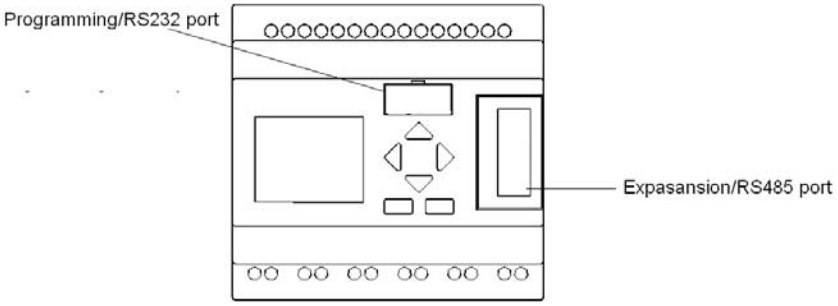
Actually, ELC-RS485 is just a convertor with photo isolation bringing out 3 wiring terminals from RS485 port (2x8pin) of CPU for your easy connection with other devices,



If "RT1", RT2" terminal are short connected, one 120R resistor will be connected between A/+ and B/-

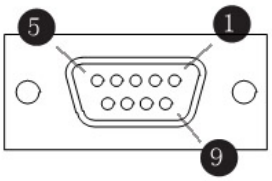
2.2.4 Communication port instructions:

ELC-18 CPUs



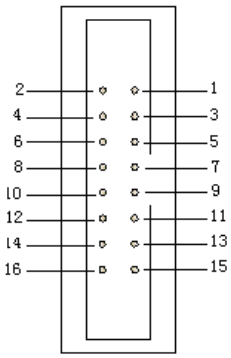
1. Programming port/RS232 port(ELC-RS232 ,ELC-USB,ELC-Copier should be inserted in this port)

When the programming port should be used as the standard RS232 port (D-shape 9 pin header) ,the ELC-RS232 cable needed.Blow is show you the pin definition of the header:



PIN	function
2	RXD
3	TXD
5	GND
others	NULL

2. Expansion port/RS485 (pin definition)

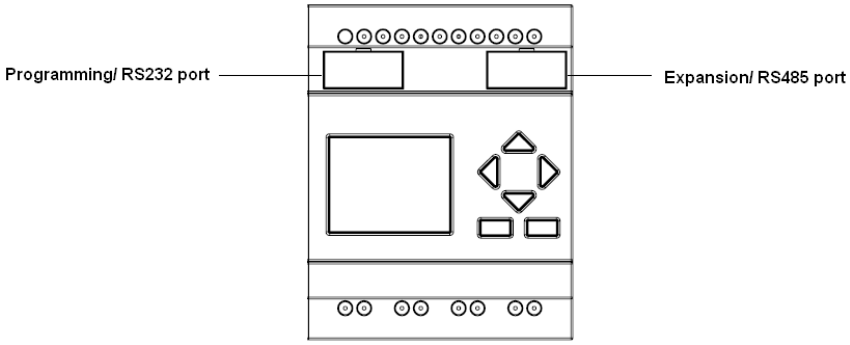


3------RS485 A
5------RS485 B
4------GND
6------GND
7------CANL
9------CANH
15------+5V
16------+5V

Communication between CPU and expansion module will use 4.7,9,15 pin.

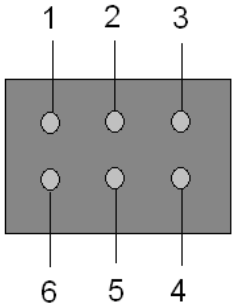
ELC-RS485 module need when ELC-18 CPU communicate with the third party devices via RS485 bus

ELC-12 CPUs



1. Programming port/RS232 port(ELC-RS232 ,ELC-USB,ELC-Copier should be inserted in this port) Named COM1.
   When the programming port should be used as the standard RS232 port (D-shape 9 pin header) ,the ELC-RS232 cable needed.

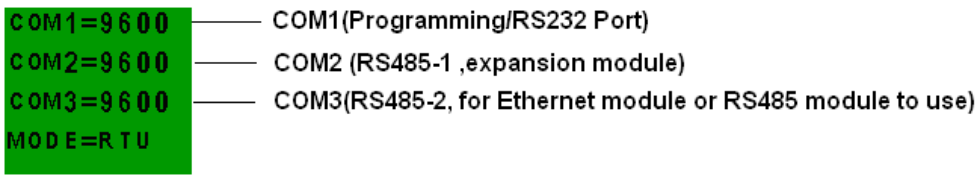
2. Expansion port/RS485 (pin definition(2X3 pin female figure)



1------RS485 A1
6------RS485 B1
2------RS485 A2
3------RS485 B2
4------GND
5------Battery

Communication between CPU and expansion module will use 1,6 pin. This named COM2. ELC12-E-RS485 module need when ELC-12 CPU communicate with the third party devices via RS485 bus and the pin 2,3 should be used by ELC12-E-RS485 module .This named COM3.

Note :1.The baud rates of COM1,COM2,COM3 can be modified via the panel key.
2. The COM2 port also can work as the common RS485 port ,when there is no expansion module in the application . The standard RS485 module cannot bring out the terminals of COM2 built-in ELC-12 series CPU, so one customize RS485 module should be used for this case.



Chapter 3 Programming xLogic

Note:
Only the new ELC-12 series&Upgraded ELC-18 CPU supports the programming via HMI panel key .

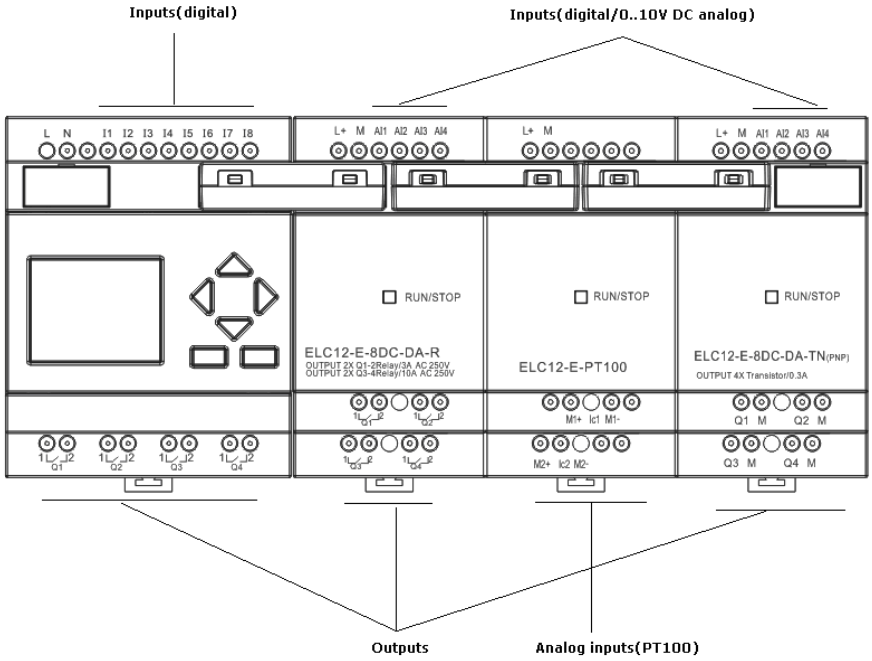
Getting started with xLogic
Programming refers to creating a circuit program from the xLogic Basic module.
In this chapter you will learn how to use xLogic to create the xLogic circuit programs for your application.
xLogicSoft is the xLogic programming software that you can use on your PC to
quickly and easily create, test, modify, save and print the circuit programs. The topics in this manual, however, relate only to the creation of circuit programs on the actual xLogic Basic module. The programming software xLogicSoft contains extensive online help.

A small example in the first part of this chapter introduces the operating principles of xLogic:
- You will learn the meaning of two basic terms, namely the connector and the block.
- As the next step, you will create a circuit program based on a simple conventional circuit.
- Lastly, you will enter this program directly in xLogic.

It will take you only a few pages of this manual to store your first executable circuit program in the xLogic unit.
With suitable hardware (switches etc.), you will then be able to carry out initial tests.

3.1 Connectors

xLogic is equipped with inputs and outputs
Example of a configuration with several modules:



Each input is identified by the letter I plus a number. When you look at xLogic from the front,you can see the input terminals at the top. Only analog modules(PT100 and 0…20mA input) have the inputs at the bottom.
Each output is identified by the letter Q plus a number (ELC-E- AQ: AQ plus number). In the figure, you can see the output terminals at the bottom.

Note
xLogic can recognize, read and switch the I/O of all expansion modules regardless of their type. The I/Os are not presented in the installation order of the modules, it rests with the address of the expansion modules.For example the first input of the expansion module with the

address 2 , the symbol will be I21 . The second output of the expansion module with the address 4 ,the symbol will be Q42. Refer to chapter 5.2.4，for how to set the expansion module address.
The following I/Os and flag blocks are available for creating your circuit program: I1 to I8(CPU),I11 to I14(EXT1), I21 to I24(EXT2),I31 to I34(EXT3).....I81 to I84 (EXT8).
AI1 to AI4(CPU), AI11 to AI14(EXT1), AI21 to AI24(EXT2),AI31 to AI34(EXT3).....AI81 to AI84 (EXT8). Q1 to Q4(CPU),Q11 to Q14(EXT1), Q21 to Q24(EXT2),Q31 to Q34(EXT3).....Q81 to Q84 (EXT8). AQ1 to AQ2(CPU), AQ11 to AQ12(EXT1), AQ21 to AQ22(EXT2),AQ31 to AQ32(EXT3).....AQ81 to AQ82 (EXT8). F1 to F64, and AF1 to AF64. Also available are the shift register bits S1 to S8, 4 cursor keys: C▲ , C▶ , C▼ and C◀ . See the "Constants and connectors" topic for more details.
The following applies to inputs AI1,AI2,AI3 and AI4 of ELC-12DC-DA-R(TN/TP) and versions: If you use I1, I2, I3 or I4 in the circuit program, this input signal is digital. If you use AI1, AI2, AI3, AI4 the input signal is analog. The expansion modules also adapt to this rules.

The illustration above with numbered AI inputs shows the conceptual usage of the inputs, not the actual physical markings on the module.

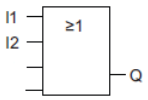
xLogic's connectors

The term connector refers to all connections and states in xLogic .
The digital I/O status can be '0' or '1'. Status '0' means that the input does not carry a specific voltage. Status '1' means that the input does carry a specific voltage.
The 'hi', 'lo' connectors have been introduced to make it easier for you to create the circuit program:
'hi' (high) is assigned the status '1',
'lo' (low) is assigned the status '0'.
You do not have to use all of the connectors of a block. The circuit program automatically assigns the unused connectors a status that ensures proper functioning of the relevant block.
For information on the meaning of the term "block", refer to the topic on "Blocks and block numbers ".

3.2 Blocks and block numbers

This chapter shows you how to use xLogic elements to create complex circuits and how blocks and I/O are interconnected.
In the topic "From circuit diagram to xLogic Program" you will learn how to transform a conventional circuit into a xLogic circuit program.
Blocks
A block in xLogic represents a function that is used to convert input information into output information. Previously you had to wire the individual elements in a control cabinet or terminal box.
When you create the circuit program, you interconnect the blocks. To do so, simply select the connection you require from the **Co** menu. The menu name **Co** is an abbreviation of the term "Connector".
Logic operations
The most elementary blocks are the logic operations:

- AND
- OR
- …



Inputs I1 and I2 are here connected to the OR block. The last two inputs of the block remain unused .

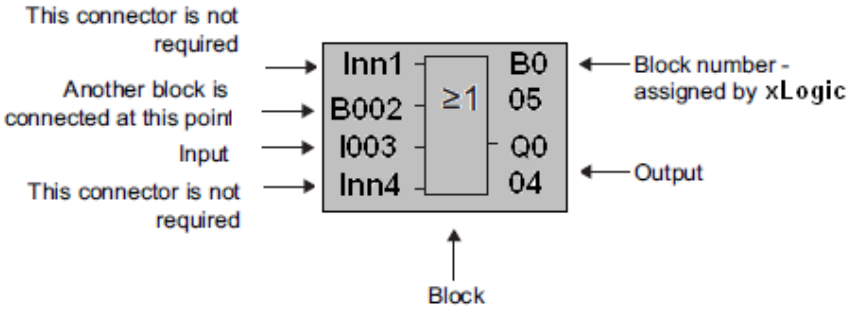
These special functions offer you significantly greater performance:
- Pulse relay
- Up/down counter
- On-delay

- Softkey
- ....

The chapter entitled " xLogic functions " gives a full list of the xLogic functions.

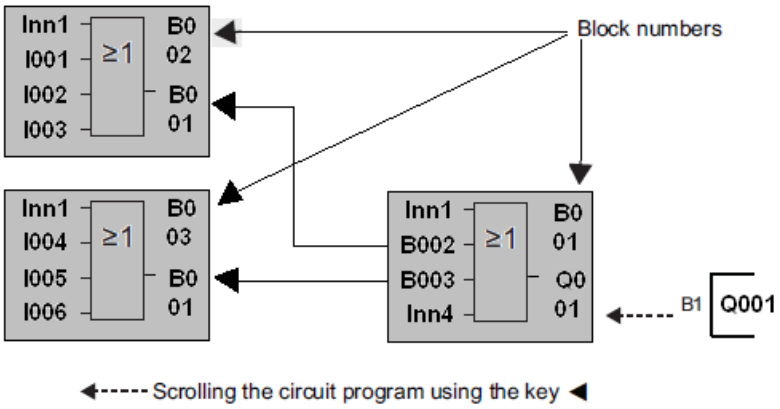
Block representation on the xLogic Display

The figure below shows a typical view of the xLogic Display. As you can see, it can show only one block at a time. We have therefore introduced block numbers to help you check the circuit structure.



Assigning a block number

xLogic assigns each new block in a circuit program a block number.
xLogic uses these block numbers to indicate the block interconnections. This means that these numbers are mainly an aid to your orientation in the circuit program.



The figure above shows you three views of the xLogic Display, which represent the circuit program. As you can see, xLogic interconnects the blocks using their numbers.

Advantages of block numbers

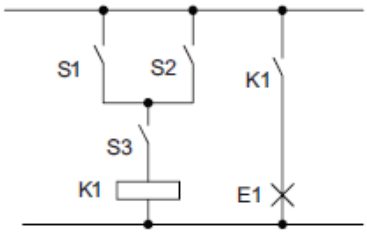
You can connect almost any block to an input of the current block by means of its block number. In this way, you can reuse the interim results of logical or other operations, reduce programming effort, save memory space and clean up your circuit layout. To do so, however, you need to know how xLogic has named the blocks.

Note
We advise you to create an organizational program chart. You will find this a valuable aid when you create the circuit program because you can enter in this chart all the block numbers that xLogic assigns.
By using the xLogicSoft software to program xLogic , you can directly create a function chart of your circuit program. xLogic Soft also allows you to assign eight character names to up to 512 blocks, and to view these on the xLogic Display in parameter assignment mode.

3.3 From circuit diagram to xLogic program

View of a circuit diagram

You know, of course, how a circuit logic is represented in a circuit diagram. Nevertheless,here is an example:

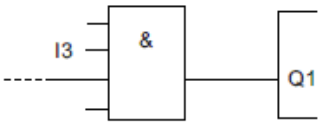


Load E1 is switched on and off by means of the switches (S1 OR S2) AND S3.

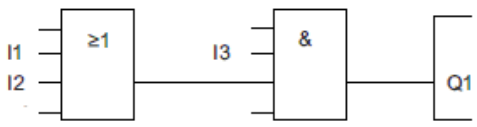
Relay K1 picks up when condition (S1 OR S2) AND S3 is met.

Creating this circuit with xLogic
In xLogic you create a circuit logic by interconnecting blocks and connectors:

To create a circuit logic in xLogic , start at the circuit output.
The output is the load or relay that is to be switched.
Convert the circuit logic into blocks by working through the circuit, starting at the output and ending at the input:
Step 1: The make contact S3 is interconnected in series to output Q1 and to a further circuit element. A series connection corresponds with the AND block:



Step 2: S1 and S2 are connected in parallel. A parallel circuit corresponds with the OR block:



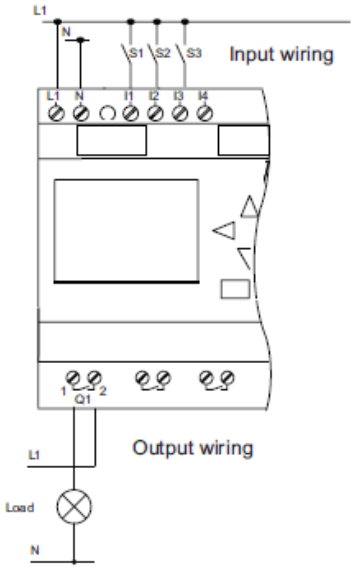
Unused inputs

Keep NULL connection for the unused connectors.
In our example we shall use only two inputs of the OR block and two inputs of the AND block; the relevant unused first and fourth inputs have no connection.

Wiring

Connect the switches S1 to S3 to the screw terminals of your xLogic :
• S1 to connector I1 of xLogic
• S2 to connector I2 of xLogic
• S3 to connector I3 of xLogic
The output of the AND block controls the relay at output Q1. The load E1 is connected to output Q1.

Wiring example
The following figure shows you the wiring, based on a 220 V AC version of xlogic.



3.4 The four golden rules for operating xLogic

Rule 1: Changing the operating mode
• You create the circuit program in programming mode. After power is on, and when the display shows "No Program ", press the ESC key to select programming mode.
• Timer and parameter values of an existing circuit program can be edited both in parameter assignment mode and in programming mode. During parameter assignment xLogic is in RUN mode; that is, it continues executing the circuit program (see the topic "Configuring xLogic"). To work in programming mode, you need to terminate the circuit program by calling the "Stop" command.
• Select the 'Start' command on the main menu to set RUN mode.
• When the system is in RUN, you can return to parameter assignment mode by pressing the ESC key.
• When parameter assignment mode is open and you want to return to programming mode, select the "Stop" command from the parameter assignment menu, and confirm "Stop Prg" prompt with "Yes". To do so, move the cursor to "Yes" and confirm with OK.

Rule 2: Outputs and inputs

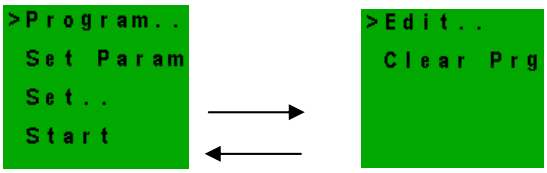
• Always create your circuit program by working from the output to the input.
• You can connect an output to several inputs, but not the same input to several outputs.
• Within the same program path you may not connect an output to an upstream input. For such internal recursions you should interconnect flags or outputs.

Rule 3: Cursor and cursor movement
The following applies when you edit a circuit program:
• You can move the cursor when it appears in the form of an underscore:
– Press , , or to move the cursor in the circuit program.
– Press OK to change to "Select connector/block".
– Press ESC to exit programming mode.
• You select a connector/block - when the cursor appears as a solid square:
– Press or to select a connector or a block.
– Confirm with OK.
– Press ESC to return to the previous step.

Rule 4: Planning
• Before you start to create a circuit program, you should either first create a design on paper or program xLogic directly using xLogicSoft.
• xLogic can only save complete and faultless circuit programs.

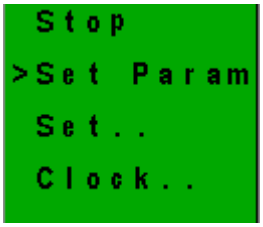
3.5 Overview of xLogic menus

Programming mode



Parameter assignment mode

Parameter assignment menu:

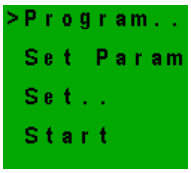


3.6 Writing and starting the circuit program
After you have designed a circuit, you want to write it to your xLogic. The small example below shows how to do this.

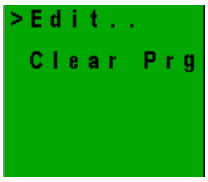
3.6.1 Selecting programming mode

You have connected xLogic to the power supply and switched it on. The display now shows you the message:

Switch xLogic to programming mode by pressing ESC. This will take you to the main menu of xLogic:

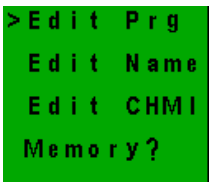


The first character in the first line is the ">" cursor. Press ▲ and ▼ to move the ">" cursor up and down. Move it to "**Program..**" and confirm with OK. xLogic opens the programming menu.



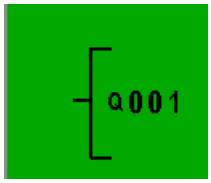
xLogic's programming menu

Here you can also move the ">" cursor by pressing ▲ and ▼. Move the ">" cursor to "**Edit..**" and confirm with OK.



The Edit menu of xLogic

Move the ">" cursor to "Edit Prg" (for editing the circuit program) and confirm with OK. xLogic now shows you the first output:



The first output of xLogic

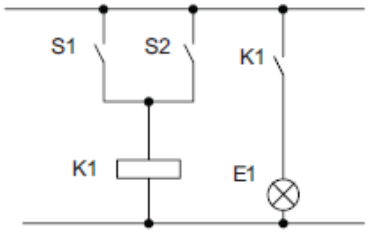
You are now in programming mode. Press ▲ and ▼ to select the other outputs. Now start to edit your circuit program.

Note
Because we have not yet saved a password for the circuit program in xLogic, you can directly enter editing mode. When you select "Edit" after you have saved a passwordprotected circuit prgram, you are prompted to enter a password and to confirm it with OK.
You can only edit the program after you have entered the correct password .

3.6.2 The first circuit program

Let us now take a look at the following parallel circuit consisting of two switches.
Circuit diagram

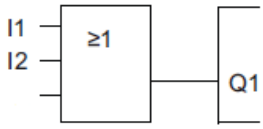
The corresponding circuit diagram



The load is switched on with S1 OR S2. **xLogic** interprets this parallel circuit as an 'OR' logic, because S1 OR S2 switches on the output.

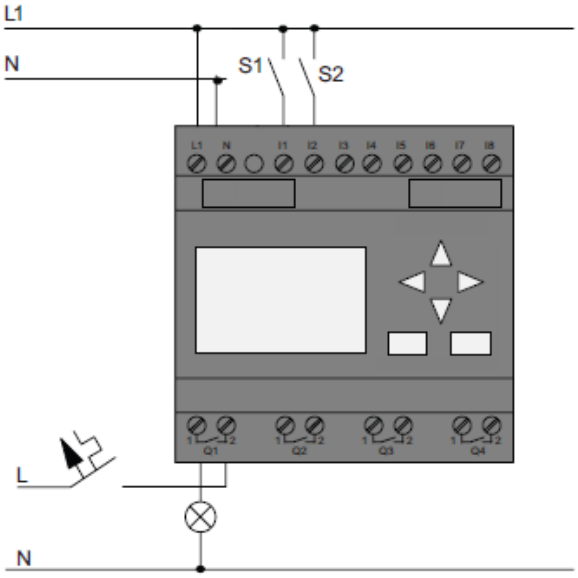
Translated into a xLogic circuit program this means: Relay K1 is (at output Q1) is controlled by means of an OR block.

Circuit program
S1 is connected to the I1 and S2 to the I2 input connector of the OR block.
The corresponding layout of the circuit program in xLogic:



Wiring

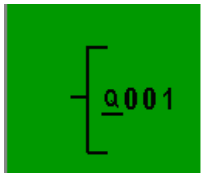
The corresponding wiring:



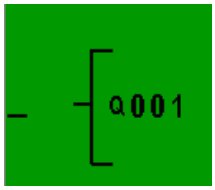
S1 switches input I1, while S2 switches input I2. The load is connected to the relay Q1.

3.6.3 Circuit program input

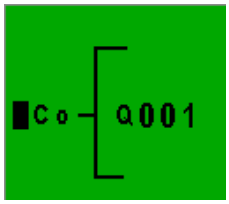
Let us now write the circuit program, starting at the output and working towards the input. xLogic initially shows the output:



You will see an underscore below the Q in Q001, which is the cursor. The cursor indicates your current position in the circuit program. You can move the cursor by pressing the ▲ , ▼ , ◀ and ▶ keys. Now press the ◀ key. The cursor moves to the left.

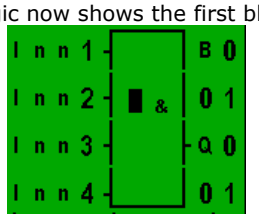


At this point you enter only the first (OR) block. Press OK to select editing mode.



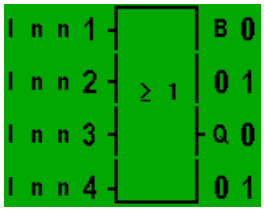
The cursor no longer appears in the form of an underscore; but instead as a flashing solid square. xLogic offers you various options here.

Select GF (basic functions) by pressing the key until GF appears, and confirm with OK.
xLogic now shows the first block from the list of basic functions:



The AND is the first block of the basic function list.
The soild square cursor prompts you to select a block.

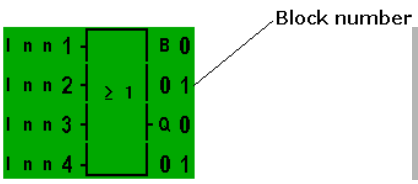
Now press ▼ or ▲ until the OR block appears on the display:

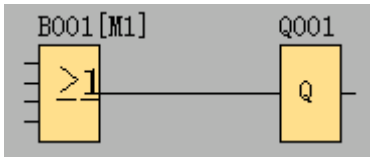


Press OK to confirm your entries and exit the dialog.

The display now shows:

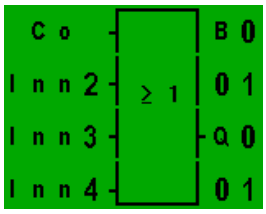
Your complete circuit program layout:





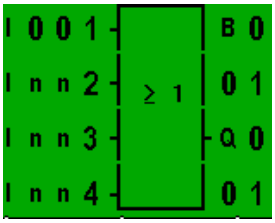
You have now entered the first block. Each new block is automatically assigned a block number. The only thing left to do is interconnect the block inputs. This is how it is done:
Press OK.

The display now shows:



Select the Co list: Press OK

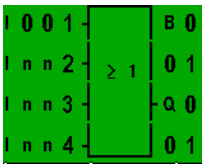
The display now shows:



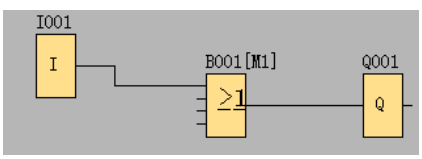
The first element of the Co list is the "Input 1" character, namely "I001".

Press OK. I1 is now connected to the input of the OR block. The cursor jumps to the next input of the OR block.

The display now shows:



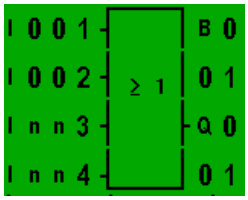
Your complete circuit program in xLogic up to now:



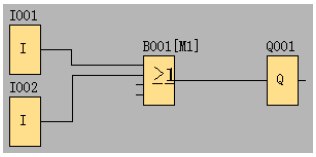
Now you connect input I2 to the input of the OR block:
1. Switch to editing mode: Press OK
2. Select the Co list: Press or

3. Confirm the Co list with: Press OK
4. Select I2: Press or
5. Apply I2: Press OK
I2 is now connected to the input of the OR block

The display now shows:



Your complete circuit program in xLogic up to now:



We do not need the last two inputs of the OR block for this circuit program.

We shall now exit circuit programming mode. To return to the programming menu: Press ESC

Note

xLogic has now saved your circuit program to nonvolatile memory. The circuit program remains in the xLogic memory until you explicitly delete it.
You can save the actual values of special functions in the case of a power outage assuming that these functions support the "Retentive" parameter and that the necessary program memory is available. The Retentive parameter is deactivated when you insert a function; to use it, you must enable this option.

3.6.4 Assigning a circuit program name

You can assign your circuit program a name that consists of up to 16 uppercase/lowercase letters, numbers and special characters.
In the programming menu:

1. Move the ">" cursor to '**Edit..**': Press ▼ or ▲
2. Accept '**Edit**': Press OK

3. Move the ">" cursor to '**Edit Name**': Press ▼ or ▲ .
4. Accept '**Edit Name**': Press OK

Press ▲ and ▼ to list the alphabet, numbers and special characters, either in ascending or descending order. You can select any letter, number or character.
To enter a space character, simply move the cursor with ▶ to the next position. This character is the first one in the list.
Examples:
Press once to select an " A "
four times to select " } ", etc.
The following character set is available:

	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O
P	Q	R	S	T	U	V	W	X	Y	Z	a	b	c	d	e
f	g	h	i	j	k	l	m	n	o	p	q	r	s	t	u
v	w	x	y	z	0	1	2	3	4	5	6	7	8	9	!
"	#	$	%	&	'	(	)	*	+	,	-	.	/	:	;
<	=	>	?	@	[	\	]	^	_	'	{	|	}	~	

Let us assume you want to name your circuit program "ABC":
1. Select " A": Press ▼
2. Move to the next letter: Press ▶
3. Select " B": Press ▼
4. Move to the next letter: Press ▶
5. Select " C": Press ▼
6. Confirm the complete name: Press OK

Your circuit program is now named "ABC", and you are returned to the programming menu.
To change the name of your circuit program, proceed in the same way.

3.6.5 Assigning system cover

Default display:

The message in first and the second line can be modified/edit in the proramming mode(also can be modified in xLogicsoft) , each line contains 10 characters.

In the programming menu:

1. Move the ">" cursor to '**Edit..**': Press ▼ or ▲
2. Accept '**Edit**': Press OK
3. Move the ">" cursor to '**Edit CHMI**': Press ▼ or ▲ .
4. Accept '**Edit CHMI**': Press OK

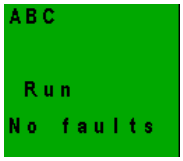
Press ▲ and ▼ to list the alphabet, numbers and special characters, either in ascending or descending order. You can select any letter, number or character.
To enter a space character, simply move the cursor with ▶ to the next position. This character is the first one in the list.

	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O
P	Q	R	S	T	U	V	W	X	Y	Z	a	b	c	d	e
f	g	h	i	j	k	l	m	n	o	p	q	r	s	t	u
v	w	x	y	z	0	1	2	3	4	5	6	7	8	9	!
"	#	$	%	&	'	(	)	*	+	,	-	.	/	:	;
<	=	>	?	@	[	\	]	^	_	'	{	|	}	~	

Let us assume you want to edit the system cover "ABC":

1. Select " A": Press ▼
2. Move to the next letter: Press ▶
3. Select " B": Press ▼
4. Move to the next letter: Press ▶
5. Select " C": Press ▼
6. Confirm: Press OK

Press "ESC"…



3.6.6 Second circuit program

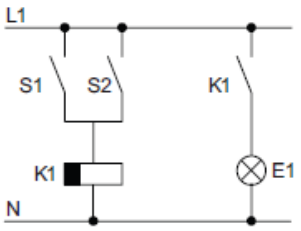
Up to this point, you have successfully created your first circuit and assigned it a name and, if desired, a password. In this section we will show you how to modify existing circuit programs and how to use the special functions.

Using this second circuit program, we will show you how to:

- Add a block to an existing circuit program.
- Select a block for a special function.
- Assign parameters.

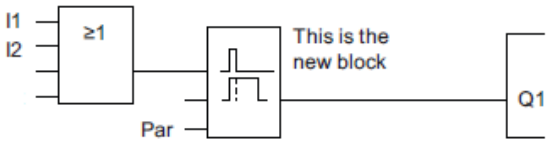
Modifying circuits

We shall use the first circuit program as a basis for the second, with some slight modifications.
First of all take a look at the circuit diagram for the second circuit program:



You already know the first part of the circuit. S1 and S2 switch a relay, which is to be used to switch on the load E1, and to switch off the load with a delay of 12 minutes.

This is the circuit program layout in xLogic:



You can see the OR block and the output relay Q1 we have already used in the first circuit program. The only difference is the new off-delay block.

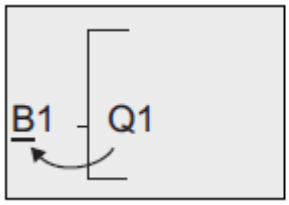
Editing the circuit program

Switch xLogic to programming mode.

As a reminder:
1. Switch xLogic to programming mode
(in RUN: Press ESC to enter the parameter assignment mode. Select the 'Stop' command, confirm with OK, then move the '>' cursor to 'Yes', and once again confirm with OK). For additional details, see the topic "The four golden rules for operating xLogic ˝.
2. On the main menu, select "Program"
3. On the Programming menu, select "Edit", confirm with OK. Next, select "Edit Prg" and confirm with OK.If required, enter your password at the prompt and confirm with OK. You can now modify the current circuit program.

Adding a block to a circuit program

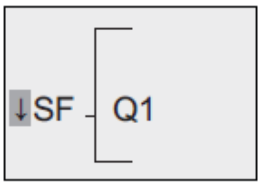
Move the cursor to the B in B1 (B1 is the number of the OR block):



To move the cursor:
Press ◀

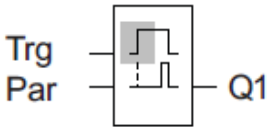
We now insert the new block at this position.
Confirm with OK.

Press ▼ to select the SF list:



The SF list contains the special function blocks.

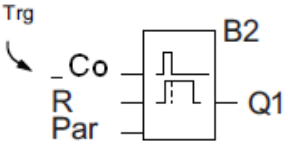
Press OK.
The block of the first special function is shown:



When you select a special or basic function block, **xLogic** shows you the relevant function block. The solid square cursor is positioned on the block.
Press ▼ or ▲ to select the required block.

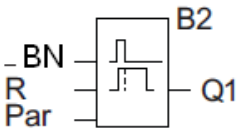
Select your block (off-delay, see the next figure), and then press OK:

Before you press 'OK', this shows the entry Trg

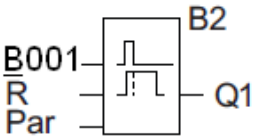


The added block is assigned the block number B2. The cursor is positioned at the top input of the added block.

Press ▼ or ▲ to select BN option .

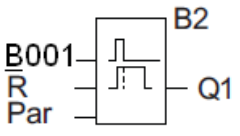


Press "OK".



The B1 block previously connected to Q1 is automatically connected to the uppermost input of the new block. Note that you can only interconnect digital inputs with digital outputs or analog inputs with analog outputs. The 'old' block will otherwise be lost.
The off-delay block has three inputs. At the top is the trigger input (Trg) you use to start the off-delay time. In our example, the OR block B1 triggers the off-delay. You reset the time and the output with a signal at the reset input. Set the off-delay time at parameter T of the input Par.

In our example we do not use the reset input of the off-delay function.



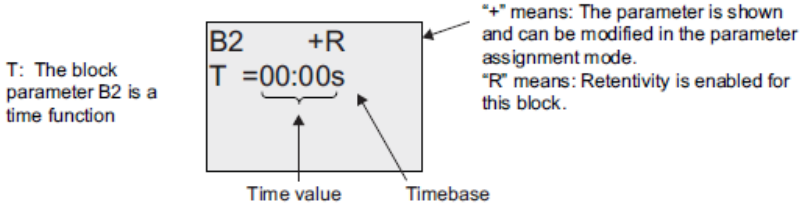
This is what the display should now show.

Assigning block parameters

Now you set the off-delay time T:

1. Move the cursor to Par, if it not already at this position: Press ▼ or ▲
2. Switch to editing mode: Press OK

xLogic shows the parameters in the parameter assignment window:



To change the time value:

- Press ◀ and ▶ to position the cursor.
- Press ▲ and ▼ to modify the value at the relevant position.
- Confirm your entries with OK.

Setting the time

Set the time T = 12:00 minutes:

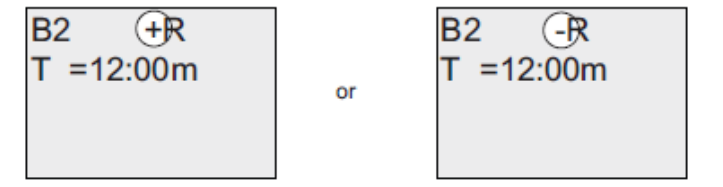
1. Move the cursor to the first digit: Press ◀ or ▶
2. Select the digit '1': Press ▲ or ▼
3. Shift the cursor to the second digit: Press ◀ or ▶
4. Select digit '2': Press ▲ or ▼
5. Move the cursor to the unit: Press ◀ or ▶
6. Select the timebase 'm' (for minutes): Press ▲ or ▼

Showing/hiding parameters - the parameter protection mode

If you want to show/hide the parameter and allow/prevent its modification in parameter assignment mode:

1. Move the cursor to the protection mode: Press ◀ or ▶
2. Select the protection mode: Press ▲ or ▼
   The display should now show:

The display should now show:



Protection mode +: The value of the time T can be modified in parameter assignment mode

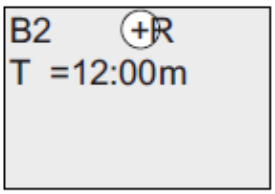
Protection mode - : The value of the time T is hidden in parameter assignment mode

3. Confirm your entries with: OK

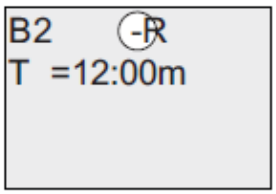
Enabling/disabling retentivity

To decide whether you want to retain your current data after a power failure or not:

1. Move the cursor to the retentivity setting: Press ◀ or ▶
2. Select the retentivity setting: Press ▲ or ▼
   The display now shows:



or



Protection mode +: The value of the time T can be modified in parameter assignment mode

Protection mode - : The value of the time T is hidden in parameter assignment mode

3. Confirm your entries with OK

Note
For further information on the protection mode, refer to the topic "Parameter protection ".
For further information on retentivity, refer to the topic "Retentivity ".
You can modify the protection mode and retentivity setting only in programming mode.
This is not possible in parameter assignment mode.
In this manual, the protection mode ("+" or "-") and retentivity ("R" or "/") settings are only shown in the displays where these can actually be changed.

Verification of the circuit program

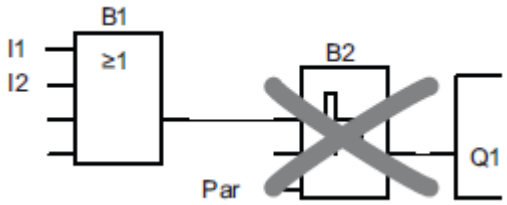
This program branch for Q1 is now completed. xLogic shows you the output Q1. You can once again view the circuit program on the display. Use the keys to browse the circuit program; that is, press or to move from block to block, and to move between the inputs at a block.

Closing the programming mode

Although you were shown how to exit the programming mode when you created your first circuit program, here is a reminder:
1. Return to the programming menu: Press ESC
2. Return to the main menu: Press ESC
3. Move the '>' cursor to 'Start': Press ▲ or ▼
4. Confirm 'Start': Press OK
   xLogic is back in RUN mode:

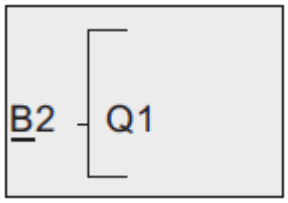
3.6.7 Deleting a block

Let us assume you want to delete the connection of block B2 from your circuit program and connect B1 directly to Q1.



Proceed as follows:

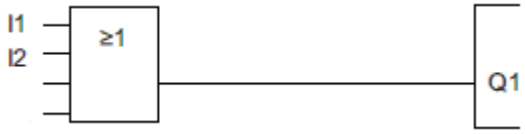
1. Switch xLogic to programming mode
(as a reminder, refer to the topic "The four golden rules for operating xLogic").
2. Select 'Edit': Press ▲ or ▼
3. Confirm 'Edit': Press OK
(If required, enter your password and confirm with OK.)
4. Select 'Edit Prg': Press ▲ or ▼
5. Confirm 'Edit Prg': Press OK
6. Move the cursor to B2, the input of Q1: Press ◀



7. Confirm with OK.
8. Now replace block B2 with block B1 at output Q1. The procedure:

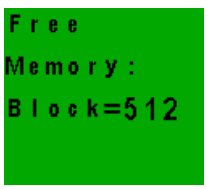
– Select the BNlist: Press ▲ or ▼
– Accept the BN list: Press OK
– Select 'B1': Press ▲ or ▼
– Apply 'B1': Press OK

Result: Block B2 is deleted, because it is no longer used in the circuit. Block B1 has replaced



3.7 Memory space and circuit program size

The size of a circuit program in xLogic is limited by the memory space (memory used by the blocks).
Up to 512 blocks can be used in xLogic.
Indication of available memory space
xLogic shows you the amount of free memory space.
Proceed as follows:
1. Switch xLogic to programming mode
(as a reminder, refer to the topic "The four golden rules for operating xLogic ".
2. Select 'Edit': Press ▲ or ▼
3. Accept 'Edit': Press OK
4. Select 'Memory?': Press ▲ or ▼
5. Accept 'Memory?': Press OK
The display now shows:



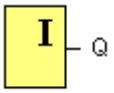
Chapter 4 xLogic functions

ELC series adopts the programming methods by the use of function blocks. A total of 8 general function blocks, 30 special function blocks, and 6 input & output function blocks are configured. And each block can achieve a specific control function independently, e.g. TOND, TOFD, SBPL, TBPL, SCHD, etc. As several blocks are linked up in a specific way, relatively complicated control functions can be performed. Programming with function blocks is simpler and better appreciated than the conventional PLC instruction programming.

The following types of operator for xLogic function blocks are available for options:

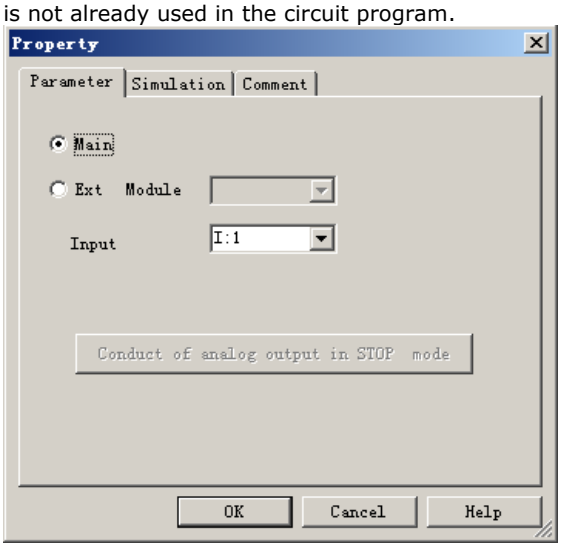
4.1 General Input & Output functions

4.1.1 Inputs

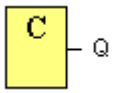


Input blocks represent the input terminals of xLogic. Up to 260 digital inputs are available to you.

In your block configuration, you can assign an input block a new input terminal, if this terminal is not already used in the circuit program.



4.1.2 Cursor keys



Up to four cursor keys are available to you. Cursor keys are programmed for the circuit program in the same ways as other inputs. Cursor keys can save switches and inputs, and allow operator control of the circuit program.

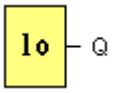
4.1.3 Outputs

Output blocks represent the output terminals of xLogic. You can use up to 254 outputs. In your block configuration, you can assign an output block a new terminal, provided this terminal is not already used in your circuit program.

The output always carries the signal of the previous program cycle. This value doe not change within the current program cycle.

4.1.4 Permanent logical levels HI and LO

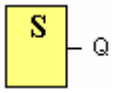
Set the block input to logical **hi** (hi = high) to set it permanently to logical '1' or 'H' state.



Set the block input to logical **lo** (lo = low) to set it permanently to logical '0' or 'L' state.

]

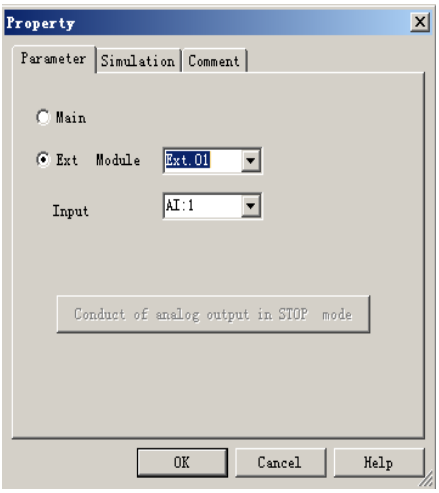
4.1.5 Shift register bits



xLogic provides the shift register bits S1 to S8, which are assigned the read-only attribute in the circuit program. The content of shift register bits can only be modified by means of the Shift register special function

4.1.6 Analog inputs

You can use up to 36 analog inputs. In your block configuration, you can assign a new input terminal to an input block, provided this terminal is not already used in the circuit program.



For help on analog block parameter, refer to Information on analog value processing.

4.2 Basic functions list – GF

Basic functions represent simple logical elements of Boolean algebra.

You can invert the inputs of individual basic functions , i.e. the circuit program inverts a logical "1" at a relevant input to a logical "0"; if "0" is set at the input, the program sets a logical "1".

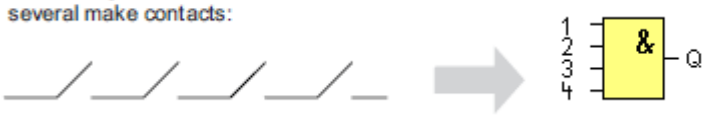
The GF list contains the basic function blocks you can use for your circuit program. The following basic functions are available:

View in the circuit diagram	View in xLogic	Name of the basic function
Series circuit make contact	1 2 3 4 — & — Q	AND
	1 2 3 4 — &↑ — Q	AND with edge evaluation
Parallel circuit with break contacts	1 2 3 4 — & — Q	NAND (Not AND)
	1 2 3 4 — &↓ — Q	NAND with edge evaluation
Parallel circuit with make contacts	1 2 3 4 — ≥1 — Q	OR
Series circuit with break contacts	1 2 3 4 — ≥1 — Q	NOR (Not OR)
Double changeover contact	1 2 — =1 — Q	XOR (exclusive OR)

Break contact	1 → Q	NOT (negation, inverter)

4.2.1 AND

Circuit diagram of a series circuit with several make contacts:



(Symbol in xLogic)

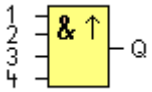
The output of an AND function is only 1 if **all** inputs are 1, i.e. when they are closed.

A block input that is not used (x) is assigned: x = 1.

Logic table of the AND block:

Input1	Input2	Input 3	Input 4	Output
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	0
0	1	1	0	0
0	1	1	1	0
1	0	0	0	0
1	0	0	1	0
1	0	1	0	0
1	0	1	1	0
1	1	0	0	0
1	1	0	1	0
1	1	1	0	0
1	1	1	1	1

4.2.2 AND with edge evaluation



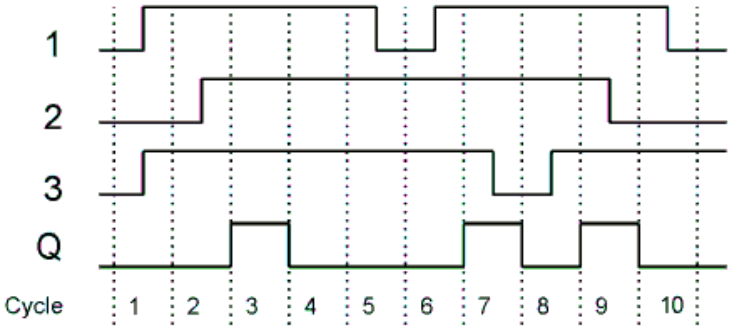
(Symbol in xLogic)

The output of an AND with edge evaluation is only 1 if **all** inputs are 1 and **at least one** input was 0 during the last cycle.

The output is set to 1 for the duration of one cycle and must be reset to 0 for the duration of the next cycle before it can be set to 1 again.

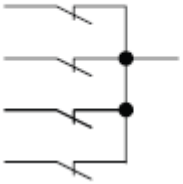
A block input that is not used (x) is assigned: x = 1.

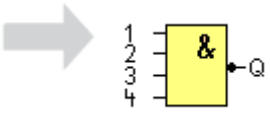
Timing diagram of an AND with edge evaluation



4.2.3 NAND

Parallel circuit with multiple break contacts in the circuit diagram:





(Symbol in xLogic)

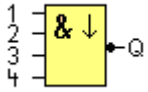
The output of an NAND function is only 0 if **all** inputs are 1, i.e. when they are closed.

A block input that is not used (x) is assigned: x = 1.

Logic table of the NAND block:

Input 1	Input 2	Input 3	Input 4	Output
1	0	0	1	1
1	0	0	1	1
0	0	1	1	1
1	0	0	1	1
0	0	1	1	1
1	1	0	1	1
0	0	1	1	1
1	1	1	0	1
0	1	0	0	1
1	1	1	1	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	1

4.2.4 NAND with edge evaluation

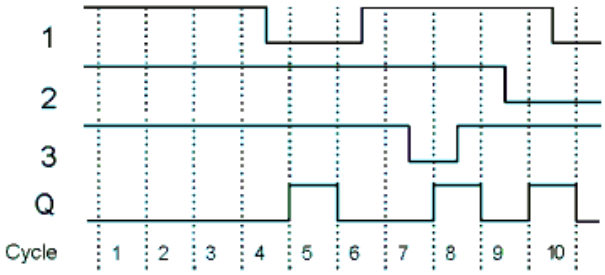


The output of a NAND with edge evaluation is only 1 **at least one** input is 0 and **all** inputs were 1 during the last cycle.

The output is set to 1 for the duration of one cycle and must be reset to 0 at least for the duration of the next cycle before it can be set to 1 again.

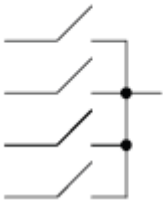
A block input that is not used (x) is assigned: x = 1.

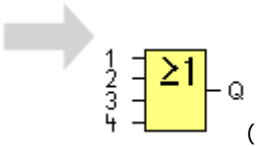
Timing diagram of a NAND with edge evaluation



4.2.5 OR

Circuit diagram of a parallel circuit with several make contacts:





(Symbol in xLogic)

The output of an OR is 1 if **at least one** input is 1 hat, i.e. when it is closed.

A block input that is not used (x) is assigned: x = 0.

Logic table of the OR function:

Input 1	Input 2	Input 3	Input 4	Output
0	0	0	0	0

0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	1
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	1
1	0	0	1	1
1	0	1	0	1
1	0	1	1	1
1	1	0	0	1
1	1	0	1	1
1	1	1	0	1
1	1	1	1	1

4.2.6 NOR

Circuit diagram of a series circuit with several break contacts:

(Symbol in xLogic)

The output of a NOR (NOT OR) is only 1 if **all** inputs are 0 hat, i.e. when they are open. When one of the inputs is switched on (logical 1 state), the output is switched off.

A block input that is not used (x) is assigned: x = 0.

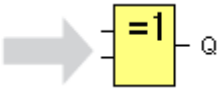
Logic table of the NOR function:

Input 1	Input 2	Input 3	Input 4	Output
0	0	0	0	1
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	0
0	1	1	0	0
0	1	1	1	0
1	0	0	0	0
1	0	0	1	0
1	0	1	0	0
1	0	1	1	0
1	1	0	0	0
1	1	0	1	0

1	1	1	0	0
1	1	1	1	0

4.2.7 XOR

The XOR in a circuit diagram, shown as series circuit with 2 changeover contacts:



(Symbol in xLogic)

The XOR (exclusive OR) output is 1 if the signal status of the inputs is **different**.

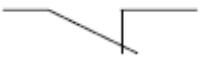
A block input that is not used (x) is assigned: x = 0.

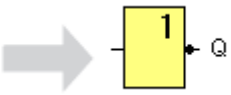
Logic table of the XOR function:

Input 1	Input 2	Output
0	0	0
0	1	1
1	0	1
1	1	0

4.2.8 NOT

A break contact in the circuit diagram:





(Symbol in xLogic)

The output is 1 if the input is 0. The NOT block inverts the input status.

Advantage of the NOT, for example: xLogic no longer requires break contacts. You simply use a make contact and convert it into a break contact with the help of the NOT function.

Logic table of the NOT function:

Input 1	Output
0	1

1	0

4.3 Basics on special functions

Because of their different input designation, you can see right away that there is a difference between the special functions and basic functions. SFs contain timer functions, retentive functions and various parameter assignment options, which allow you to adapt the circuit program to suit your own requirements.
This section provides you with a brief overview of input designations and with some particular background information on SFs. The SFs in particular are described in Chapter 4

4.3.1 Designation of the inputs

Logical inputs

Here, you will find the description of the connectors you can use to create a logical link to other blocks or to the inputs of the xLogic unit.

S (Set):
A signal at input S sets the output to logical "1".

R (Reset):
The reset input R takes priority over all other inputs and resets the outputs.

Trg (Trigger):

This input is used to trigger the start of a function.

Cnt (Count):

This input is used for counting pulses.

Fre (Frequency):

Frequency signals to be evaluated are applied to this input.

Dir (Direction):

This input determines the direction of count.

En (Enable):
This input enables a block function. When this input is "0", other signals to the block will be ignored.

Inv (Invert):
A signal at this input inverts the output signal of the block.

Ral (Reset all):

All internal values are reset.

Parameter inputs

At some of the inputs you do not apply any signals. You configure the relevant block values instead. Examples:

- **Par (Parameter):**

This input will not be connected. Here, you set the relevant block parameters (times, on/off thresholds etc.).

- **No (Cam):**

This input will not be connected. Here, you configure the time patterns.

- **P (Priority):**

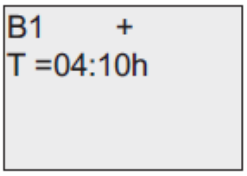
This is an open input. Here, you define priorities and specify whether a message is to be acknowledged in RUN.

4.3.2 Time response

Parameter T

At some of the SFs it is possible to configure a time value T. When you preset this time, note that your input values are based on the timebase set:

Timebase	_ _ : _ _
s (seconds)	seconds : 1/100 seconds
m (minutes)	minutes : seconds
h (hours)	hours : minutes



Setting a time T of 250 minutes:
Unit in hours h:

04:00 hours	240 minutes
00:10 hours	+10 minutes
=	250

Accuracy of T

Because of slight tolerances in the characteristics of electronic components, the set time T may deviate. The xLogic has a maximum tolerance of ± 0.02 %.

When 0.02 % of the time T is smaller than 0.02 seconds, the maximum deviation is 0.02 seconds.

Example: The maximum tolerance per hour (3600 seconds) is ±0.02%, which is proportional to ± 0.72 seconds. The maximum tolerance per minute (60 seconds) is ± 0.02 seconds.

Accuracy of the timer (weekly/yearly timer)

The maximum timing in accuracy is ± 5 s/day.

4.3.3 Backup of the real-time clock

Because the internal real-time clock of an xLogic is backed up, it continues operation after a power failure. The ambient temperature influences the backup time. At an ambient temperature of 25°C, the typical backup time of standard/economic ELC-18 is 10 hours ; standard ELC-12&upgraded ELC-18 is 100 hours;ELC-6&economy ELC-12 is 72 hours.

4.3.4 Retentivity

The switching states and counter values of SFs can be set retentive. This means that current data are retained after a power failure, and that the block resumes operation at the break point. The timer is not reset, but resumes operation until the time-to-go has expired, for example, to enable this response, however, the relevant functions must be set retentive.

R: The data is retained.

/: Current data is not retained (default). See the section in topic "Second circuit program " on enabling and disabling retentivity.

SFs hours counter, weekly timer, yearly timer and PI controller are always retentive.

4.3.5 Parameter protection

In the parameter protection settings, you can determine whether or not the parameters can be displayed and edited in xLogic parameter assignment mode. Two options are available:
+: The parameter attribute permits read/write access in parameter assignment mode (default).
-: The parameter settings are read–/write–protected in parameter assignment mode, and can be edited only in programming mode. See the parameter protection mode example in the "Second circuit program".

4.3.6 Calculating the gain and offset of analog values

A sensor is connected to the analog input and converts a process variable into an electrical signal. This value of signal lies within the typical range of this sensor. xLogic always converts the electrical signals at the analog input into digital values from 0 to 1000. A voltage of 0 to 10 V at input AI is transformed internally into range of values from 0 to 1000. An input voltage exceeding 10 V is shown as internal value 1000.

Because you can not always process the range of values from 0 to 1000 as predetermined by xLogic, you can multiply the digital values by a gain factor and then shift the zero of the range of values (offset). This allows you to output an analog value to the xLogic display, which is proportional to the actual process variable.

Parameter	Minimum	Maximum
Input voltage (in V)	0	≥ 10
Internal value	0	1000
Gain	–10.00	+10.00
Offset	–10000	+10000

Mathematical rule

Actual value **Ax** =
(internal value at input Ax·gain) + offset

Gain and offset calculation
The gain and offset is calculated based on the relevant high and low values of the function.

Example 1:

The available thermocouples have the following technical data: –30 to +70℃, 0 to 10 V DC (i.e. 0 to 1000 in xLogic).
Actual value = (internal value ·gain) + offset, thus
–30 = (0 ·A) + B, i.e. offset B = –30
+70 = (1000 ·A) –30, i.e. gain A = 0.1

Example 2:

A pressure sensor converts a pressure of 1000 mbar into a voltage of 0 V, and a pressure of 5000 mbar into a voltage of 10 V.

Actual value = (internal value. gain) + offset, thus

1000 = (0·A) + B, i.e. offset B = 1000

5000 = （1000 ·A) +1000, i.e. gain A = 4

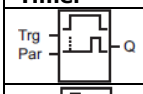
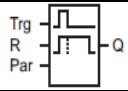
Example of analog values

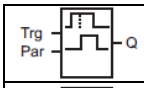
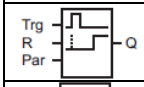
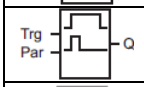
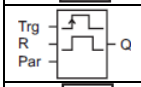
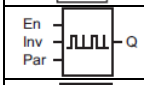
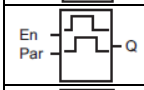
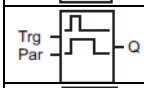
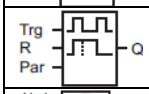
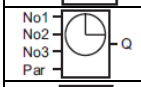
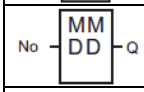
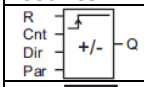
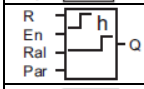
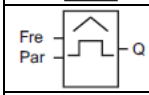
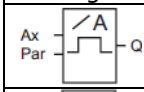
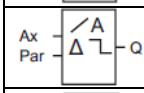
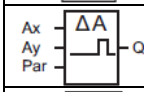
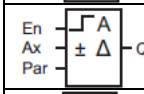
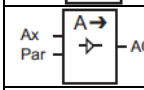
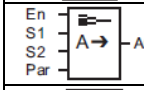
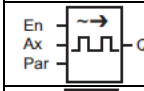
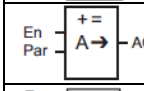
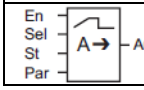
Process variable	Voltage (V)	Internal value	Gain	Offset	Value shown (Ax)
−30⁰ C	0	0	**0.1**	−30	−30
0⁰ C	3	300	**0.1**	−30	0
+70⁰ C	10	1000	**0.1**	−30	70
1000 mbar	0	0	4	1000	1000
3700 mbar	6.75	675	4	1000	3700
5000 mbar	10	1000	4	1000	5000
	0	0	0.01	0	0
	5	500	0.01	0	5
	10	1000	0.01	0	10
	0	0	1	0	0
	5	500	1	0	500
	10	1000	1	0	1000
	0	0	10	0	0
	5	500	10	0	5000
	10	1000	10	0	10000
	0	0	0.01	5	5
	5	500	0.01	5	10
	10	1000	0.01	5	15
	0	0	1	500	500
	5	500	1	500	1000
	10	1000	1	500	1500
	0	0	1	−200	−200
	5	500	1	−200	300
	10	1000	1	−200	800
	0	0	10	−10000	−10000
	10	1000	10	−10000	0
	0.02	2	0.01	0	0
	0.02	2	0.1	0	0
	0.02	2	1	0	2
	0.02	2	10	0	20

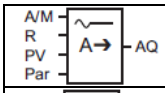
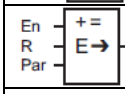
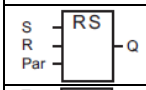
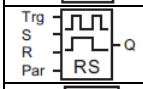
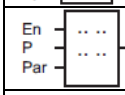
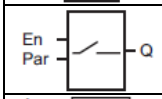
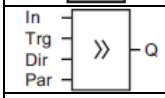
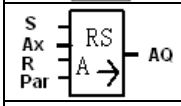
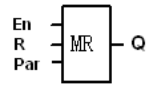
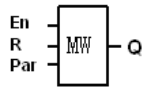
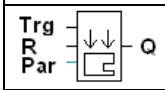
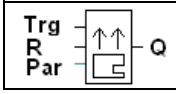
4.4 Special functions list – SF

When you create your circuit program in xLogicsoft, you find the special function blocks in the SF list.

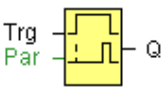
You can invert the inputs of SFs individually, i.e. the circuit program converts a logical "1" at the input into a logical "0"; a logical "0" it converts into a logical "1". The table also specifies whether the relevant function can be set retentivity (Rem). The following SFs are available:

View in xLogic	Name of the special function	Rem
Timer		
	On-delay	REM
	Off-delay	REM

	On-/Off-delay	REM
	Retentive on-delay	REM
	Wiping relay(pulse out)	REM
	Edge triggered wiping relay	REM
	Asynchronous pulse generator	REM
	Random generator	
	Stairway lighting switch	REM
	Multiple funcition switch	REM
	Weekly timer	
	Yearly timer	
Counter		
	Up/down counter	REM
	Hours counter	REM
	Threshold trigger	
Analog		
	Analog threshold trigger	
	Analog differential trigger	
	Analog comparator	
	Analog value monitoring	
	Analog amplifier	
	Analog multiplexer	
	Pulse Width Modulator(PWM)	
	Analog math	
	Analog ramp	

	PI controller	
	Analog math error detection	
Miscellaneous		
	Latching relay	
	Pulse relay	
	Message texts	
	Softkey	
	Shift register	
	Data latchg relay	
	Modbus Read	
	Modbus Write	
	Memory Write	
	Memory Read	

4.4.1 On-delay



Short description

The output is not switched on until a configured delay time has expired.

Connection	Description
Trg input	The on delay time is triggered via the Trg (Trigger) input
Parameter	**T** represents the on delay time after which the output is switched on (output signal transition 0 to 1).

	Retentivity on = the status is retentive in memory.
Output **Q**	Q switches on after a specified time T has expired, provided Trg is still set.

Parameter T

The time for parameter T can also be preset based on the actual value of another, already-configured function. You can use the actual values of the following functions:

Analog comparator: Ax - Ay

Analog trigger: Ax

Analog amplifier: Ax

Analog multiplexer: AQ

Analog ramp: AQ

Analog math: AQ

PI controller:AQ

Data latching relay: AQ

Up/Down counter: Cnt

You select the required function via the block number. Time-base can be adjusted.

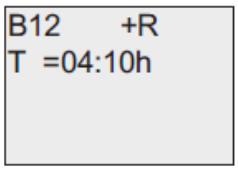
The value of "T" can be set/modified in parameter mode. For information about how to modify，refer to chapter 5.2.2 please.

For information on the validity and accuracy of the time base, refer to the xLogic time-base list as follows:

Valid ranges of the time-base, if T = parameter

Time-base	**Max. value**	**Min. resolution**	**Accuracy**
s (seconds)	99:99	10 ms	**±** 10 ms
m (minutes)	99:59	1s	± 1 s
h (hours)	99:59	1 min	± 1 min

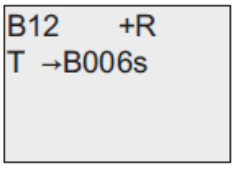
The display in programming mode (example):



Valid ranges of the timebase, if T = Actual value of an already-programmed function

Timebase	max. value	Meaning	Accuracy
ms	99990	Number of ms	+ 10 ms
s	5999	Number of s	+ 1 s
m	5999	Number of min	+ 1 min

The display in programming mode (example):



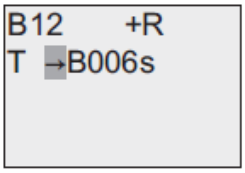
If the referenced block (B6, in the example) returns a value that lies out of the valid range,the value is rounded up or down to the next valid value.

Parameter preset = Actual value of an already-programmed function

How to include the actual value of an already-programmed function:

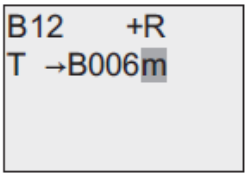
1. Press ▶ to move the cursor to the equal sign of parameter T.

2. Press ▼ to change the equal sign into an arrow. If it exists, the last referenced block and its timebase is shown.

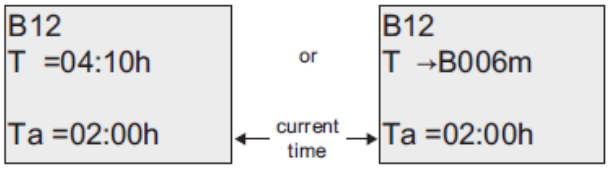


3. Press ▶ to move the cursor to the "B" of the shown block, and then press to select the required block number.

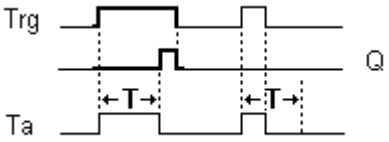
4. Press ▶ to move the cursor to the block's timebase and press to select the required timebase.



The view in parameter assignment mode (example):



Timing diagram



Description of the function

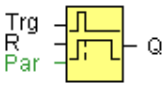
The time Ta (the current time in xLogic) is triggered with the 0 to 1 transition at input Trg.

If the status at input Trg stays 1 at least for the duration of the configured time T, the output is set to 1 when this time has expired (the on signal of the output follows the on signal of the input with delay).

The time is reset if the status at input Trg changes to 0 again before the time T has expired.

The output is reset to 0 when input Trg is 0.

4.4.2 Off-delay



Short description

The output with off delay is not reset until a defined time has expired.

Connection	Description
Input **Trg**	Start the off delay time with a negative edge (1 to 0 transition) at input Trg (Trigger)
Input **R**	Reset the off delay time and set the output to 0 via the R (Reset) input. Reset has priority over Trg
Parameter	**T:** The output is switched off on expiration of the delay time T (output signal transition 1 to 0). **Retentivity** on = the status is retentive in memory.
Output **Q**	Q is switched on for the duration of the time T after a trigger at input Trg.

Parameter

The time set in parameter T can be supplied by the value of another already-programmed function:

Analog comparator: Ax - Ay

Analog trigger: Ax

Analog amplifier: Ax

Analog multiplexer: AQ

Analog ramp: AQ

Analog math: AQ

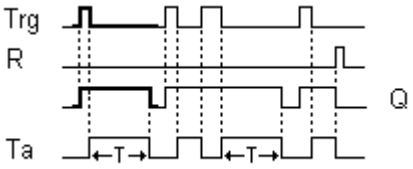
PI controller:AQ

Data latching relay: AQ

Up/Down counter: Cnt

The value of " T " can be set/modified in parameter mode. For information about how to modify, refer to chapter 5.2.2 please.
Select the required function by the block number. The timebase is configurable. For information on valid timebase ranges and parameter preset, refer to chapter 4.4.1 the On-delay topic.

Timing diagram



Description of the function

Output Q is set to 1 momentarily with a 0 to 1 transition at input Trg.

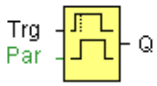
At the 1 to 0 transition at input Trg, xLogic retriggers the current time T, and the output remains set. The output Q is reset to 0 when T_a reaches the value specified in T (T_a=T) (off delay).

A one-shot at input Trg retriggers the time Ta.

You can reset the time Ta and the output via the input R (Reset) before the time Ta has expired.

If retentivity is not set, output Q and the expired time are reset after a power failure.

4.4.3 On-/Off-delay



Short description

The on/off delay function is used to set an output after a configured on delay time and then reset it again upon expiration of a second configured time.

Connection	Description
Input **Trg**	You trigger the on delay with a positive edge (0 to 1 transition) at input Trg (Trigger). You trigger the off delay with a negative edge (1 to 0 transition).
Parameter	**T_H** is the on delay time for the output (output signal transition 0 to 1). **T_L** is the off delay time for the output (output signal transition 1 to 0). **Retentivity** on = the status is retentive in memory.

Output **Q**	Q is switched on upon expiration of a configured time T_H if Trg is still set. It is switched off again upon expiration of the time T_L and if Trg has not been set again.

Parameter

The on-delay time and off-delay time set in parameter **T**H and **T**L can be provided by the actual value of another already-programmed function:

Analog comparator: Ax – Ay

Analog trigger: Ax

Analog amplifier: Ax

Analog multiplexer: AQ

Analog ramp: AQ

Analog math: AQ

PI controller:AQ

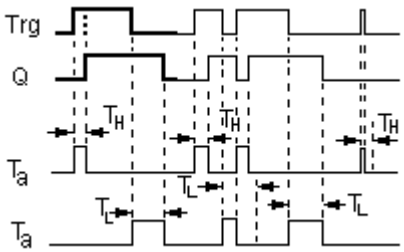
Data latching relay: AQ

Up/Down counter: Cnt

The value of "TH", "TL" can be set/modified in parameter mode. For information about how to modify, refer to chapter 5.2.2 please.

For information on the validity and accuracy of the time base, refer to 4.4.1

Timing diagram



Description of the function

The time T_H is triggered with a 0 to 1 transition at input Trg.

If the status at input Trg is 1 at least for the duration of the configured time T_H, the output is set to logical 1 upon expiration of this time (output is on delayed to the input signal).

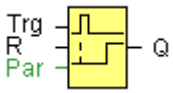
The time T_H is reset if the status at input Trg is reset to 0 before this time has expired.

The time T_L is triggered with the 1 to 0 transition at the output.

If the status at input Trg remains 0 at least for the duration of a configured time T_L, the output is reset to 0 upon expiration of this time (output is off delayed to the input signal).

The time T_L is reset if the status at input Trg is returns to 1 before this time has expired.

4.4.4 Retentive on-delay



Short description

A one-shot at the input triggers a configurable time. The output is set upon expiration of this time.

Connection	Description
Input **Trg**	Trigger the on delay time via the Trg (Trigger) input.
Input **R**	Reset the time on delay time and reset the output to 0 via input R (Reset). Reset takes priority over Trg.
Parameter	**T** is the on delay time for the output (output signal transition 0 to 1). **Retentivity** on = the status is retentive in memory.
Output **Q**	Q is switched on upon expiration of the time T.

Parameter

The time in parameter T can be provided by the value of another already-programmed function:

Analog comparator: Ax – Ay

Analog trigger: Ax

Analog amplifier: Ax

Analog multiplexer: AQ

Analog ramp: AQ

Analog math: AQ

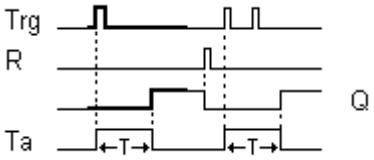
PI controller:AQ

Data latching relay: AQ

Up/Down counter: Cnt

The value of " T " can be set/modified in parameter mode. For information about how to modify, refer to chapter 5.2.2 please.

Timing diagram



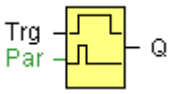
Description of the function

The current time Ta is triggered with a 0 to 1 signal transition at input Trg. Output Q is set to 1 when Ta reaches the time T. A further pulse at input Trg does not affect Ta.

The output and the time Ta are only reset to 0 with a1 signal at input R.

If retentivity is not set, output Q and the expired time are reset after a power failure.

4.4.5 Wiping relay (pulse output)



Short description

An input signal generates an output signal of a configurable length.

Connection	Description
Input **Trg**	You trigger the time for the wiping relay with a signal at input Trg (Trigger)
Parameter	**T**L represents the time after which the output is reset (output signal transition 1 to 0). **Retentivity** set (on) = the status is retentive in memory.
Output **Q**	A pulse at Trg sets Q. The output stays set until the time T has expired and if Trg = 1 for the duration of this time. A 1 to 0 transition at Trg prior to the expiration of T also resets the output to 0.

Parameter

The off time T can be provided by the actual value of another already-programmed function:

Analog comparator: Ax – Ay

Analog trigger: Ax

Analog amplifier: Ax

Analog multiplexer: AQ

Analog ramp: AQ

Analog math: AQ

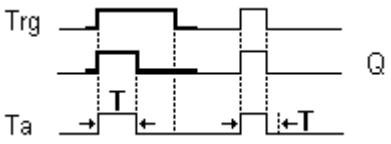
PI controller:AQ

Data latching relay: AQ

Up/Down counter: Cnt

The value of " T L " can be set/modified in parameter mode. For information about how to modify, refer to chapter 5.2.2 please.

Timing diagram



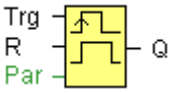
Description of the function

With the input signal Trg = 1, output Q is set to 1. The signal also triggers the time Ta, while the output remains set.

When Ta reaches the value defined at T (Ta=T), the output Q is reset to 0 state (pulse output).

If the signal at input Trg changes from 1 to 0 before this time has expired, the output is immediately reset from 1 to 0.

4.4.6 Edge triggered wiping relay



Short description

An input pulse generates a preset number of output pulses with a defined pulse/pause ratio (retriggerable), after a configured delay time has expired.

Connection	Description
Input **Trg**	You trigger the times for the Edge-triggered wiping relay with a signal at input Trg (Trigger).
Input **R**	The output and the current time Ta are reset to 0 with a signal at input R.
Parameter	**TL, TH:** The interpulse period T_L and the pulse period T_H are adjustable. **N** determines the number of pulse/pause cycles T_L / T_H: Value range: 1...9. **Retentivity** set (on) = the status is retentive in memory.
Output **Q**	Output Q is set when the time T_L has expired and is reset when T_H has expired.

Parameter

The pulse width TH and the interpulse width TL can be provided by the actual value of another already-programmed function:

Analog comparator: Ax – Ay

Analog trigger: Ax

Analog amplifier: Ax

Analog multiplexer: AQ

Analog ramp: AQ

Analog math: AQ

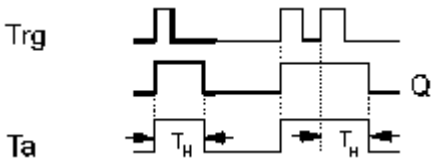
PI controller:AQ

Data latching relay: AQ

Up/Down counter: Cnt

The value of "TH", "TL" can be set/modified in parameter mode. For information about how to modify, refer to chapter 5.2.2 please.

Timing diagram



T_L = 0; N = 1

Description of the function

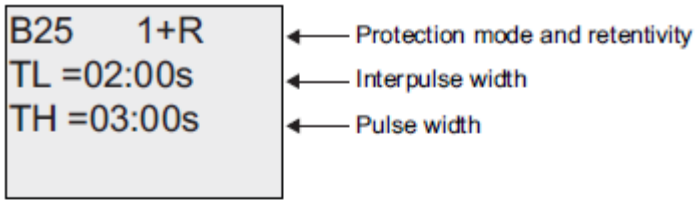
With the change at input Trg to 1, the time T_L (time low) is triggered. After the time T_L has expired, output Q is set to 1 for the duration of the time T_H (time high).

If input Trg is retriggered prior to the expiration of the preset time (T_L + T_H), the time Ta is reset and the pulse/pause period is restarted.

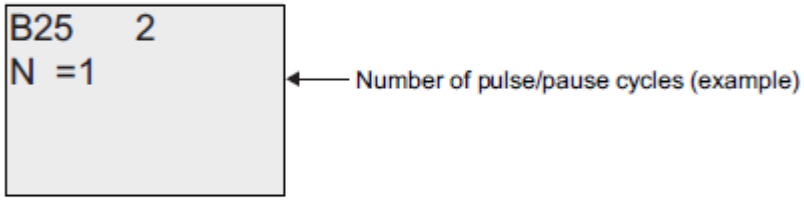
If retentivity is not set, output Q and the time are reset after a power failure.

Setting the Par parameter

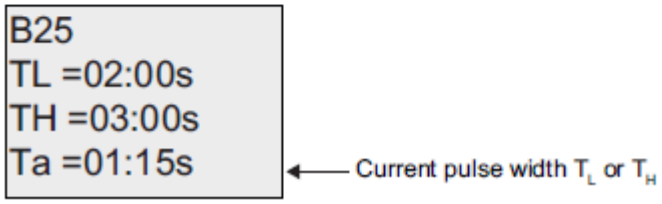
View in programming mode (example):



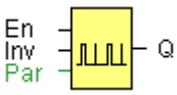
Press ▶



View in parameter assignment mode (example):



4.4.7 Asynchronous pulse generator



Description of function

The pulse shape at the output can be modified via a configurable pulse/pause ratio.

Connection	Description
Input **Inv**	The Inv input can be used to invert the output signal of the active asynchronous pulse generator..
Input **En**	You enable/disable the asynchronous pulse generator with the signal at input En.
Parameter	**TL; TH:** You can customize the pulse (TL)/ pause (TH)

	ratio. **Retentivity** set (on) = the status is retentive in memory.
Output **Q**	Q is toggled on and off cyclically with the pulse times T_H and T_L.

Parameter

The pulse width TH and the interpulse width TL can be provided by the actual value of another already-programmed function:

Analog comparator: Ax – Ay

Analog trigger: Ax

Analog amplifier: Ax

Analog multiplexer: AQ

Analog ramp: AQ

Analog math: AQ

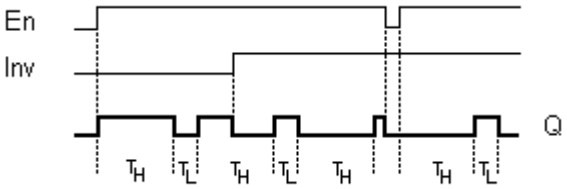
PI controller:AQ

Data latching relay: AQ

Up/Down counter: Cnt

The value of "TH", "TL" can be set/modified in parameter mode. For information about how to modify, refer to chapter 5.2.2 please.

Timing diagram

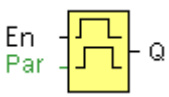


Description of the function

You can set the pulse/pause ratio at the TH (Time High) and TL (Time Low) parameters.

The INV input can be used to invert the output signal. The input block INV only inverts the output signal if the block is enabled via EN.
If retentivity is not set, output Q and the expired time are reset after a power failure.

4.4.8 Random generator



Short description

The output of a random generator is toggled within a configurable time.

Connection	Description

Input **En**	The positive edge (0 to 1 transition) at the enable input En (Enable) triggers the on delay for the random generator. The negative edge (1 to 0 transition) triggers the off delay for the random generator.
Parameter	**TH:** The on delay is determined at random and lies between 0 s and T_H. **TL:** The off delay is determined at random and lies between 0 s and T_L.
Output **Q**	Q is set on expiration of the on delay if En is still set. It is reset when the off delay time has expired and if En has not been set again.

Parameter

The on-delay time TH and the off-delay time TL can be provided by the actual value of another already-programmed function:

Analog comparator: Ax – Ay

Analog trigger: Ax

Analog amplifier: Ax

Analog multiplexer: AQ

Analog ramp: AQ

Analog math: AQ

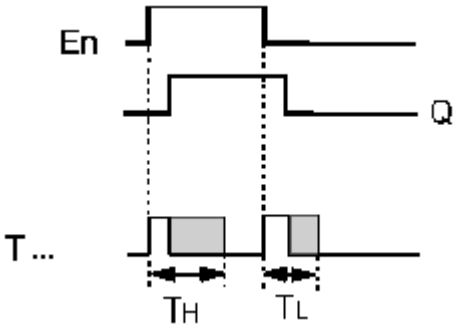
PI controller:AQ

Data latching relay: AQ

Up/Down counter: Cnt

The value of "TH", "TL" can be set/modified in parameter mode. For information about how to modify, refer to chapter 5.2.2 please.

Timing diagram



Description of the function

With the 0 to 1 transition at input En, a random time (on delay time) between 0 s and T_H is set and triggered. If the status at input En is 1 at least for the duration of the on delay, the output is set to 1 when this on delay time has expired.

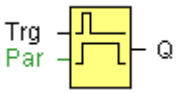
The time is reset if the status at input En is reset to 0 before the on delay time has expired.

When input En is reset 0, a random time (off delay time) between 0 s and T_L is set and triggered.

If the status at input En is 0 at least for the duration of the off delay time, the output Q is reset to 0 when the off delay time has expired.

The time is reset if the status at input En returns to 1 before the on delay time has expired.

4.4.9 Stairway lighting switch



Short description

The edge of an input pulse triggers a configurable time. The output is reset when this time has expired. An off warning can be output prior to the expiration of this time.

Connection	Description
Input **Trg**	You trigger the time (off delay) for the stairway switch with a signal at input Trg (Trigger).
Parameter	**T:** The output is reset (1 to 0 transition when the time T has expired. **T!** Determines the triggering time for the pre-warning. **T!L** determines the length of the pre-warning time. **Retentivity** set (on) = the status is retentive in memory.
Output **Q**	Q is reset after the time T has expired. A warning signal can be output before this time has expired.

Parameter

The off-delay time T, the prewarning time T! and the prewarning period T!L can be provided by the actual value of another already-programmed function:

Analog comparator: Ax – Ay

Analog trigger: Ax

Analog amplifier: Ax

Analog multiplexer: AQ

Analog ramp: AQ

Analog math: AQ

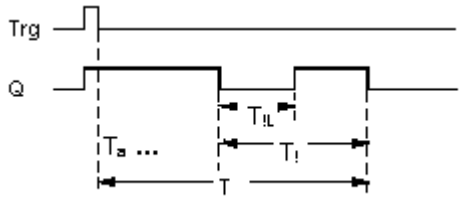
PI controller:AQ

Data latching relay: AQ

Up/Down counter: Cnt

The value of " T " can be set/modified in parameter mode. For information about how to modify, refer to chapter 5.2.2 please.

Timing diagram



Changing the time base

You can change the pre-warning time base and the period.

Time base T	Pre-warning time	Pre-warning period
Seconds	750 ms	50 ms
Minutes	15 s	1 s
Hours	15 min	1 min
* makes sense only for programs with a cycle time of < 25 ms		

Description of the function

Output Q is set to 1 with a 0 to 1 signal transition at input Trg. The 1 to 0 transition at input Trg triggers the current time and output Q remains set.

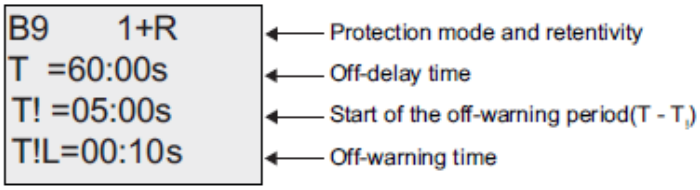
Output Q is reset to 0 when Ta reaches the time T. Before the off delay time (T - $T_!$) has expired, you can output a pre-warning that resets Q for the duration of the off pre-warning time $T_{!L}$.

Ta is retriggered (optional) at the next high/low transition at input Trg and if Ta is expiring. If retentivity is not set, output Q and the expired time are reset after a power failure.

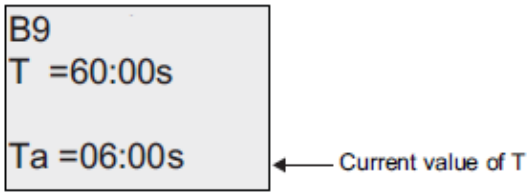
Setting the Par parameter

Note

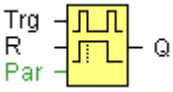
All times must have the same timebase.



View in parameter assignment mode (example):



4.4.10 Multiple function switch



Short description

Switch with two different functions:

- Pulse switch with off delay
- Switch (continuous light)

Connection	Description
Input **Trg**	With a signal at input Trg (Trigger) you set output Q (continuous light), or reset Q with off delay. Output Q can be reset with a signal at the Trg input.
Input **R**	You set the current time Ta, and reset the output to 0, with a signal at input R.
Parameter	**T:** The output is reset (1 to 0 transition) when the time T has expired. **T_L** determines the period during which the input must be set in order to enable the permanent light function. **$T_!$** Determines the on delay for the pre-warning time. **$T_{!L}$** determines the length of the pre-warning time. **Retentivity** set (on) = the status is retentive in memory.
Output **Q**	Output Q is set with a signal at input Trg, and it is reset again after a configured time has expired and depending on the pulse width at input Trg, or it is reset with another signal at input Trg.

Parameter

The off-delay time T, the permanent light time TL, the on-delay prewarning time T!, and the prewarning time period T!L can be provided by the actual value of another already-programmed function:

Analog comparator: Ax – Ay

Analog trigger: Ax

Analog amplifier: Ax

Analog multiplexer: AQ

Analog ramp: AQ

Analog math: AQ

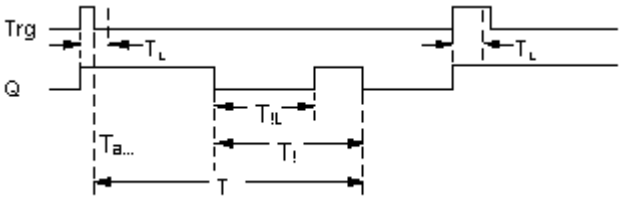
PI controller:AQ

Data latching relay: AQ

Up/Down counter: Cnt

The value of "T","TL" can be set/modified in parameter mode. For information about how to modify, refer to chapter 5.2.2 please.

Timing diagram



Description of the function

Output Q is set to 1 with a 0 to 1 signal transition at Trg.

If output Q = 0, and input Trg is set hi at least for the duration of TL, the permanent lighting function is enabled and output Q is set accordingly.

The off delay time T is triggered when the status at input Trg changes to 0 before the time T_L has expired.

Output Q is reset when the Ta = T.

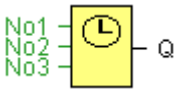
Before the off delay time (T - $T_!$) has expired, you can output an off pre-warning that resets Q for the duration of the off pre-warning time $T_{!L}$. A further signal at input Trg always resets T and output Q.

Caution

The time base for the T, $T_!$ and $T_{!L}$ must be identical.

If retentivity is not set, output Q and the expired time are reset after a power failure.

4.4.11 Weekly timer



Caution

Your xLogic must be equipped with an internal real-time clock if you are going to use this SFB.

Short description

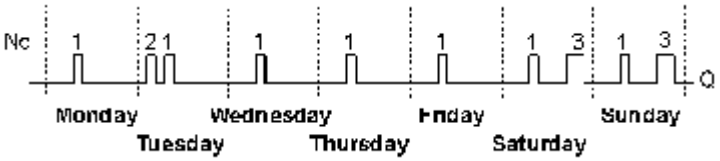
The output is controlled by means of a configurable on/off date. The function supports any combination of weekdays.

Connection	Description
Parameter	At the **No1, No2, No3** (cam) parameters you set the on and off triggers for each cam of the weekly timer. The parameter units are the days and the time-of-day.
Output Q	Q is set when the configured cam is actuated.

Parameter

You can configure a time hysteresis for each individual cam in parameter mode. For information about how to modify, refer to chapter 4.2.2 please.

Timing diagram (three practical examples)



No1: Daily: 06:30 h to 08:00 h

No2: Tuesday: 03:10 h to 04:15 h

No3: Saturday and Sunday: 16:30 h to 23:10 h

Description of the function

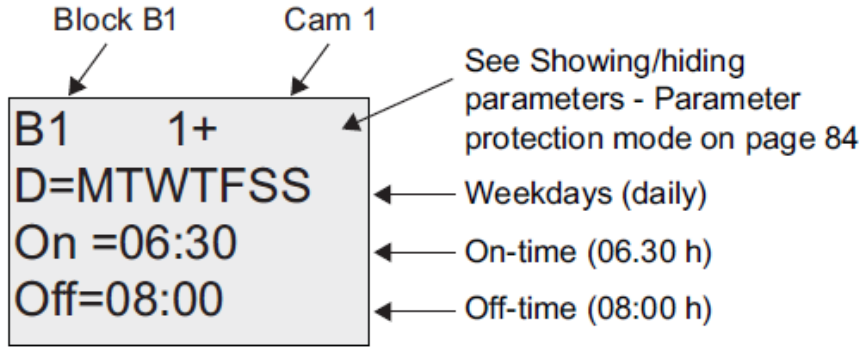
Each weekly timer is equipped with three cams. You can configure a time hysteresis for each individual cam. At the cams you set the on and off hysteresis. The weekly timer sets the output at a certain time, provided it is not already set.

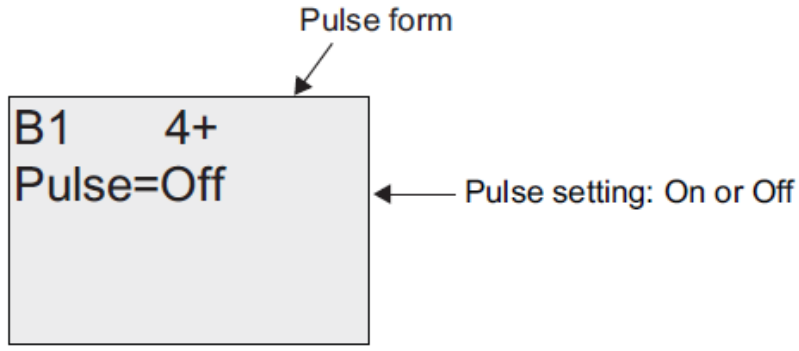
The output is reset at a certain time, provided it is not already reset. A conflict is generated in the weekly timer when the set on time and the set off time at another cam are identical. In this case, cam 3 takes priority over cam 2, while cam 2 takes priority over cam 1.

The switching status of the weekly timer is determined by the status at the No1, No2 and No3 cams.

Parameter assignment screen form

View of the parameter assignment screen form, for example for Cam1 and the Pulse setting:





Note: The "pulse" only applied to ELC-12&Upgraded ELC-18 CPU and the standard/economic

ELC-18 with the hardware version number must be not less than "2.04".

Days of the week
The prefix "D=" (Day) has the following meaning:
- M: Monday
- T: Tuesday
- W: Wednesday
- T: Thursday
- F: Friday
- S: Saturday
- S: Sunday

Uppercase letters indicate a specific day of the week. A "-" indicates no selection for the day of the week.

On-/Off-times
Any time between 00:00 h and 23:59 h is possible.You can also configure the on time to be a pulse signal. The timer block will be activated at the specified time for one cycle and then the output is reset.
- -:- - means: No on-/off-times set.

Setting the weekly timer
To set the on-/off-times:
1. Move the cursor to one of the Cam parameters of the timer (e.g. No1).
2. Press **OK**. xLogic opens the Cam parameter assignment screen form. The cursor is positioned on the day of the week.
3. Press ▲ and ▼ to select one or several days of the week.
4. Press ▶ to move the cursor to the first position of the on-time.
5. Set the on-time.

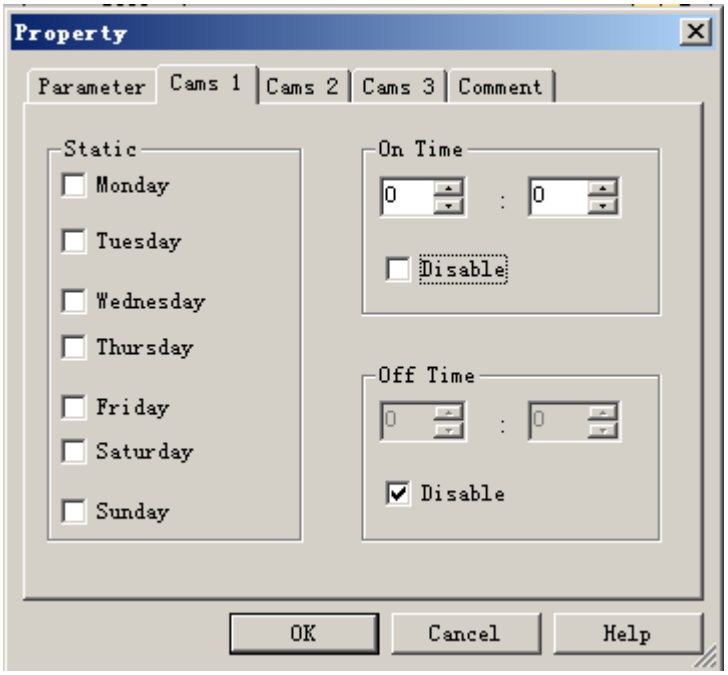
Modify the value at the respective position, using the keys ▲ and ▼ . Move to the cursor to the various positions, using the keys ◀ and ▶ . At the first position, you can only select the value - -:- -
(- -:- - means: No on-/off-times set).
6. Press ▶ to move the cursor to the first position of the off-time.
7. Set the off-time (in same way as in step 5).
8. Confirm your entries with **OK**.

The cursor is now positioned on the No2 parameter (Cam2) and you can configure a further cam.

Special characteristics to note when configuring

The block properties window offers a tab for each one of the three cams. Here you can set the weekly on times for the cams. Each tab offers you in addition an option of defining the on and off times for each cam in hour and minute units. Hence, the shortest switching cycle is one minute.

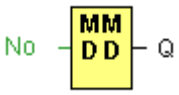
You can disable the on and off times individually, i.e. you can achieve switching cycles extending across more than one day, for example, by setting the on time for cam 1 to Monday 7:00 h and the off time of cam 2 to Wednesday 13:07 h, while disabling the on time for cam 2.



Backup of the real-time clock

The internal real-time clock of xLogic is buffered against power failure. The buffering time is influenced by the ambient temperature, and is typically 10 hours (standard&economic ELC-18) or 100 hours(standard ELC-12&upgraded ELC-18) or 72 hours(ELC-6&economic ELC-12) at an ambient temperature of 25°C.

4.4.12 Yearly timer



Caution

Your xLogic must be equipped with an internal real-time clock if you are going to use this SFB.

Short description

The output is controlled by means of a configurable on/off date

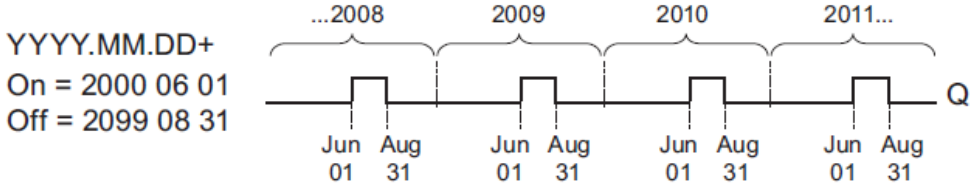
Connection	Description
Parameter	At the **No** (cam) parameter you set the on and off trigger for the cam of the yearly timer.
Output **Q**	Q is set on when the configured cam is switched on.

Parameter

The on and off trigger for the cam of the yearly timer can be set/modified in parameter mode and you can configure what you need. For information about how to modify, refer to chapter 5.2.2 please.

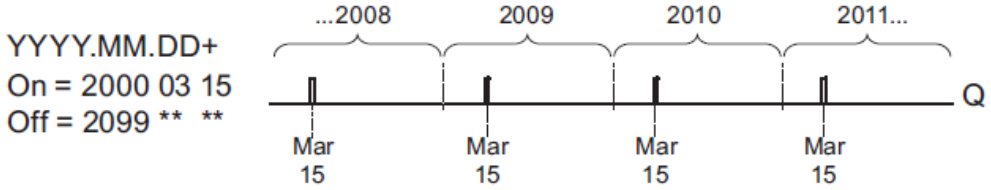
Timing diagrams

Example 1: Yearly mode on, Monthly mode off, Pulse Off, On Time = 2000-06-01, Off Time = 2099-08-31: Every year on June 1 the timer output switches on and remains on until August 31.



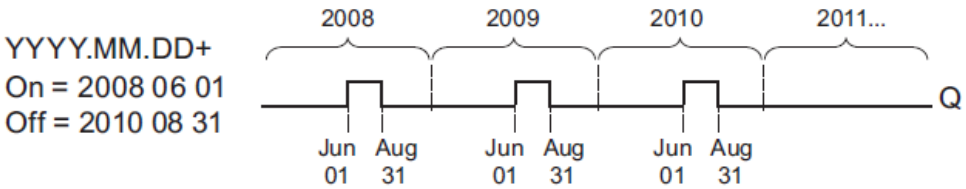
B6 1+	B6 2+	B6 3+
Yearly = On	ON :	OFF :
Monthly = Off	YYYY-MM-DD	YYYY-MM-DD
Pulse = Off	2000-06-01	2099-08-31

Example 2:Yearly mode on, Monthly mode off, Pulse on, On Time = 2000-03-15, Off Time = 2099-**-**: Every year on March 15, the timer switches on for one cycle.



B6 1+	B6 2+	B6 3+
Yearly = On	ON :	OFF :
Monthly = Off	YYYY-MM-DD	YYYY-MM-DD
Pulse = On	2000-03-15	2099-**-**

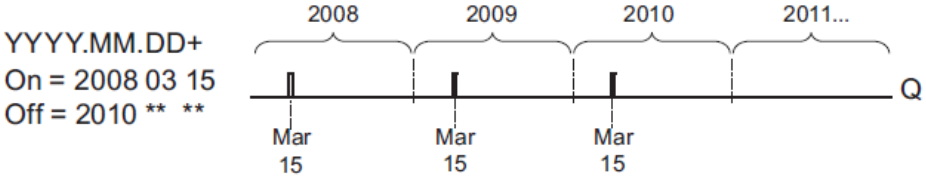
Example 3: Yearly mode on, Monthly mode off, Pulse off, On Time = 2008-06-01, Off Time = 2010-08-31: On June 1 of 2008, 2009, and 2010 the timer output switches on and remains on until August 31.



B6 1+	B6 2+	B6 3+
Yearly = On	ON :	OFF :
Monthly = Off	YYYY-MM-DD	YYYY-MM-DD
Pulse = Off	2008-06-01	2010-08-31

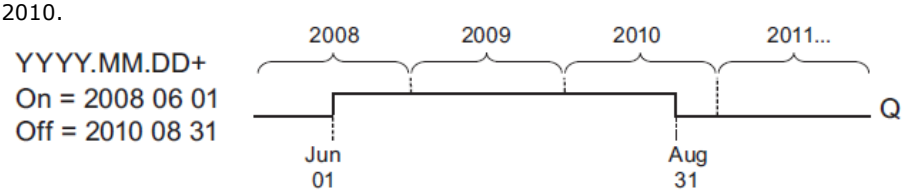
Example 4:Yearly mode on, Monthly mode off, Pulse on, On Time = 2008-03-15, Off Time = 2010-**-**: On March 15 of 2008, 2009, and 2010, the timer output switches on for one

cycle.



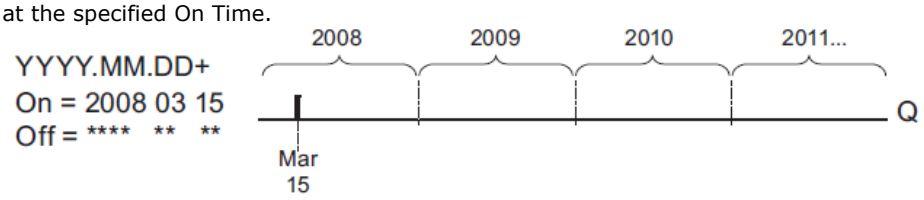
B6 1+	B6 2+	B6 3+
Yearly = On	ON :	OFF :
Monthly = Off	YYYY-MM-DD	YYYY-MM-DD
Pulse = On	2008-03-15	2010-**-**

Example 5:Yearly mode off, Monthly mode off, Pulse off, On Time = 2008-06-01, Off Time = 2008-08-31: On June 1, 2008 the timer output switches on and remains on until August 31, 2010.



B6 1+	B6 2+	B6 3+
Yearly = Off	ON :	OFF :
Monthly = Off	YYYY-MM-DD	YYYY-MM-DD
Pulse = Off	2008-06-01	2010-08-31

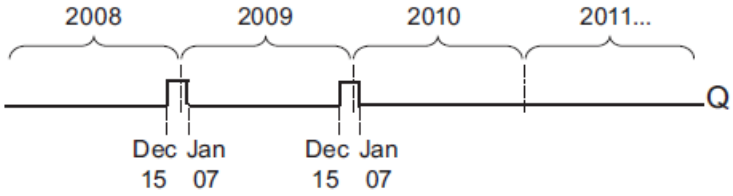
Example 6:Yearly mode off, Monthly mode off, Pulse selected, On Time = 2008-03-15, Off Time = ****-**-**: On March 15, 2008 the timer output switches on for one cycle. Because the timer does not have a monthly action or yearly action, the timer output pulses only one time at the specified On Time.



B6 1+	B6 2+	B6 3+
Yearly = Off	ON :	OFF :
Monthly = Off	YYYY-MM-DD	YYYY-MM-DD
Pulse = On	2008-03-15	****-**-**

Example 7:Yearly mode on, Monthly mode off, Pulse off, On Time = 2008-12-15, Off Time = 2010-01-07: On December 15 of 2008 and 2009, the timer output switches on and remains on until January 7 of the following year. When the timer output turns off on January 7, 2010 it does NOT turn on again the following December 15.

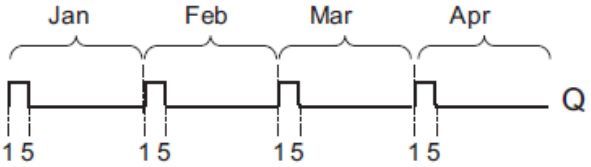
YYYY.MM.DD+
On = 2008 12 15
Off = 2010 01 07



B6 1+	B6 2+	B6 3+
Yearly = On	ON :	OFF :
Monthly = Off	YYYY-MM-DD	YYYY-MM-DD
Pulse = Off	2008-12-15	2010-01-07

Example 8:Yearly mode on, Monthly mode on, On Time = 2008-**-01, Off Time = 2010-**-05: Starting in 2008, on the first day of each month the timer output switches on and switches off on the fifth day of the month. The timer continues in this pattern through the last month of 2010.

YYYY.MM.DD+
On = 2008 ** 01
Off = 2010 ** 05



B6 1+	B6 2+	B6 3+
Yearly = On	ON :	OFF :
Monthly = On	YYYY-MM-DD	YYYY-MM-DD
Pulse = Off	2008-**-01	2010-**-05

Description of the function

The yearly timer sets and resets the output at specific on and off times.

The off-date identifies the day on which the output is reset again. The first value defines the month, the second the day.

When you select the every month check box, the yearly clock switches on or off at a certain day of every month.

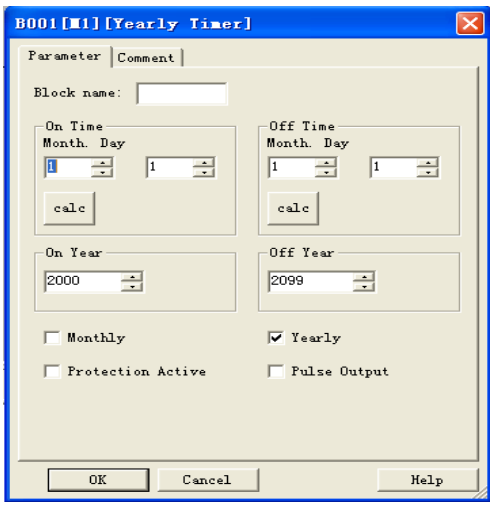
Backup of the real-time clock

The internal real-time clock of xLogic is buffered against power failure. The buffering time is influenced by the ambient temperature, and is typically 10 hours (ELC-18) or 100 hours(ELC-12) at an ambient temperature of 25°C.

Special characteristics to note when configuring

A click on the dialog box enables direct keyboard input of the month and day values. The values entered may not exceed the logical maximum of the relevant input boxes; otherwise xLogicsoft returns an error message.

The **calendar** icon offers you an easy way of setting the date. It opens a window where you can set the days and months by clicking the relevant buttons.

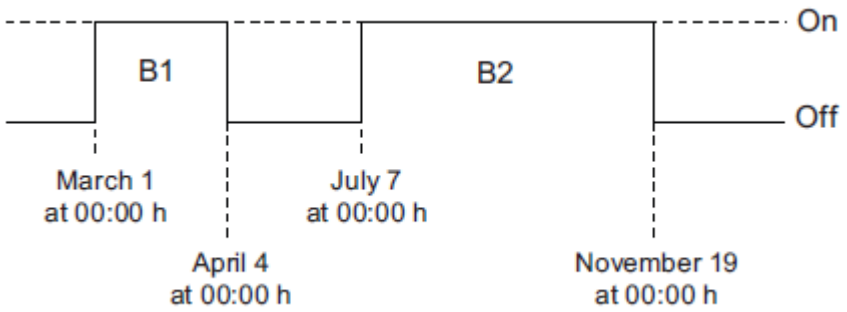


Sample configuration

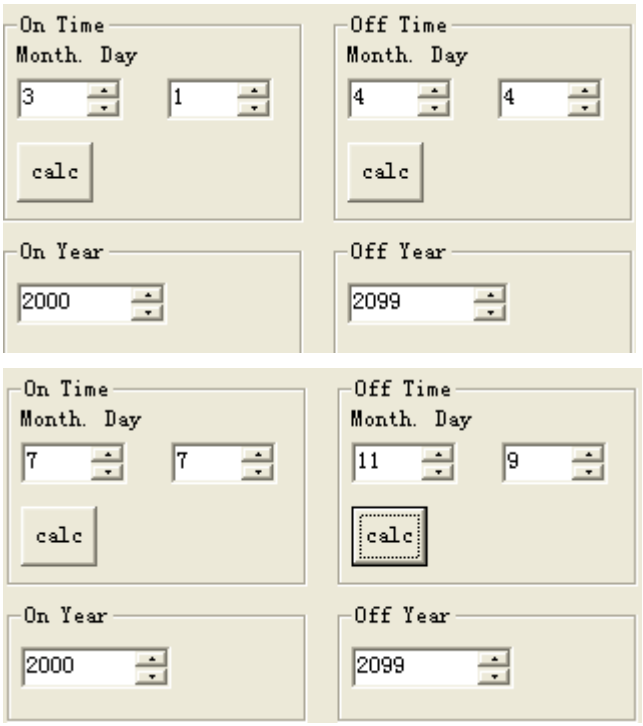
The output of an xLogic is to be set annually on March 1, reset on April 4, set again on July 7, and reset again on November 19. You need to configure two yearly timers with corresponding on-times. Then logically link the outputs by means of an OR block.

B1 1+ Yearly = On Monthly = Off Pulse = Off	Yearly Timer 1 On-time Mar 1 Off-time Apr 4	B2 1+ Yearly = On Monthly = Off Pulse = Off	Yearly Timer 2 On-time Jul 7 Off-time Nov 19
B1 2+ ON : YYYY-MM-DD 2000-03-01		B2 2+ ON : YYYY-MM-DD 2000-07-07	
B1 3+ OFF : YYYY-MM-DD 2099-04-04		B2 3+ OFF : YYYY-MM-DD 2099-11-19	

Result

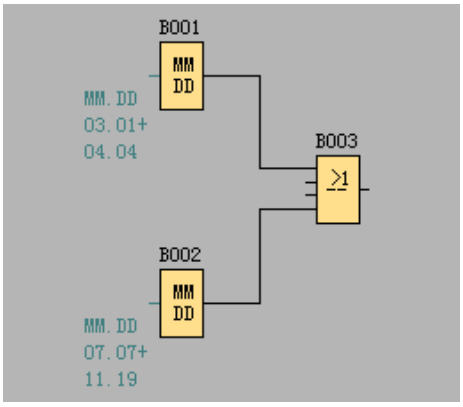


Place two yearly timer switch SFBs on your programming interface and configure the blocks as specified.

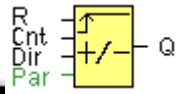


Create a logical link of the blocks via a standard OR block. The OR output is 1 if at least one of the yearly timer switches is set.

Note: The Yearly and Pulse settings are available only for the ELC-12&Upgraded ELC-18 series and the standard/economic ELC-18 with the hardware version number must be not less than "2.04".



4.4.13 Up/Down counter



Short description

An input pulse increments or decrements an internal value, depending on the parameter setting. The output is set or reset when a configured threshold is reached. The direction of count can be changed with a signal at input Dir

Connection	Description
Input **R**	You reset the output and the internal counter value to zero with a signal at input R (Reset).
Input **Cnt**	This function counts the 0 to 1 transitions at input Cnt. It does not count 1 to 0 transitions.
Input **Dir**	Input Dir (Direction) determines the direction of count: Dir = 0: Up Dir = 1: Down
Parameter	**On:** On threshold Value range: 0...99999999 **Off:** Off threshold Value range: 0...99999999 **StartVal:** Initial value from which to begin counting either down or up. **Retentivity** set (on) = the status is retentive in memory.
Output **Q**	Q is set and reset according to the actual value at Cnt and the set thresholds.

Note:
The Start Value parameter does only exist on ELC-12 CPU（new version）. The counter always counts up or down from 0 on ELC-18 CPU.

Parameter

The on threshold On and the off threshold Off can be provided by the value of another already-programmed function:

Analog comparator: Ax – Ay

Analog trigger: Ax

Analog amplifier: Ax

Analog multiplexer: AQ

Analog ramp: AQ

Analog math: AQ

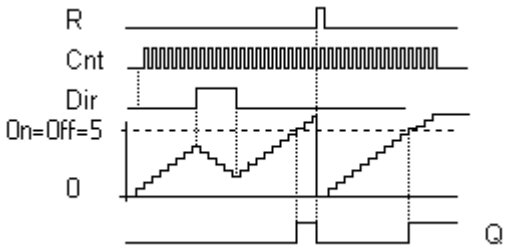
PI controller:AQ

Data latching relay: AQ

Up/Down counter: Cnt

The value of " On " , " Off " and " Cnt " can be set/modified in parameter mode. For information about how to modify ,refer to chapter 5.2.2 please.

Timing diagram



Description of the function

The function increments (Dir = 0) or decrements (Dir = 1) the internal counter by one count with every positive edge at input Cnt.

You can reset the internal counter value to '000000', with a signal at the reset input R. As long as R=1, the output is 0 and the pulses at input Cnt are not counted.

Output Q is set and reset according to the actual value at Cnt and the set thresholds. See the following rules for calculation.

Calculation rule

- If the on threshold >= off threshold, then:
  Q = 1, if Cnt >= On
  Q = 0, if Cnt < Off.
- If the on threshold < off threshold, then:
  Q = 1, if On <= Cnt < Off.

Caution

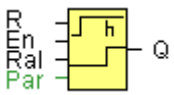
The function polls the limit value of the counter once in each cycle.

Thus, if the pulses at the fast inputs (ELC-12) or IB/IC(ELC-18) are faster than the scan cycle time, the SFB might not switch until the so specified limit has been exceeded.

Example: Up to 100 pulses per cycle can be counted; 900 pulses have been counted so far. On = 950; Off = 10000. The output is set in the next cycle, after the value has reached 1000.

The output would not be set at all if the value Off = 980

4.4.14 Hours counter

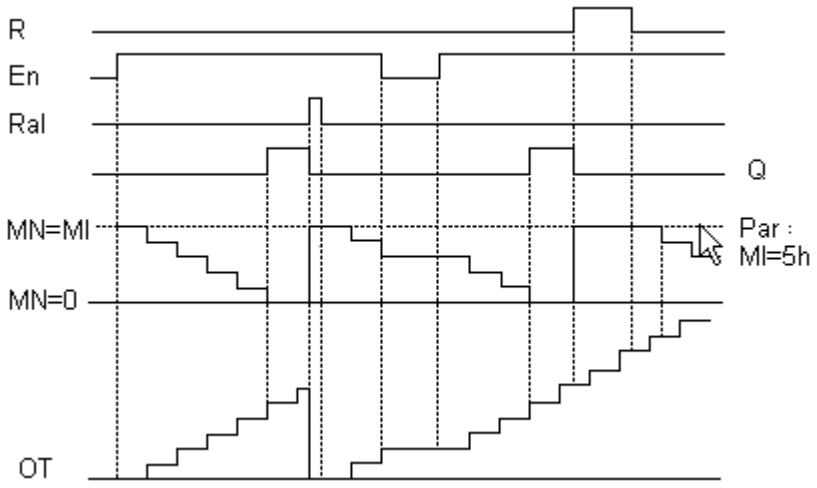


Short description

A configured time is triggered with a signal at the monitoring input. The output is set when this time has expired.

Connection	Description
Input **R**	A positive edge (0 to 1 transition) at input R resets output Q and sets a configured value MI at the counter for the duration of the time-to-go (MN).
Input **En**	En is the monitoring input. xLogic scans the on-time of this input.
Input **Ral**	A positive edge at input Ral (Reset all) resets both the hours counter (OT) and the output, and sets the configured value MI at the counter to for the duration of the time-to-go (MN). That is, • Output Q = 0, • The measured operating hours OT = 0, and • The time-to-go of the maintenance interval MN = MI.
Parameter	**MI**: Maintenance interval to be specified in hour units Range of values: 0000...9999 h **OT**: Expired total operation time. An offset can be specified. Range of values: 00000...99999 h **Q** 0: • When "R" is selected: Q = 1, if MN = 0; Q = 0, if R = 1 or Ral = 1 • When "R+En" is selected: Q = 1, if MN = 0; Q = 0, if R = 1 or Ral = 1 or En = 0.
Output **Q**	The output is set when the time-to-go MN = 0. The output is reset: • When "Q 0:R+En", if R = 1 or Ral = 1 or En = 0 • When "Q 0:R", if R = 1 or Ral = 1.

Timing diagram



MI = Configured time interval

MN = Time-to-go

OT = Total time expired since the last 1 signal at the Ral input

These values are principally held retentive!

Parameter

The maintenance interval MI can be provided by the actual value of another already-programmed function:

Analog comparator: Ax – Ay

Analog trigger: Ax

Analog amplifier: Ax

Analog multiplexer: AQ

Analog ramp: AQ

Analog math: AQ

PI controller:AQ

Data latching relay: AQ

Up/Down counter: Cnt

The value of "MI" can be set and modified in parameter mode. For information about how to modify, refer to chapter 4.2.2 please.

Description of the function

The hours counter monitors input En. As long as the status at this input is 1, xLogic calculates the time expired and the time-to-go MN. xLogic displays these times when set to configuration mode. The output is set to 1 when the time-to-go is equal to zero.

You reset output Q and the time-to-go counter to the specified value MI with a signal at input R. The operation hour counter OT remains unaffected.

You reset output Q and the time-to-go counter to the specified value MI with a signal at input Ral. The operation hour counter OT is reset to 0.

Depending on your configuration of the Q parameter, the output is either reset with a reset signal at input R or Ral, or when the reset signal is 1 or the En signal is 0.

Limit value of OT

The value of the operating hours in OT are retained when you reset the hours counter with a signal at input R. The hours counter OT continues the count as long as En = 1, irrespective of the status at the reset input R. The counter limit of OT is 99999 h. The hours counter stops when it reaches this value.

In programming mode, you can set the initial value of OT. The counter starts operation at any value other than zero. MN is automatically calculated at the START, based on the MI and OT values.

Example: MI = 100, OT = 130, the result is MN = 70

Parameter preset

In xLogicsoft, you can define MI and an OT start value.

You determine that Q does not depend on En by selecting the corresponding check box.

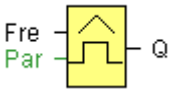
Retentivity with the hours counter

The hours counter in the xLogic is generally retentive.

However, if the values of the hours counter are lost after a power failure, then select the respective block in your circuit program. Right mouse click on the hours counter and select **Block Properties > Parameters**. The option **Retentivity** must be activated and not changeable (grayed out).

If the **Retentivity** option is not available, then delete the block and insert a new special function **hours counter** at the same position.

4.4.15 Threshold trigger



Short description

The output is switched on and off, depending on two configurable frequencies.

Connection	Description
Input Fre	The function count 0 to 1 transitions at input Fre. ! to 0 transitions are not counted. Use • Inputs I5,I6,I7,I8(ELC-12) or IB/IC(ELC-18) for fast counts max. 14 kHz (I5,I6),60kHz（I7,I8）(ELC-12) or 14 kHz (ELC-18). • Any other input or circuit element for low frequencies (typical 4 Hz).
Parameter	**On:** On threshold Range of values: 0000...9999 **Off:** Off threshold Range of values: 0000...9999 **G_T:** Time interval or gate time during which the input pulses are measured. Range of values: 00:05 s...99:99 s
Output **Q**	Q is set or reset according to the threshold values.

Parameter

The gate time G_T can be provided by the actual value of another already-programmed function:

Analog comparator: Ax – Ay

Analog trigger: Ax

Analog amplifier: Ax

Analog multiplexer: AQ

Analog ramp: AQ

PI controller:AQ

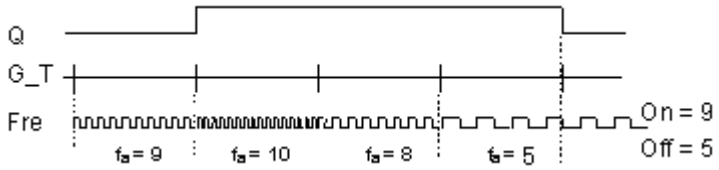
Up/Down counter: Cnt

Data latching relay: AQ

Analog Math AQ

The value of " On " , " Off " can be set/modified in parameter mode. For information about how to modify, refer to chapter 5.2.2 please.

Timing diagram



fa = Input frequency

Description of the function

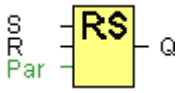
The trigger measures the signals at input Fre. The pulses are captured during a configurable period G_T.

Q is set or reset according to the set thresholds. See the following calculation rule.

Calculation rule

- If the threshold (On) > threshold (Off), then:
  Q = 1, if fa >= On
  Q = 0, if fa < Off.
- If the threshold (On) < threshold (Off), then Q = 1, if
  On <= fa < Off.

4.4.16 Latching relay



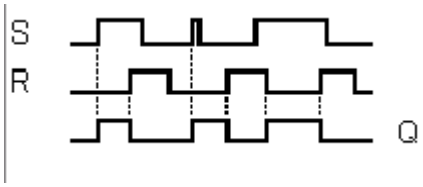
Short description

A signal at input S sets output Q. A signal at input R resets output Q.

Connection	Description
Input **S**	Set output Q with a signal at input S (Set).
Input **R**	Reset output Q with a signal at input R (Reset). Output Q is reset if S and R are both set (reset has priority over set).
Parameter	**Retentivity** set (on) = the status is retentive in memory.
Output **Q**	Q is set with a signal at input S and remains set until it is reset with signal at input R.

No parameter of Latching relay can be set/modified in parameter mode .

Timing diagram



Description of the function

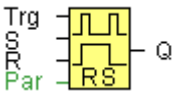
The latching relay represents a simple binary memory logic. The output value depends on the input states and the previous status at the output.

Logic table of the latching relay:

S	R	Q	Remark
0	0	x	Status unchanged
0	1	0	Reset
1	0	1	Set
1	1	0	Reset

When retentivity is enabled, the output signal corresponds with the signal status prior to the power failure.

4.4.17 Pulse relay



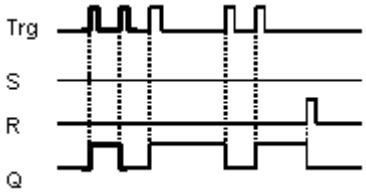
Short description

The output is set and reset with a short one-shot at the input.

Connection	Description
Input **Trg**	You switch output Q on or off with a signal at input Trg (Trigger) input.
Input **S**	A one-shot at input S (Set) sets the output to logical 1.
Input **R**	A one-shot at input R (Reset) resets the output to logical 0
Parameter	**Selection:** RS (input R priority), or SR (input S priority) **Retentivity** set (on) = the status is retentive in memory.
Output **Q**	Q is switched on with a signal at Trg and is reset again at the next Trg pulse, if both S and R = 0.

No parameter of Latching relay can be set/modified in parameter mode .

Timing diagram



Description of the function

The status of output Q changes with each 0 to 1 transition at input Trg and if both S and R = 0, i.e. the output is switched on or off.

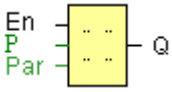
Input Trg does not influence the SFB when S = 1 or R = 1.

A one-shot at input S sets the pulse relay, i.e. the output is set to logical 1.

A one-shot at input R resets the pulse relay to its initial state, i.e. the output is set to logical 0.

Either the input R takes priority over input S (i.e. the signal at input S has no effect as long as R = 1), or the input S takes priority over input R (i.e. the signal at input R has no effect as long as S = 1), depending on your configuration.

4.4.18 Message text



Short description

Display parameterized message texts and parameters of other blocks in RUN mode.

Connection	Description
Input **En**	A 0 to 1 transition at En (Enable) triggers the output of the message text.
Input **P**	P is the priority of the message text. 1 is the lowest, 32 is the highest priority. Quit: Acknowledgement of the message text
Parameter	**Text:** Input of the message text **Par:** Parameter or actual value of another, already configured function (see "Visible parameters or actual values") **Time:** Shows the continuously updated time-of-day **Date:** Shows the continuously updated date **EnTime:** Shows the time of the 0 to 1 transition **EnDate:** Shows the 0 to 1 transition of the date
Output **Q**	Q remains set as long as the message text is queued.

Description of the function

With a 0 to 1 transition of the signal at input En, the display outputs your configured message text (actual value, text, TOD, date) in RUN mode.

Acknowledgement disabled (Ack = Off):

The message text is hidden with a 0 to 1 signal transition at input En.

Acknowledgement enabled (Ack = On):

After input En is reset to 0, the message text is displayed until acknowledged by pressing the OK button. The message text cannot be acknowledged as long as input En is high.

If several message text functions were triggered with En=1, the message with the highest priority (1 = lowest, 64=highest) is displayed. This also implies that a new message text is only displayed if its priority is higher than that of previously enabled message texts.

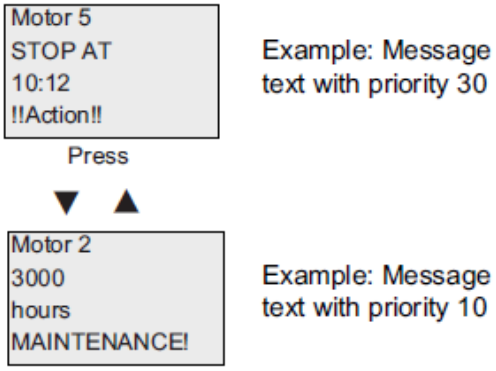
After a message text is disabled or acknowledged, the function automatically shows the previously active message text that takes the highest priority.

You can press the ▲ and ▼ keys to step through multiple active message texts.

Example

This is how two message texts could be shown:

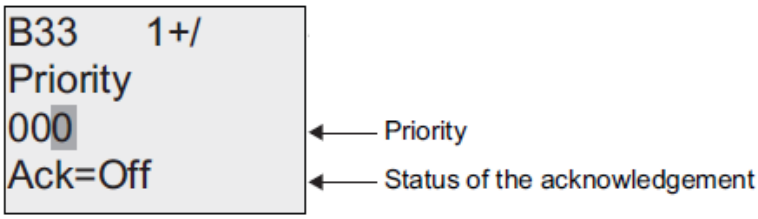
Display field of xLogic in RUN mode



Input P configuration

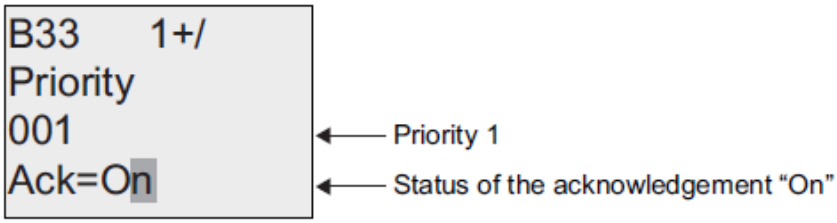
From the input P, you configure the following characteristics of the message text:

- Priority
- Acknowledgement
- Message destination



1. Increase the priority to 1: Cursor on '0' + ▲
2. Change to 'Ack': Press ▶
3. Enable 'Ack': Press ▲ or ▼

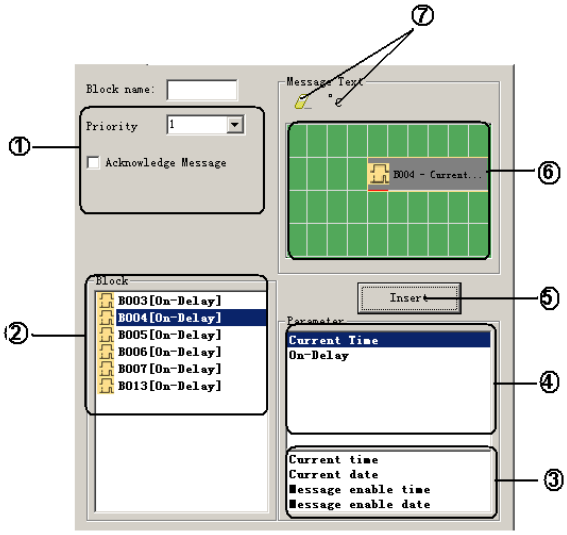
xLogic shows:



Restrictions

Up to 64 message text functions are available for ELC-12 cpus and 32 message text functions are available for ELC-18 cpus.

Particular characteristics to be noted when configuring



To arrange the message text

1 **"General" area**

Here you will find the following settings:·

- Priority of the message text
- Check box for message text acknowledgement

2 **"Blocks" area**

Shows a list of all the circuit program blocks and their parameters.

3 **"General parameters" area**

Shows general parameters such as the current date.

4 **"Block parameters" area**

Shows the parameters of a block selected from the "Blocks" area which you can output in the message text.

5 **"Insert" button**

Button for inserting a parameter selected from the "Block parameters" area.

"Block parameters" or **"General parameters"** area into the message text.

6 **"Messages" area**

You arrange the message text in this area. Information entered in this area corresponds with that on the xLogic display.

7 **"Delete" button**

Button for deleting entries from the **"Messages"** area

"Special characters" button

Button for inserting special characters in the **"Messages"** area

1. From the **"Blocks"** area, select the block whose parameters you want to output.

2. Drag and drop the parameters required from the **"Block parameters"** to the **"Messages"** area. You may also use the **"Insert"** button to do so.

3. In the **"Messages"** area, you can add parameter data as required.

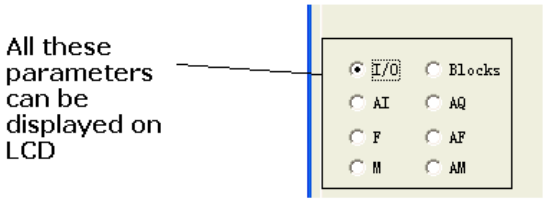
Particular characteristics to be noted when configuring

The message text can be configured in the block properties dialog. You can enter up to 4 lines for each message text (the text display of the xLogic has 4 x 10 characters) and set the priority. You can move to the next line using the cursor keys or the mouse. Hit the [ENTER] key to confirm all your entries in the block properties dialog and to close the dialog.

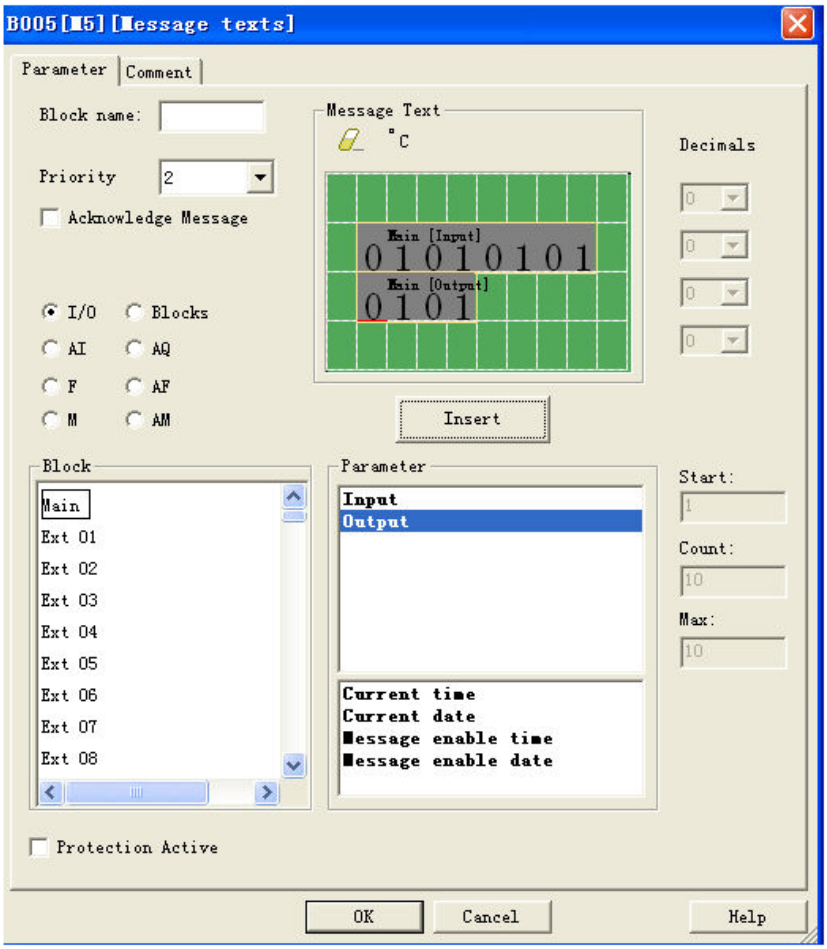
You may also enter the actual values of other blocks in the text lines. To do so, select the relevant block from the Block dialog. A Parameter dialog opens to display a list of all parameters available for the selected block. The block parameter you select in this dialog is written to the selected text line. The actual parameter value is now included when you call the message text.

Set the "Acknowledge message" attribute to specify whether a message is be acknowledged before it is closed.

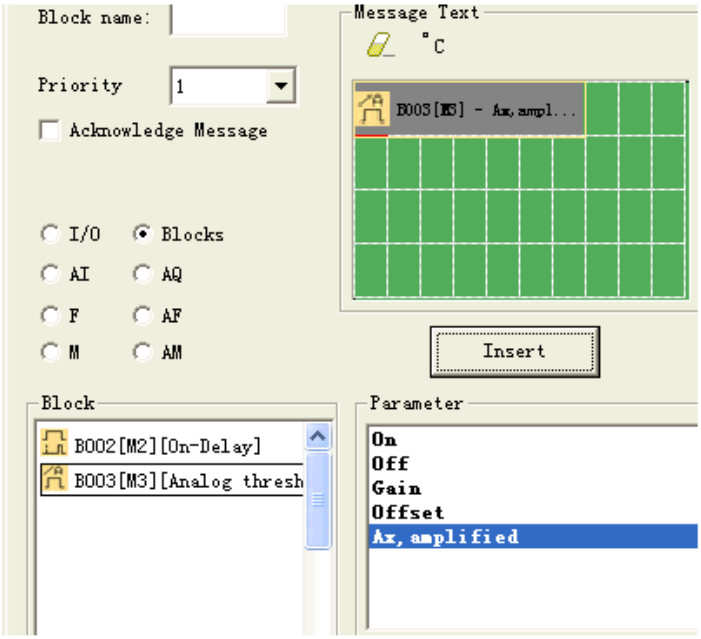
New feature applied to ELC-12 &Upgraded ELC-18 CPU.



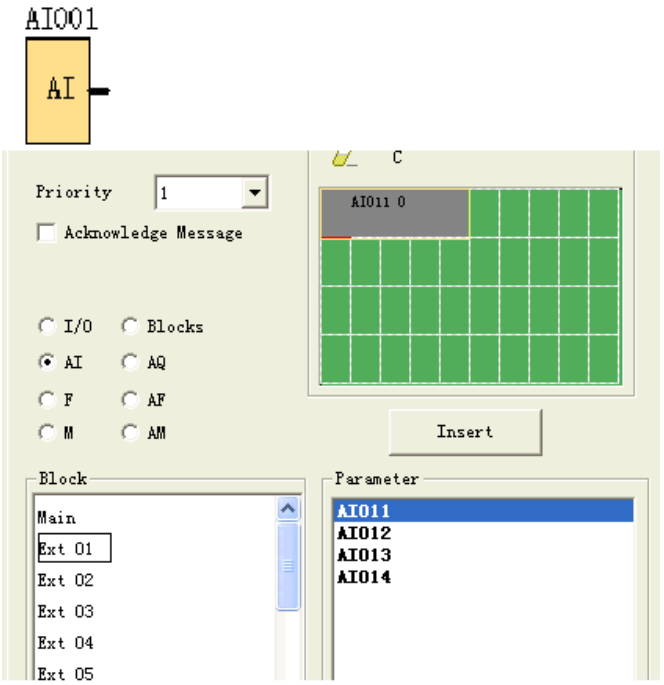
A. I/O status of CPU and extensions


B005 [M5] [Message texts]
Parameter
Comment
Block name:
Priority
2
Acknowledge Message
I/O
Blocks
AI
AQ
F
AF
M
AM
Message Text
°C
Main [Input]
01010101
Main [Output]
0101
Insert
Decimals
0
0
0
0
Block
Main
Ext 01
Ext 02
Ext 03
Ext 04
Ext 05
Ext 06
Ext 07
Ext 08
Parameter
Input
Output
Current time
Current date
Message enable time
Message enable date
Start:
1
Count:
10
Max:
10
Protection Active
OK
Cancel
Help

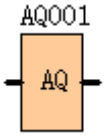
B .Blocks

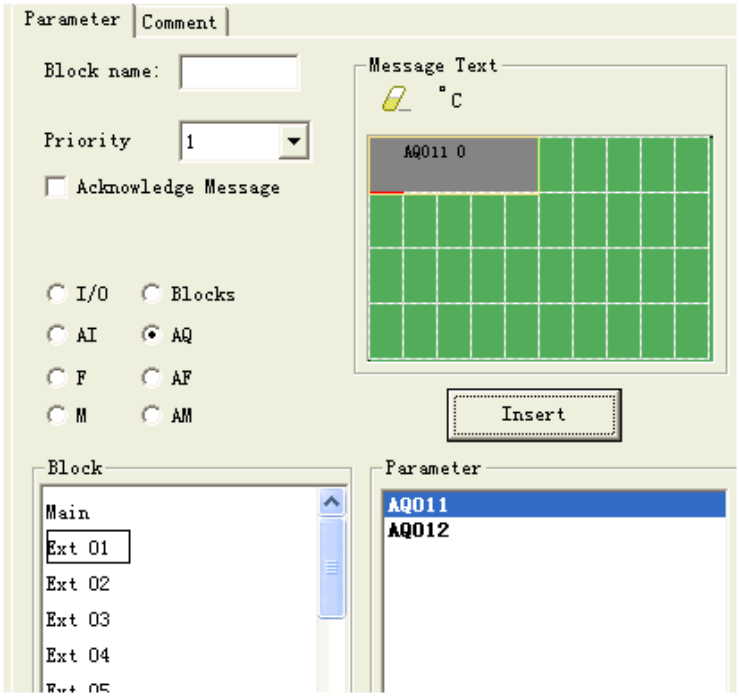


C. Analog input value of ELC-12 CPU and extensions

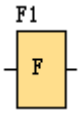


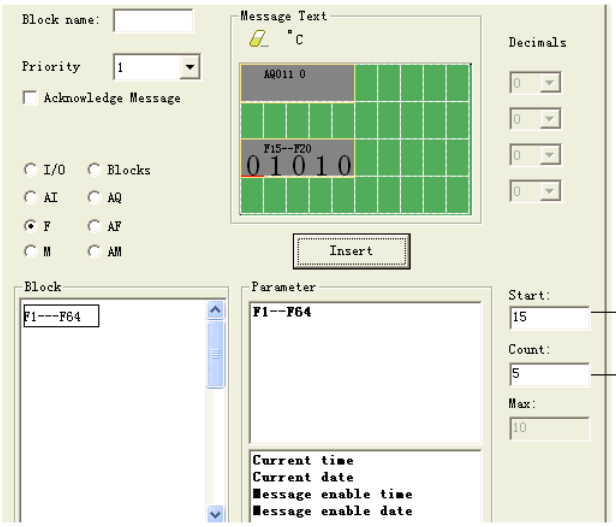
D. Analog output value of CPU and extensions



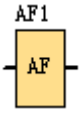


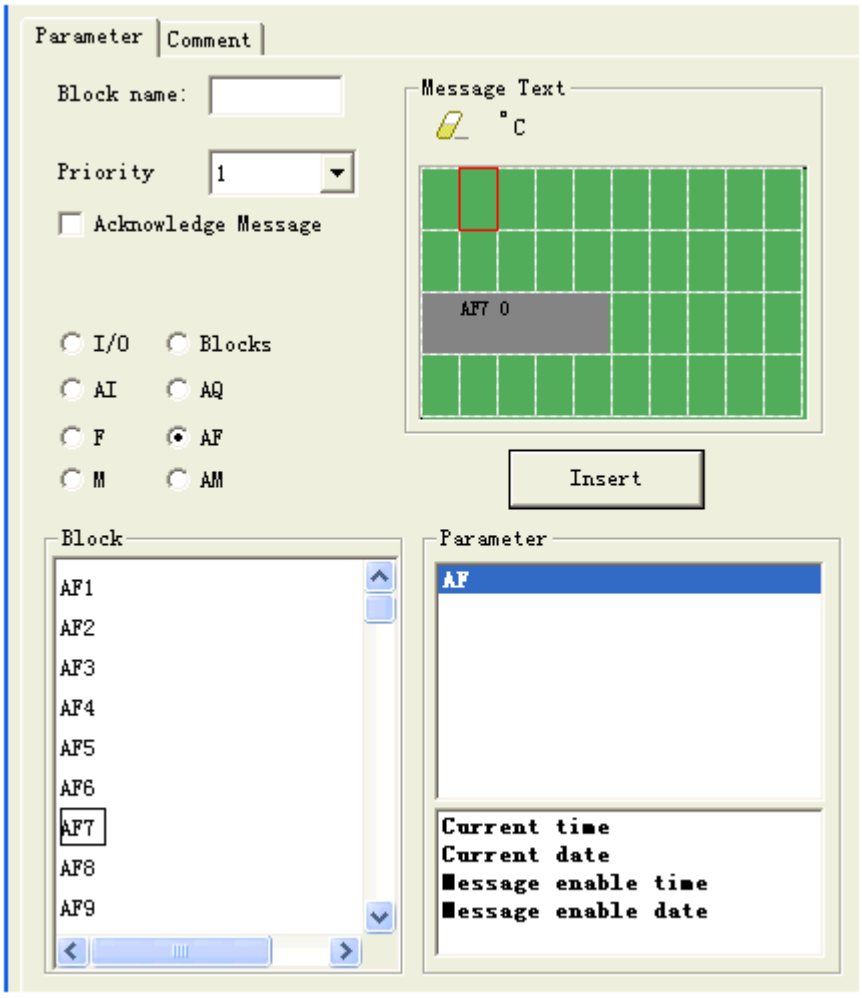
E. F (digital flag) status



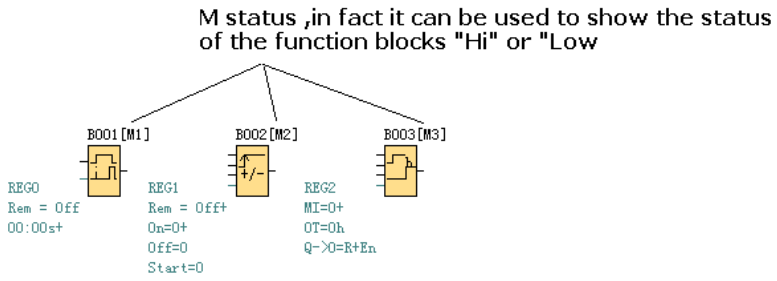


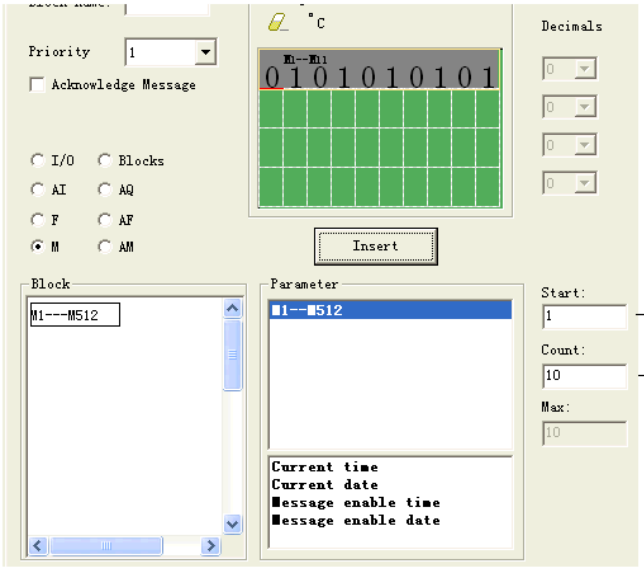
F. AF(analog flag) value


AF1
AF


Parameter
Comment
Block name:
Priority
1
Acknowledge Message
Message Text
°C
AF7 0
I/O
Blocks
AI
AQ
F
AF
M
AM
Insert
Block
AF1
AF2
AF3
AF4
AF5
AF6
AF7
AF8
AF9
Parameter
AF
Current time
Current date
Message enable time
Message enable date

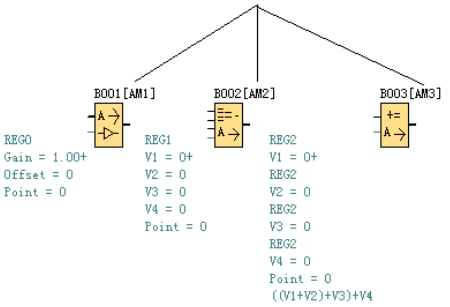
G. M status

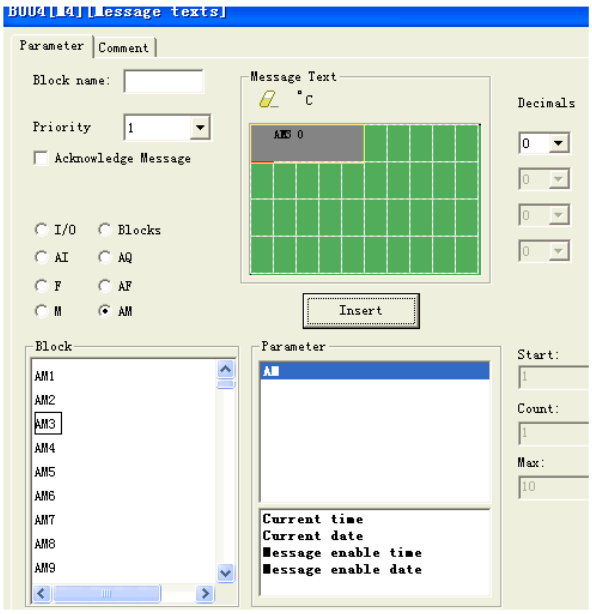

M status ,in fact it can be used to show the status
of the function blocks "Hi" or "Low
BOO1[M1]
BOO2[M2]
BOO3[M3]
REG0
Rem = Off
00:00s+
REG1
Rem = Off+
On=0+
Off=0
Start=0
REG2
MI=0+
OT=0h
Q->0=R+En



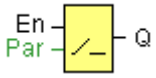
H. AM value

AM value ,it can be used to display the current value of analog blocks which has analog output pin .





4.4.19 Softkey



Short description

This SFB provides the action of a mechanical pushbutton or switch.

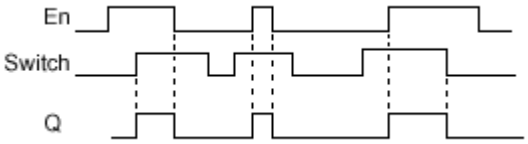
Connection	Description
Input **En**	Output Q is set with a 0 to 1 signal transition at input En (Enable) and if, in addition, 'Status=On' has been confirmed in configuration mode.
Parameter	**Type:** Sets either a pushbutton action for one cycle or a switching action of the function. **Status**: On or Off state that is applied in the initial cycle after program startup, is retentivity is not set. **Retentivity** set (on) = the status is retentive in memory.
Output **Q**	Output Q remains set 1, as long as En=1 and the status at the parameter Type = Switch and Status = On. Output Q is set for the duration of one cycle if EN=1 and the status at the parameters Type = momentary (pushbutton) and Status = On.

The status of this switch can be changed momentarily in parameter mode. For information about how to modify, refer to chapter 5.2.2 please.

Factory state

Default of 'Type' is 'momentary action switch'.

Timing diagram



Description of the function

The output is set, when input En is set and the 'Status' parameter is set to 'On' and confirmed with OK. This action is performed irrespective of a configured switch or pushbutton function.

The output is reset to '0' in the following threes cases:

- With a 1 to 0 signal transition at input En.
- When a pushbutton function is configured and one cycle has expired after its actuation.
- When the 'Status' parameter sets the 'Off' status in configuration mode, and this has been confirmed with OK.

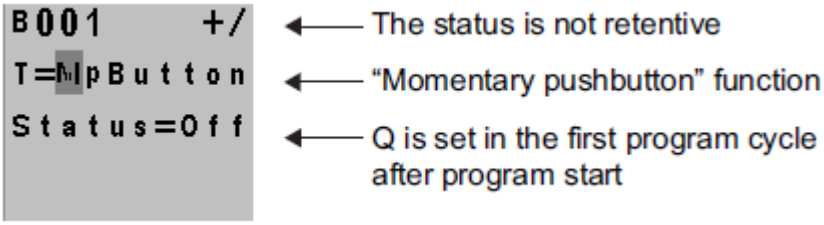
Particular characteristics to be noted when configuring

The softkey can be used both with momentary pushbutton or switching action. At the status parameter you can define the on (actuated) or off state for the switch/pushbutton.

If the softkey is assigned a pushbutton action, the output is always set for the duration of one cycle with a 0 to 1 transition at input En when the pushbutton is in on state, or if the pushbutton state changes from Off to On when En=1.

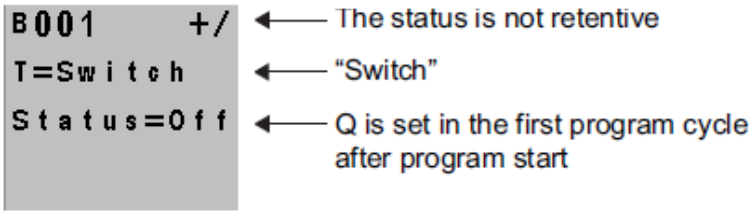
Setting the Par parameter
View in programming mode (example):
1. Select the 'Softkey' function.
2. Select input En and confirm with **OK**. The cursor is now positioned below 'Par'.
3. Change to the input mode of 'Par': Confirm with **OK**

(the cursor is now positioned to 'Off')



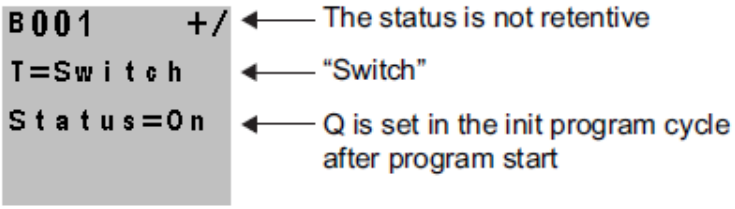
To change 'Par' to 'Switch' action and the initialization status after the program start:

4. To select 'Momentary pushbutton' or 'Switch' action: Press ▲ or ▼.

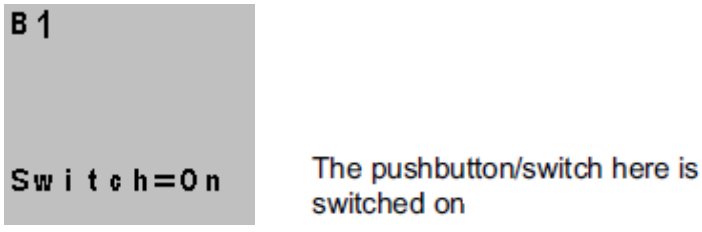


5. To change to the start state: Press ◀ or ▶

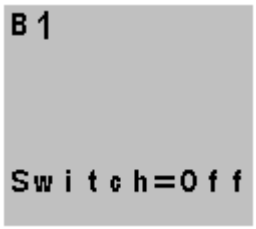
6. To change the start state: Press ▲ or ▼



7. Confirm your entries with OK
View in parameter assignment mode (example):
Here, you can set or reset the 'Switch' parameter (On/Off). When in RUN, xLogic shows the following display:

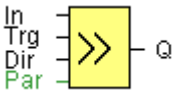


Let us assume you want to set 'Switch' (Off).
1. Change to the editing mode: Confirm with OK (the cursor is now positioned on 'On')
2. To change from 'On' to 'Off': Press ▲ or ▼
3. Confirm your entries with Press OK



The pushbutton/switch here is switched off

4.4.20 Shift register

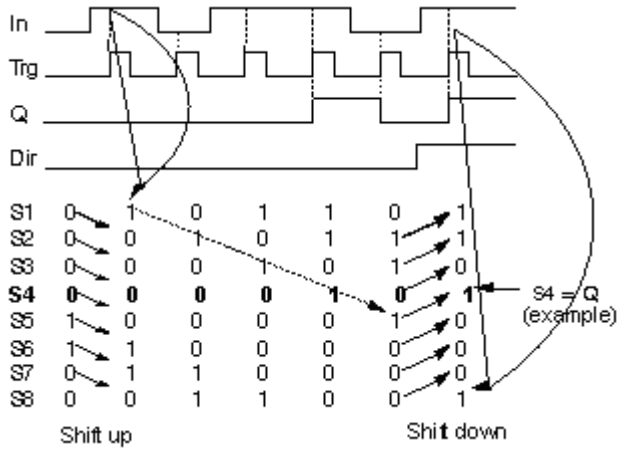


Short description

The shift register function can be used to read an input value and to shift the bits. The output value corresponds with the configured shift register bit. The shift direction can be changed at a special input.

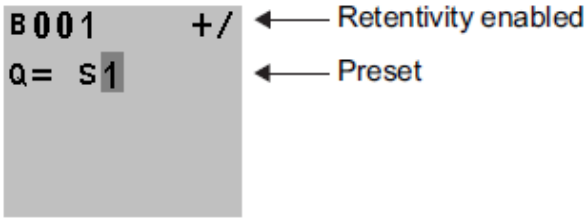
Connection	Description
Input **In**	The function when started reads this input value.
Input **Trg**	The SFB is started with a positive edge (0 t 1 transition) at input Trg (Trigger). A 1 to 0 transition is irrelevant.
Input **Dir**	You define the shift direction of the shift register bits S1...S8 at the Dir input: Dir = 0: shift up (S1 >> S8) Dir = 1: shift down (S8 >> S1)
Parameter	**Shift register bit** that determines the value of output Q. Possible settings: S1 ... S8 **Retentivity** set (on) = the status is retentive in memory.
Output **Q**	The output value corresponds with the configured shift register bit.

Timing diagram

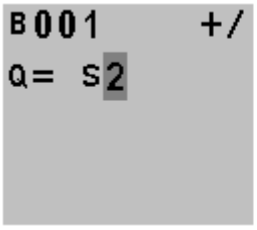


Setting the Par parameter

View in programming mode:



Press ▲



etc. You can select S1... S8

This special function is not available in parameter assignment mode.

Description of the function

The function reads the value of input In with a positive edge (0 to 1 transition) at input Trg (Trigger).

This value is written to shift register bits S1 or S8, depending on the set shift direction:

- Shift up: S1 accepts the value of input In; the previous value of S1 is shifted to S2, S2 is shifted to S3, etc.
- Shift down: S8 accepts the value of input In; the previous value of S8 is shifted to S7, S7 is shifted to S6, etc.

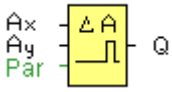
Q outputs the value of the configured shift register bits.

If retentivity is not enabled, the shift function restarts at S1 or S8 after a power failure.

Note

The special function shift register can be used only once in the circuit program.

4.4.21 Analog comparator



Short description

The output is set and reset depending on the difference Ax - Ay and on two configurable thresholds.

Connection	Description
Inputs **Ax, Ay**	Input the analog signals of which you want to determine the delta at the inputs Ax and Ay. Use the analog inputs AI1...AI8, the analog outputs AQ1 and AQ2. AI1..AI8: 0 - 10 V corresponds with 0 - 1000 (internal value).
Parameter	**A:** Gain Range of values: ± 10.00 **B:** Zero offset Range of values: ± 10,000 **On**: On threshold Range of values: ± 20,000 **Off:** Off threshold Range of values: ± 20,000 **p:** Number of decimals Range of values: 0, 1, 2, 3
Output **Q**	Q is set or reset depending on the set thresholds.

Parameter p (number of decimals)

The on threshold On and the off threshold Off can be provided by the actual value of another already-programmed function:

Analog comparator: Ax – Ay

Analog trigger: Ax

Analog amplifier: Ax

Analog multiplexer: AQ

Analog ramp: AQ

Analog math: AQ

PI controller:AQ

Data latching relay: AQ

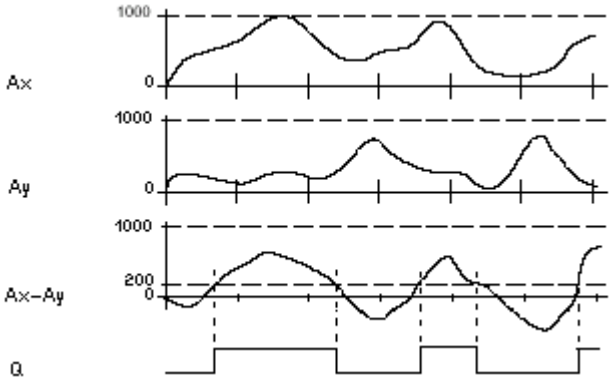
Up/Down counter: Cnt

Applies only to Ax, Ay, Delta, On and Off values displayed in a message text.

Does not apply to the comparison of on and off values! (The compare function ignores the decimal point.)

The value of " On " , " Off " and " Dec " can be set/modified in parameter mode. For information about how to set/modify, refer to chapter 5.2.2 please.(Dec means decimal point.)

Timing diagram



Q for Ax - Ay > 200, On = Off = 200

Description of the function

The function reads the value of the signal at the analog input Ax.

This value is multiplied by the value of parameter A (gain). Parameter B (offset) is added to the product, hence

(Ax · gain) + offset = Actual value Ax.

(Ay · gain) + offset = Actual value Ay.

Output Q is set or reset depending on the difference of the actual values Ax - Ay and the set thresholds. See the following calculation rule.

Calculation rule

- If threshold On ≥Threshold Off, then:

Q = 1, if (actual value Ax - actual value Ay) > On

Q = 0, if (actual value Ax - actual value Ay) ≤Off.

- If threshold On < Threshold Off, then Q = 1, falls:

On ≤ (actual value Ax - actual value Ay) < Off.

Reducing the input sensitivity of the analog comparator

You can delay the output of the analog comparator selectively by means of the "on delay" and "off delay" SFBs. By doing so, you determine that output Q is only set if the input trigger length Trg (= output of the analog comparator) exceeds the defined on delay time.

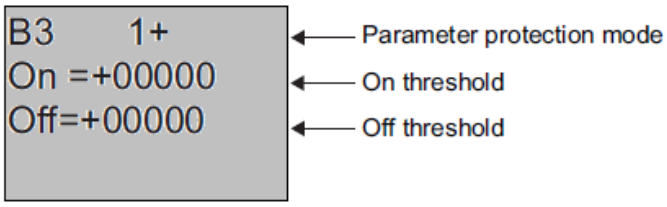
This way you can set a virtual hysteresis, which renders the input less sensitive to short changes.

Particular characteristics to be noted when configuring

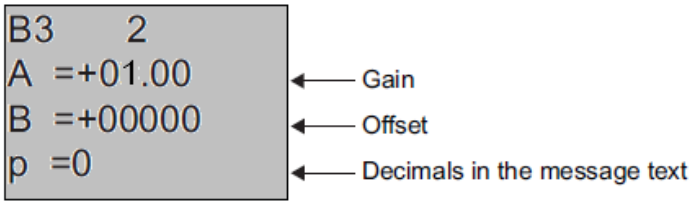
For help on analog block parameters, refer to the Analog value processing section.

Setting the Par parameter
The gain and offset parameters are used to adapt the sensors to the relevant application.

View in programming mode:



Press ▶

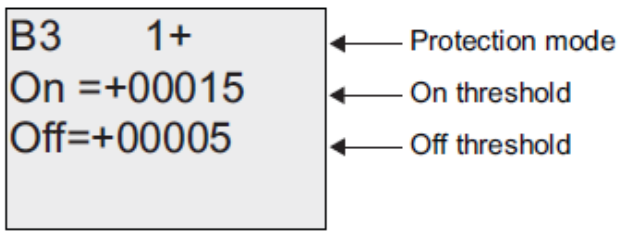


Example
In a heating control system, the supply Tv and return line temperatures Tr are to be compared, for example with a sensor at AI2.
A control signal is to be triggered (for example "heater On") when the difference between the supply and return line temperatures is greater than 15 °C. The control signal is reset when the difference is less than 5 °C.
The process variable of the temperature is to be shown in parameter assignment mode.

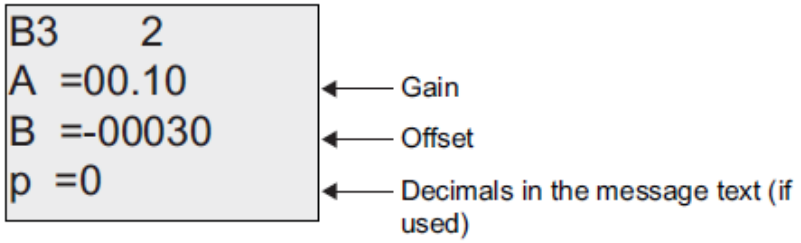
The thermocouples available have the following technical data: -30 to +70 °C, 0 to 10 VDC.

Application	Internal mapping
-30 to +70 °C = 0 to 10 V DC	0 to 1000
0 °C	300 → Offset = -30
Range of values: -30 to +70 °C = 100	1000 → Gain = 100/1000 = 0.1
On threshold = 15 °C	Threshold = 15
Off threshold = 5 °C	Threshold = 5

Configuration (example):



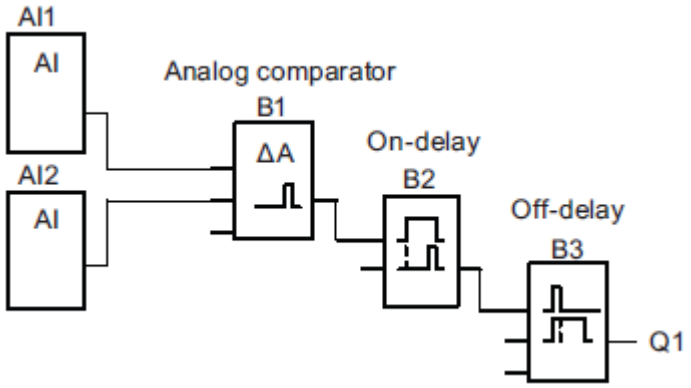
Press ▶



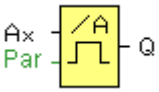
Reducing the input response of the analog comparator
You can selectively delay the output of an analog comparator by means of the "On-delay" and "Off-delay" special functions. With on-delay, output Q is only set if the pulse width of the

triggering signal at input Trg (=analog comparator output) is longer than the on-delay time. Using this method, you will obtain a virtual hysteresis and reduce the input response to short signals.

Function block diagram



4.4.22 Analog threshold trigger



Short description

The output is set or reset depending on two configurable thresholds (hysteresis).

Connection	Description
Input **Ax**	Input the analog signal to be evaluated at input Ax. Use the analog inputs AI1...AI8, the analog outputs AQ1 and AQ2. 0 - 10 V is proportional to 0 - 1000 (internal value).
Parameter	**A:** Gain Range of values: ± 10.00 **B:** Zero offset Range of values: ± 10,000 **On:** On threshold Range of values: ±20,000 **Off:** Off threshold Range of values: ± 20,000 **p:** Number of decimals Range of values: 0, 1, 2, 3
Output **Q**	Q is set or reset depending on the set thresholds.

Parameter On and Off

The On and Off parameters can be provided by the actual value of another already-programmed function:

Analog comparator: Ax – Ay

Analog trigger: Ax

Analog amplifier: Ax

Analog multiplexer: AQ

Analog ramp: AQ

Analog math: AQ

PI controller:AQ

Data latching relay: AQ

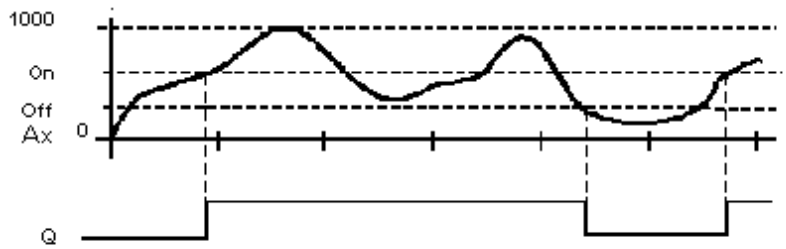
Up/Down counter: Cnt

Applies only to the display of On, Off and Ax values in a message text.

Does not apply to the comparison of On and Off values! (The compare function ignores the decimal point.)

The value of " On " , " Off " and " Dec " can be set/modified in parameter mode. For information about how to set/modify, refer to chapter 4.2.2 please.(Dec means decimal point.)

Timing diagram



Description of the function

The function reads the value of the signal at the analog input Ax.

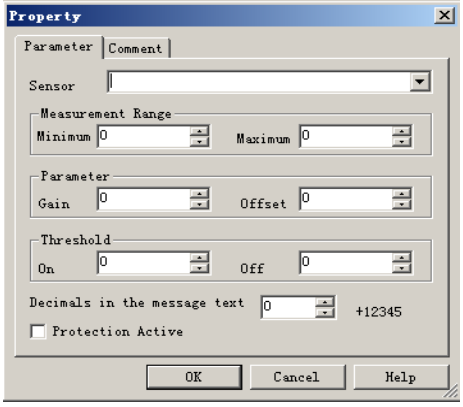
This value is multiplied by the value of parameter A (gain). Parameter B (offset) is added to the product, hence

(Ax ·Gain) + Offset = Actual value Ax.

Output Q is set or reset depending on the set threshold values. See the following calculation rule.

Calculation rule

- If threshold (On) ≥ threshold (Off), then:
  Q = 1, if the actual value Ax > On
  Q = 0, if the actual value Ax ≤Off.
- If threshold (On) < threshold (Off), then Q = 1, if
  On ≤ the actual value Ax < Off.

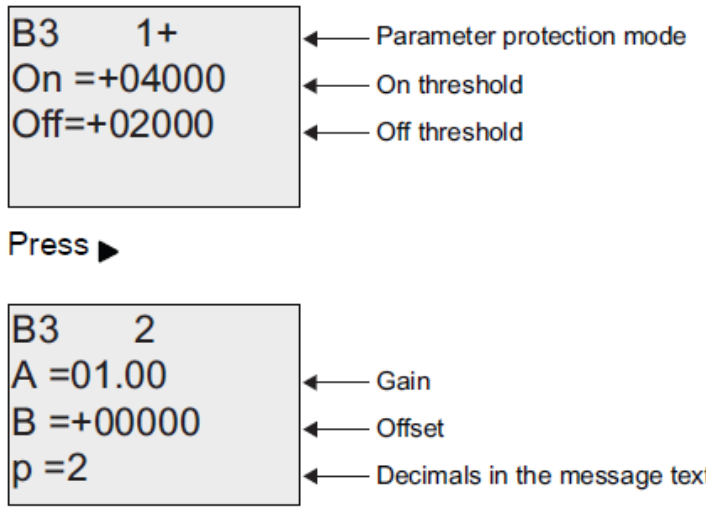


Note

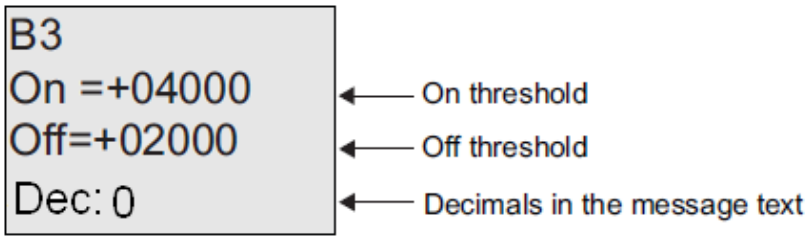
The decimal point setting must be identical in the min. and max. range.

Setting the Par parameter

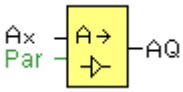
The gain and offset parameters are used to adapt the sensors to the relevant application.
View in programming mode (example):



View in parameter assignment mode (example):



4.4.23 Analog amplifier



Short description

This SFB amplifies an analog input value and returns it at the analog output.

Connection	Description
Input **Ax**	Input the analog signal to be amplified at input Ax. Use the analog inputs AI1...AI8, the analog outputs AQ1 and AQ2. AI1..AI8: 0 - 10 V corresponds with 0 - 1000 (internal value).
Parameter	**A:** Gain Range of values: ± 10.00 **B:** Zero offset Range of values: ± 10000 **p:** Number of decimals Range of values: 0, 1, 2, 3
Output A**Q**	Analog output Value range for AQ: -32768...+32767

Parameter p (number of decimals)

Applies only to the display of Ax and Ay values in a message text.

Does not apply to the comparison of On and Off values! (The compare function ignores the decimal point.)

Description of the function

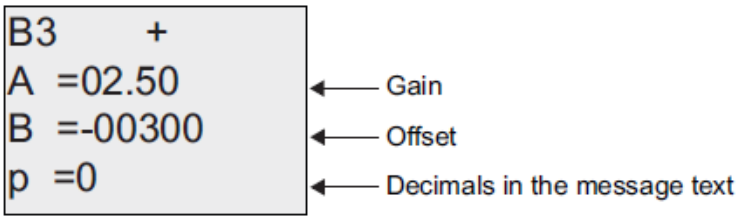
The function reads the value of an analog signal at the analog input Ax.

This value is multiplied by the gain parameter A. Parameter B (offset) is added to the product, i.e.

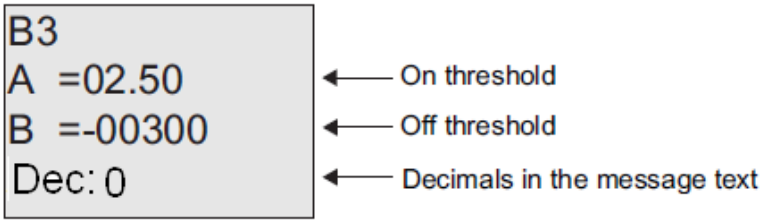
(Ax ·gain) + offset = Actual value Ax.

Setting the Par parameter
The gain and offset parameters are used to adapt the sensors to the relevant application.

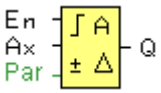
View in programming mode (example):



View in parameter assignment mode (example):



4.4.24 Analog value monitoring



Short description

This special function saves the process variable of an analog input to memory, and sets the output when the output variable exceeds or drops below this stored value plus a configurable offset.

Connection	Description
Input **En**	A positive edge (0 to 1 transition) at input En saves the analog value at input Ax ("Aen") to memory and starts monitoring of the analog range Aen ± Delta.
Input **Ax**	You apply the analog signal to be monitored at input Ax. Use the analog inputs AI1...AI8, the analog outputs AQ1 and AQ2. 0 - 10 V is proportional to 0 - 1000 (internal value).
Parameter	**A:** Gain Range of values: ± 10.00 **B:** Zero offset Range of values: ± 10,000 **Delta:** Difference value for the Aen on/off threshold Range of values: ± 20,000 **p:** Number of decimals Range of values: 0, 1, 2, 3
Output **Q**	Q is set/reset, depending on the stored analog value and the offset.

Parameter p (number of decimals)

The two threshold parameters Threshold 1 and Threshold 2 can be provided by the actual value of another already-programmed function:

Analog comparator: Ax – Ay

Analog trigger: Ax

Analog amplifier: Ax

Analog multiplexer: AQ

Analog ramp: AQ

Analog math: AQ

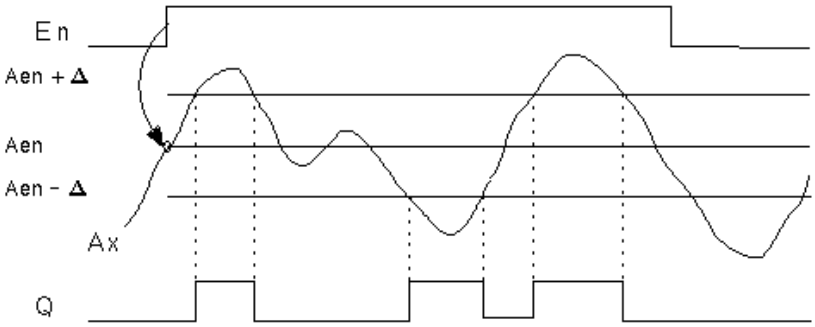
PI controller:AQ

Data latching relay: AQ

Up/Down counter: Cnt

Applies only to the display of Aen, Ax and Delta values in a message text.

Timing diagram



Description of the function

A 0 to 1 transition at input En saves the value of the signal at the analog input Ax. This saved process variable is referred to as Aen".

Both the analog actual values Ax and Aen are multiplied by the value at parameter A (gain), and parameter B (offset) is then added to the product, i.e.

(Ax · gain) + offset = Actual value Aen, when input En changes from 0 to 1, or

(Ax · gain) + offset = Actual value Ax.

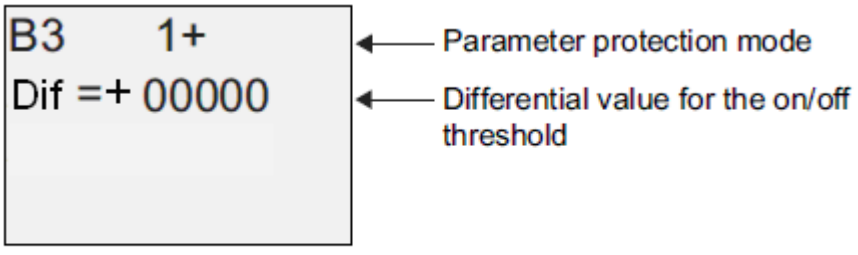
Output Q is set when the signal at input En = 1 and if the actual value at input Ax is out of range of Aen ± Delta.

Output Q is reset, when the actual value at input Ax lies within the range of Aen +- Delta, or when the signal at input En changes to lo.

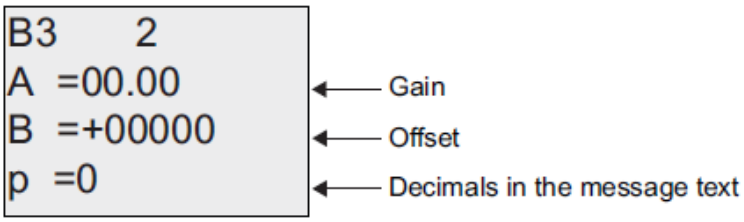
Setting the Par parameter

The gain and offset parameters are used to adapt the used sensors to the respective application.

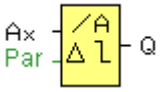
View in programming mode:



Press ▶



4.4.25 Analog differential trigger



Short description

The output is set and reset depending on a configurable threshold and a differential value.

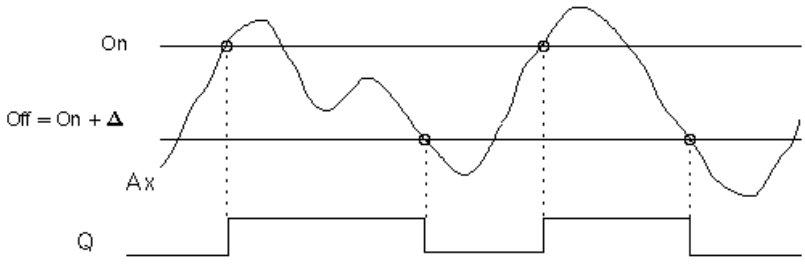
Connection	Description
Input **Ax**	You apply the analog signal to be analyzed at input Ax. Use the analog inputs AI1...AI8, the analog outputs AQ1 and AQ2. 0 - 10 V is proportional to 0 - 1000 (internal value).
Parameter	**A:** Gain Range of values: ± 10.00 **B:** Zero offset Range of values: ± 10,000 **On**: On threshold Range of values: ±20,000 **Delta:** Differential value for calculating the off parameter Range of values: ± 20,000 **p:** Number of decimals Range of values: 0, 1, 2, 3
Output **Q**	Q is set or reset, depending on the threshold and difference values.

Parameter p (number of decimals)

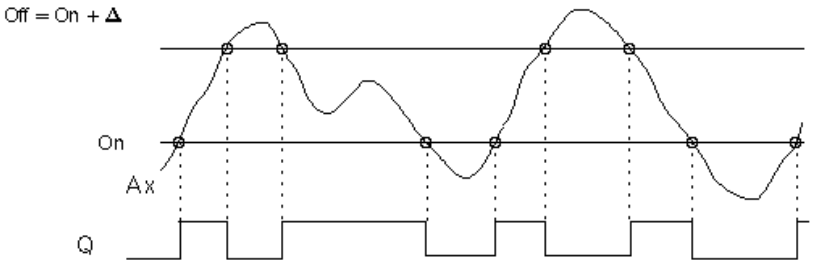
Applies only to the display of On, Off and Ax values in a message text.

Does not apply to the comparison of On and Off values! (The compare function ignores the decimal point.)

Timing diagram A: Function with negative difference Delta



Timing diagram B: Function with positive difference Delta



Description of the function

The function fetches the analog signal at input Ax.

Ax is multiplied by the value of the A (gain) parameter, and the value at parameter B (offset) is added to product, i.e.

(Ax · gain) + offset = actual value of Ax.

Output Q is set or reset, depending on the set (On) threshold and difference value (Delta). The function automatically calculates the Off parameter: Off = On + Delta, whereby Delta may be positive or negative. See the calculation rule below.

Calculation rule

- When you set a negative differential value Delta, the On threshold >= Off threshold, and:

Q = 1, if the actual value Ax > On

Q = 0, if the actual value Ax ≤ Off.

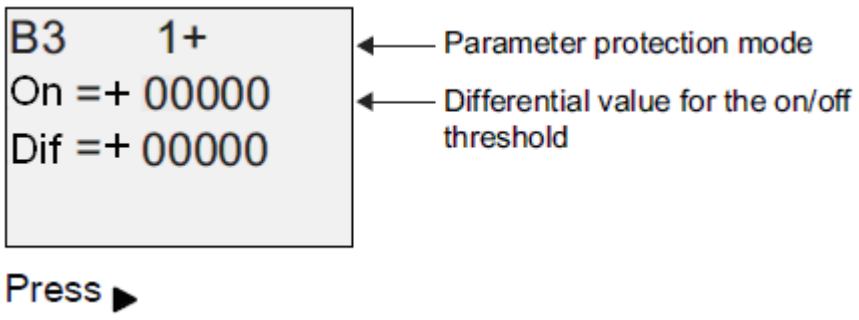
See the timing diagram A.

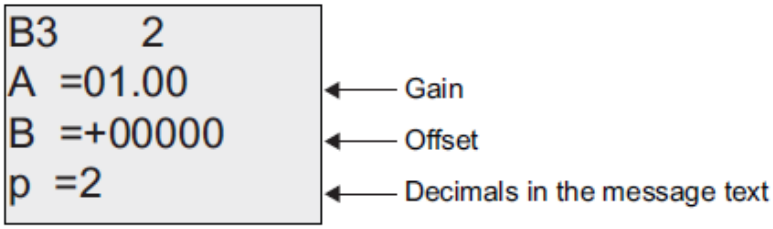
- When you set a positive differential value Delta, the On threshold < the Off threshold, and Q = 1, if:

On ≤ the actual value Ax < Off.

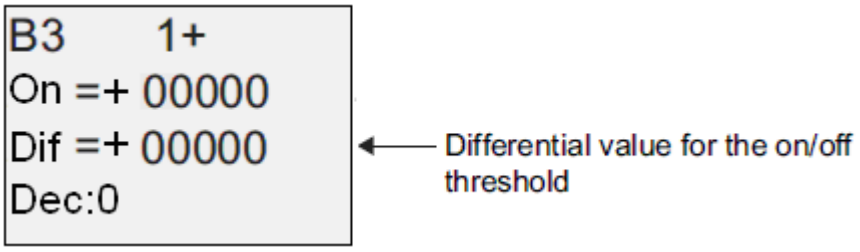
See the timing diagram B.

Setting the Par parameter
The gain and offset parameters are used to adapt the sensors to the relevant application.
View in programming mode (example):

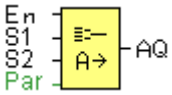




View in parameter assignment mode (example):



4.4.26 Analog multiplexer



Short Description

This special function displays 0 or one of 4 saved analog values on the analog output.

Connection	Description
Input **En**	1 on input En (Enable) switches, dependent on S1 and S2, a parameterized analog value to the output AQ. 0 on input EN switches 0 to the output AQ.
Inputs **S1** and **S2**	S1 and S2 (selectors) for selecting the analog value to be issued. S1 = 0 and S2 = 0: The value 1 is issued S1 = 0 and S2 = 1: The value 2 is issued S1 = 1 and S2 = 0: The value 3 is issued S1 = 1 and S2 = 1: The value 4 is issued
Parameter	**V1...V4**: Analog values (Value) that will be issued. Value range: -32768...+32767 **p**: Number of decimal places value range: 0, 1, 2, 3
Output **AQ**	Analog output Value range for AQ: -32768...+32767

Parameters V1…V4

The values for V1…V4 can be provided by the value of another already-programmed function:

Analog comparator: Ax – Ay

Analog trigger: Ax

Analog amplifier: Ax

Analog multiplexer: AQ

Analog ramp: AQ

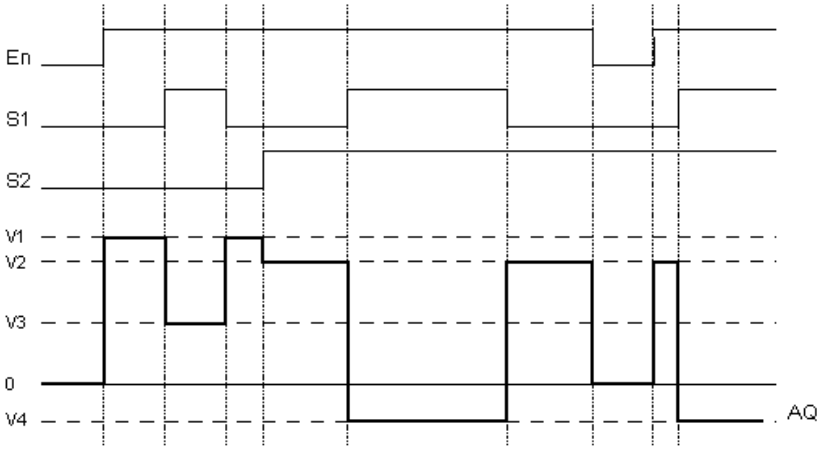
Analog math: AQ

PI controller:AQ

Data latching relay: AQ

Up/Down counter: Cnt

Timing Diagram



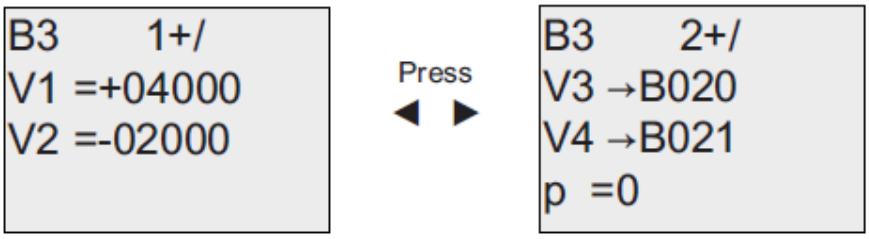
Description of Function

If input En is set, then the function issues one of 4 possible analog values V1 to V4 at the output AQ, depending on the parameters S1 and S2.

If the input En is not set, then the function issues the analog value 0 at output AQ.

Analog output
If you interconnect this special function with a real analog output, note that the analog output can only process values between 0 and 1000. To do this, you may need to connect an additional amplifier between the analog output of the special function and the real analog output. Using this amplifier, you standardize the output range of the special function to a value range of 0 to 1000.

Setting the Par parameter
View in programming mode (example):

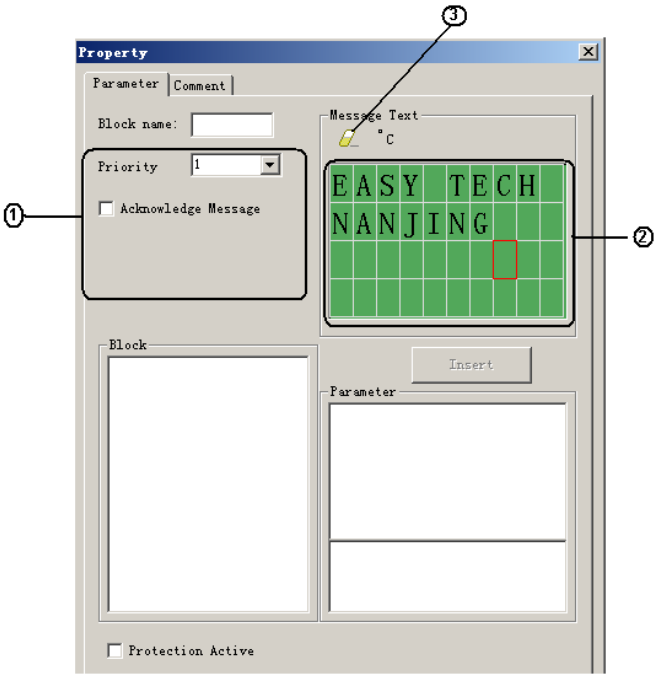


4.5 **HMI** (Human Machine Interface) **Block**

4.5.1 System cover

This block cannot directly be found in the block list ,however, it is set as default by system of xLogic, hence system cover can be available if you follow the below procedures : use your mouse to left-click "Tools" menu->select "Edit Cover HMI" by left-click in xLogicsoft .

Short description
Display the status (Run or Stop) of xLogic when power-on or simulation by soft.
Particular characteristics to be noted when configuring



1."General" area （not available）

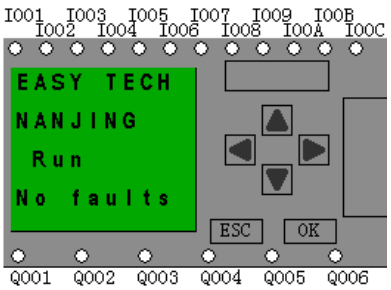
Here you will find the following settings:

Priority of the system cover

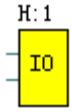
2. "Messages" area
Users can edit the messages in the first and second line, the third line displays the state RUN or STOP, and the messages saying whether your program has mistakes or not will be shown in the fourth line.

3. "Delete" button

Button for deleting the "Messages" in the first and second line.



4.5.2 System input/output

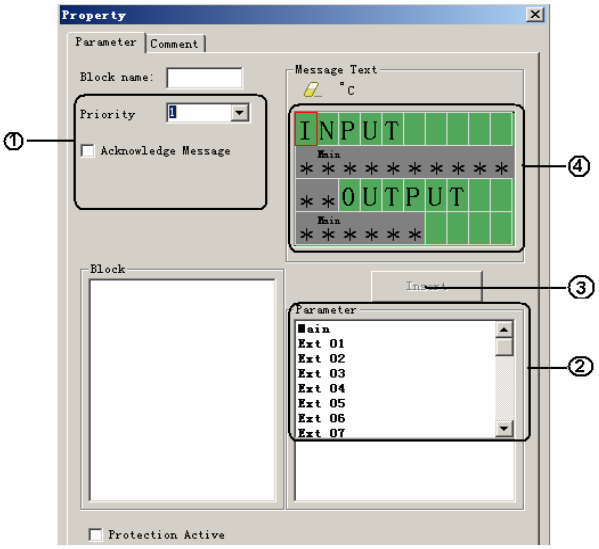


Only apply in ELC-18 CPUs

Short description

Display the status of the input and output of the main and expansion modules.

Particular characteristics to be noted when configuring



1. "General" area

Here you will find the following settings:

Priority of the system input and output.

2. "Blocks" area

Displaying all extendable and main modules can be inserted

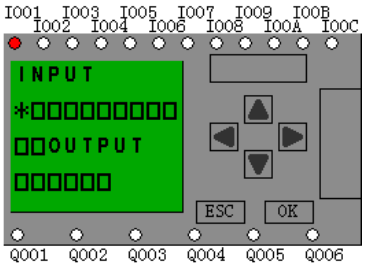
3. "Insert" button

Button for inserting the selected blocks to the "Message" area.

4. "Message" area

Display input and output of the blocks you inserted.

The following figure would be shown when xLogic is power-on or under simulation status in soft.



Note:

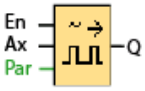
✻ represents high pulse, ☐ represents low pulse.

Description of the priority of HMI blocks:

If several HMI blocks are placed in the program, the messages in the respective block would be displayed according to priority level (1 = lowest, 32 = highest). After a message is disabled or acknowledged, the function automatically shows the previously active message text that takes the highest priority, but you can change the human machine interface via press the Left and Right key.

Note: IO status can be viewed by click "left" or "right" button after the "system IO" block had put in your program.

4.6 Pulse Width Modulator (PWM)



Short Description:

The Pulse Width Modulator (PWM) instruction modulates the analog input value Ax to a pulsed digital output signal. The pulse width is proportional to the analog value Ax.

connection	**Description**
EN	A positive edge (0 to 1 transition) at input En enables the PWM function block.
Input **Ax**	Analog signal to be modulated to a pulsed digital output signal.
parameter	**A:** Gain Range of values: +- 10.00 **B:** Zero offset Range of values: +- 10,000 **PT:** Periodic time over which the digital output is modulated **p:** Number of decimals Range of values: 0, 1, 2, 3
Output **Q**	Q is set or reset for the proportion of each time period according to the proportion of the standardized value Ax to the analog value range.

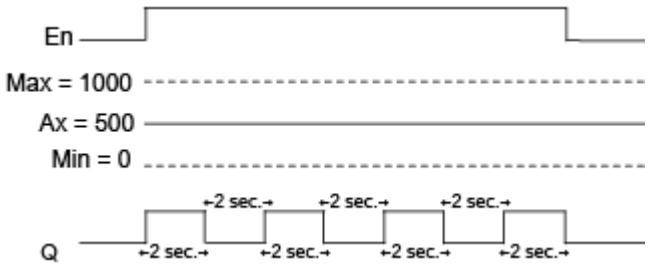
Parameter PT
The periodic time PT can be provided by the actual value of another already-programmed function:
Analog comparator: Ax – Ay
Analog trigger: Ax
Analog amplifier: Ax
Analog multiplexer: AQ
Analog ramp: AQ
PI controller:AQ
Analog math: AQ
Data latching relay: AQ
Up/Down counter: Cnt

Parameter p (number of decimals)
Parameter p applies only to the display of the Ax value in a message text.

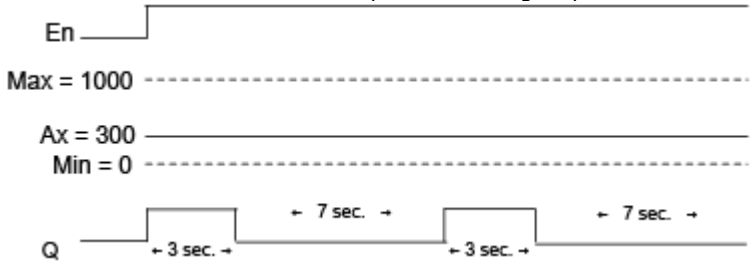
Description of the function
The function reads the value of the signal at the analog input Ax.
This value is multiplied by the value of parameter A (gain). Parameter B (offset) is added to the product, as follows:
(Ax * Gain) + Offset = Actual value Ax
The function block calculates the proportion of the value Ax to the range. The block sets the digital output Q high for the same proportion of the PT (periodic time) parameter, and sets Q low for the remainder of the time period.

Examples with Timing Diagrams
The following examples show how the PWM instruction modulates a digital output signal from the analog input value:
Example 1
Analog input value: 500 (range 0...1000)
Periodic time T: 4 seconds

The digital output of the PWM function is 2 seconds high, 2 seconds low, 2 seconds high, 2 seconds low and continues in that pattern as long as parameter "En" = high.



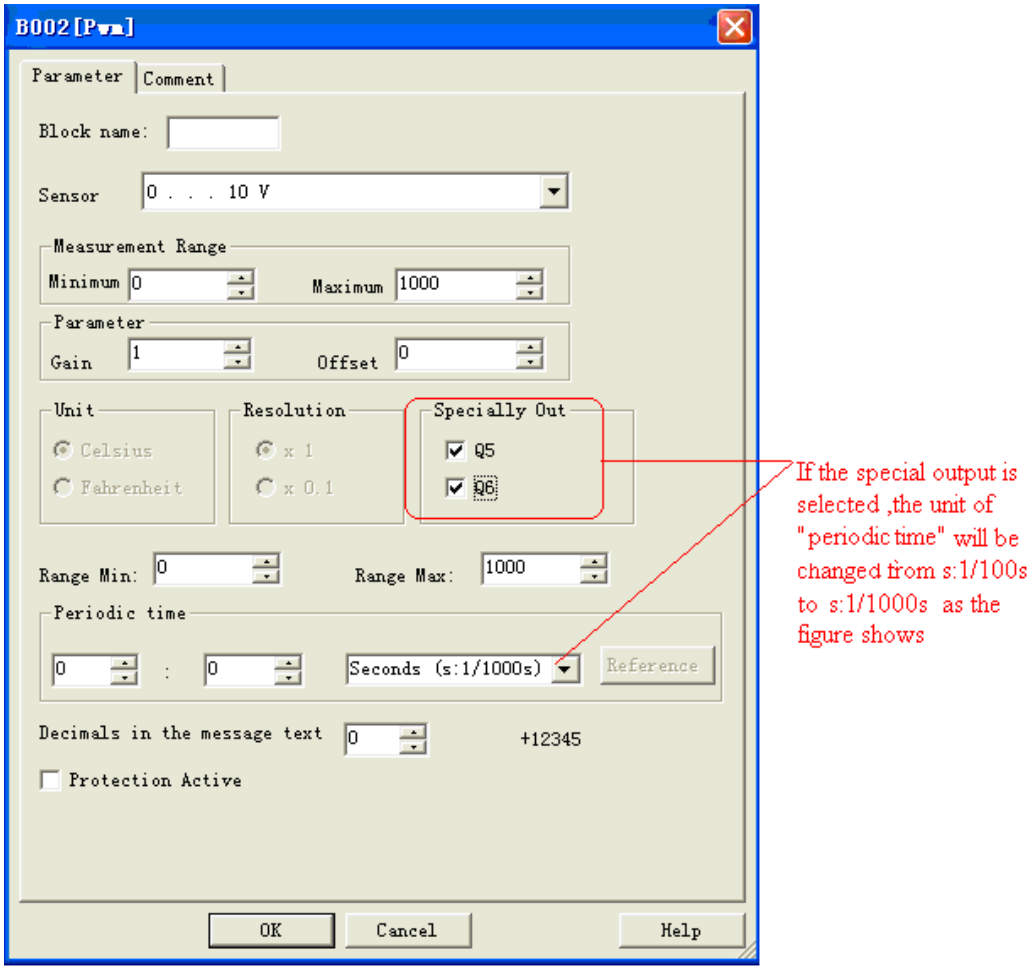
Example 2
Analog input value: 300 (range 0...1000)
Periodic time T: 10 seconds
The digital output of the PWM function is 3 seconds high, 7 seconds low, 3 seconds high, 7 seconds low and continues in that pattern as long as parameter "En" = high.



Calculation rule
Q = 1, for (Ax – Min) / (Max – Min) of time period PT

Q = 0, for PT – [(Ax – Min) / (Max – Min)] of time period PT.
Note: Ax in this calculation refers to the actual value Ax as calculated using the Gain and Offset. Min and Max refer to the minimum and maximum values specified for the range

Special feature.
General, the output frequency could be up to 30Hz But the Q5,Q6 of ELC-18DC-D/DA-TN type (PNP transistor output) CPU could be up to 333 Hz and the property dialog box of PWM function block setting as follows:

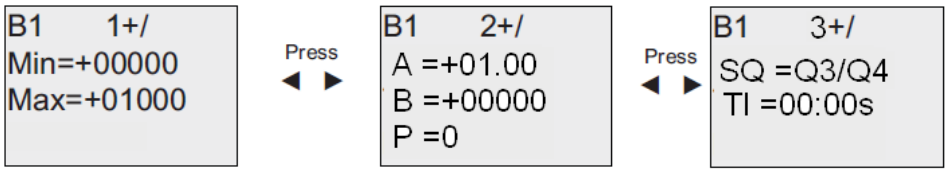


If the special output is selected in the property dialog box of PWM block, then the unit of "periodic time" will be changed from s:1/100s to s:1/1000s, so if you input 3 (1/1000s) , then its frequency is 1000/3 Hz.

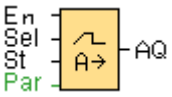
Notes:

1.The periodic time must be no less than 3 ms.
2. If the specific output is selected in the property dialog box of PWM block, then the output pin of PWM function block cannot be linked as input to other blocks.
3. Q5, Q6 of ELC-18DC-DA-TN & ELC-18DC-D-TN on the above dialog box are exactly corresponding to Q3, Q4 of ELC-12DC-D/DA-TN (PNP) CPUs

Setting the Par Parameter
The following illustration shows the view in programming mode that corresponds to the first example:



4.7 Analog Ramp



Short Description:

The Analog Ramp instruction allows the output to be changed from the current level to le selected level at a specified rate.

Connection	Description
Input **En**	A change in the status from 0 to 1 at input EN (Enable) applies the start/stop level (Offset "B" + StSp) to the output for 100 ms and starts the ramp operation to the selected level. A change in the status from 1 to 0 immediately sets the current level to Offset "B", which makes output AQ equal to 0.
Input **Sel**	SeI = 0: The step 1 (level 1) is selected. SeI = 1: The step 2 (level 2) is selected. A change in status of Sel causes the current level to start changing to the selected level at the specified rate.
Input **St**	A change in the status from 0 to 1 at input St (Decelerated Stop) causes the current level to decrease at a constant rate until the start/stop level (Offset "B" + StSp) is reached. The start/stop level is maintained for 100 ms and then the current level is set to Offset "B", which makes output AQ equal to 0.
parameter	Level1 and Level2: Levels to be reached; value range for each level: -10,000 to +20,000 MaxL: Maximum value that must not be exceeded. Value range: -10,000 to +20,000 StSp: Start/Stop offset: value that is added to Offset "B" to create the start/stop level. If the Start/Stop offset is 0, then the start/stop level is Offset "B"). Value range: 0 to +20,000 Rate: Speed with which level 1, level 2 or 0ffset is reached. Steps/seconds are issued. Value range: 1 to 10,000 A: Gain Value range: 0 to 10,00 B: Offset Value range: +- 10.000 p: Number of decimal places Value range: 0, 1, 2, 3
Output **AQ**	The output AQ is scaled using the formula: (Current Level - Offset "B") / Gain "A" Note: When AQ is displayed in parameter mode or message mode, it is displayed as an unscaled value (engineering units: current level). Value range for AQ: 0...+32767

Parameter p (number of decimal places)

The level parameters Level1 and Level2 can be provided by the value of another already-programmed function:

Analog comparator: Ax – Ay

Analog trigger: Ax

Analog amplifier: Ax

Analog multiplexer: AQ

Analog ramp: AQ

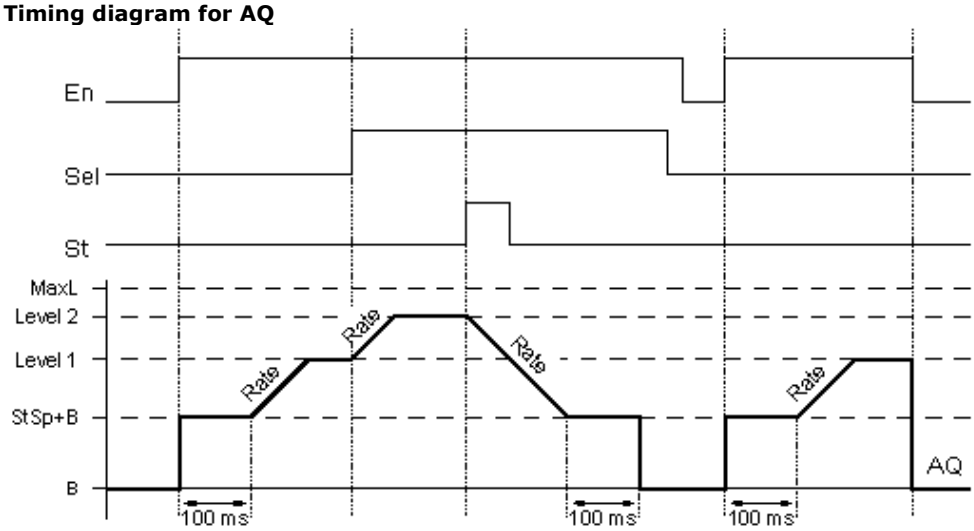
Analog math: AQ

PI controller:AQ
Data latching relay: AQ

Up/Down counter: Cnt

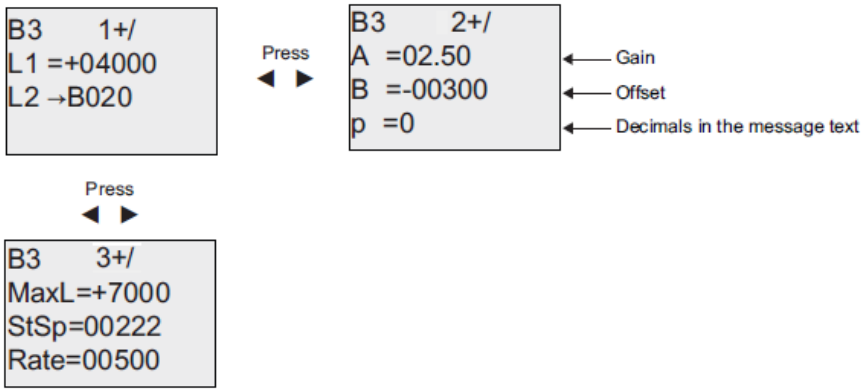
Parameter p only applies for displaying the values of AQ, level 1, level 2, MaxL, StSp, and Rate in a message text.

Timing diagram for AQ

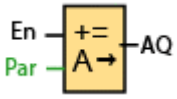


Description of function
If the input En is set, then the function sets the value StSp + Offset "B" for 100 ms.
Then, depending on the connection of Sel, the function runs from the level StSp + Offset "B" to either level 1 or level 2 at the acceleration set in Rate.
If the input St is set, the function runs to a level of StSp + B at the acceleration set in Rate. Then the function holds the level at StSp + Offset "B" for 100 ms. After 100 ms, the level is set to Offset "B". output AQ. The scaled value (output AQ) is 0.
If the input St is set, the function can only be restarted once the inputs St and En have been reset.
If input Sel has been changed, depending on the connection of Sel, the function runs from the current target level to the new target level at the rate that is specified.
If the input En is reset, the function immediately sets the current level to Offset "B".
The current level is updated every 100 ms. Note the relationship between output AQ and the current level:
Output AQ = (current level – Offset "B") / Gain "A"

Setting the Par parameter
View in programming mode (example):



4.8 Analog Math



Short Description
The analog math block calculates the value AQ of an equation formed from the user-defined operands and operators.

Connection	Description
Input **EN**	Enable the analog math function block.
Parameter	**V1:**Value 1: First operand **V2:** Value 2: Second operand **V3:** Value 3: Third operand **V4:** Value 4: Forth operand **Operator 1:** First operator **Operator2:** Second operator **Operator 3:** Third operator **Priority 1:** Priority of first operation **Priority 2:** Priority of second operation **Priority 3:** Priority of third operation **P:** number of decimals Range of values: 0,1,2,3
Output **AQ**	The output AQ is the result of the equation formed from the operand values and operators. AQ will be set to 32767 if a divide by 0 or overflow occurs, and -32768 if a negative overflow (underflow) occurs.

Parameter p (number of decimals)
The values V1, V2, V3, and V4 can be provided by the actual value of another already-programmed function:
Analog comparator: Ax – Ay
Analog trigger: Ax
Analog amplifier: Ax
Analog multiplexer: AQ
Analog ramp: AQ
Analog math: AQ
PI controller: AQ

Data latching relay: AQ
Up/Down counter: Cnt

Parameter p applies to the display of V1, V2, V3, V4 and AQ in a message text.

Description of the function

The analog math function combines the four operands and three operators to form an equation. The operator can be any one of the four standard operators: +, -, *, or /. For each operator, you must set a unique priority of High ("H"), Medium ("M"), or Low ("L"). The high operation will be performed first, followed by the medium operation, and then by the low operation. You must have exactly one operation of each priority. The operand values can reference another previously-defined function to provide the value. The analog math function rounds the result to the nearest integer value.

The number of operand values is fixed at four and the number of operators is fixed at 3. If you need to use fewer operands, use constructions such as " + 0" or " * 1" to fill the remaining parameters.

You can also configure the behavior of the function when the Enable parameter "En"=0. The function block can either retain its last value or be set to 0.

Possible errors: Zero division and overflow

If the analog math function block execution results in zero division or overflow, it sets internal bits that indicate the type of error that occurred. You can program an analog math error detection function block in your circuit program to detect these errors, and to control the program behavior as needed. You program one analog math error detection function block to reference one specific analog math function block.

Examples

The following tables show some simple example analog math block parameters, and the resulting equations and output values:

V1	Operator1	V2	Operator2	V3	Operator3	V4
12	+(M)	6	/(H)	3	-(L)	1

Equation: (12 + (6 / 3)) - 1
Result: 13

V1	Operator1	V2	Operator2	V3	Operator3	V4
2	+(L)	3	*(M)	1	+(H)	4

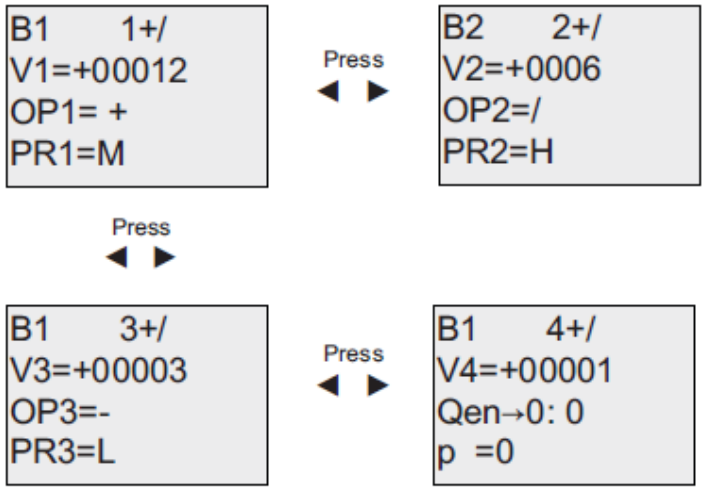
Equation: 2+ (3*(1+4))
Result: 17

V1	Operator1	V2	Operator2	V3	Operator3	V4
100	-(H)	25	/(L)	2	+(M)	1

Equation: (100 – 25) / (2 + 1)
Result: 25

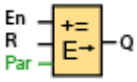
Setting the Par parameter

The following illustration shows the view in programming mode that corresponds to the first example (12 + (6 / 3)) - 1:



Use the ◀ and ▶ keys to navigate between the operand value, operator, and operation priority. To change a value, use the ▲ and ▼ keys to scroll through value choices for each value. Use the ◀ key to navigate from one screen to the previous screen when the cursor is on the V1..V4 line, and the ▶ key to navigate to the next screen from the PR1..PR3 line. Use the OK key to accept changes.

4.9 Analog math error detection



Short Description

The analog math error detection block sets an output if an error has occurred in the referenced analog math function block.

Connection	Description
Input **EN**	Enable the analog math error detection function block.
Input **R**	Reset the output
Parameter	**Referenced FB:** block number of an analog math instruction **Error to detect:** Zero division, Overflow, or Zero division OR Overflow. **Auto Reset:** Reset the output when the failure condition clears.
Output **AQ**	Q is set high if the error to detect occurred in the last execution of the referenced analog math function block.

Parameter Referenced FB

The value for the Referenced FB parameter references the block number of an already-programmed analog math function block.

Description of the function

The analog math error detection block sets the output when the referenced analog math function block has an error. You can program the function to set the output on a zero division error, an overflow error, or when either type of error occurs.
If you select the automatically reset checkbox, the output is reset prior to the next execution of the function block. If not, the output retains its state until the analog math error detection block is reset with the R parameter.
In any scan cycle, if the referenced analog math function block executes before the analog math error detection function block, the error is detected in the same scan cycle. if the referenced analog math function

block executes after the analog math error detection function block, the error is detected in the next scan cycle.

Analog math error detection logic table

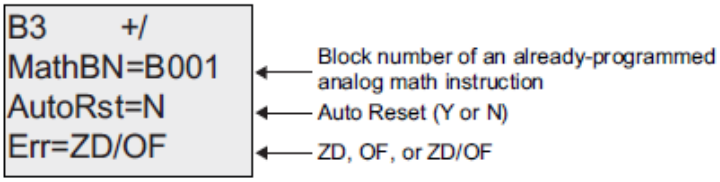
In the table below, Error to Detect represents the parameter of the analog math error detection instruction that selects which type of error to detect. Zero represents the zero division bit set by the analog math instruction at the end of its execution: 1 if the error occurred, 0 if not. OF represents the overflow bit set by the analog math instruction: 1 if the error occurred, 0 if not. Zero division OR Overflow represents the logical OR of the zero division bit and the overflow bit of the referenced analog math instruction. Output (Q) represents the output of the analog math error detection function. An "x" indicates that the bit can be either 0 or 1 with no influence on the output.

Error to Detect	zero	of	Output
Zero division	1	X	1
Zero division	0	X	0
Overflow	X	1	1
Overflow	X	0	0
Zero division OR Overflow	1	0	1
Zero division OR Overflow	0	1	1
Zero division OR Overflow	1	1	1
Zero division OR Overflow	0	0	0

If the Referenced Analog Math FB is null, then the output is always 0.

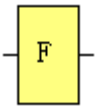
Setting the Par parameter
The parameters MathBN, AutoRst, and Err can be set in programming mode or parameter assignment mode.

View in programming mode (example):



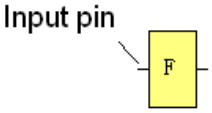
Use the ◄ and ► keys to navigate between the MathBN, AutoRst, and Err parameters. To change a value, use the ▲ and ▼ keys to scroll through value choices for each value. Use the OK key to accept changes.

4.10 F(digital flag)



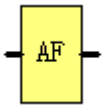
Flags are only used when xLogic works in a communication system. F is digital flag which is used to save /transfer signal 1 or 0(data format is Bit) and AF is analog flag which is used to save /transfer analog values (data format is Signed short) between the master and slave device. Both of flags (digital/analog) are up to 32(ELC-18)/64(ELC-12) can be used when programming. In your block configuration, you can assign a new number to the flag, provided this flag number does not already exist in your circuit program.
The output always carries the signal of the previous program cycle. This value does not change if the communication were failed .

Notes: 1.The address of " F" can be found in the modbus communication protocol file . Not all the versions hardware of ELC-18 support the write property of F , only the hardware version number which is not less than "C7"(HEX) can support the function , in addition, the F block figure must have the input pin in the xLogicsoft showing.



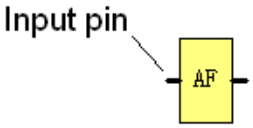
2. Keep the input pin of F NULL(do not connect with other blocks) ,if you want to use the write property.

4.11 AF(Analog flag)



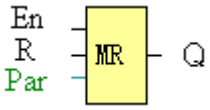
Flags are only used when xLogic works in a communication system. F is digital flag which is used to save /transfer signal 1 or 0(data format is Bit) and AF is analog flag which is used to save /transfer analog values (data format is Signed short) between the master and slave device. Both of flags (digital/analog) are up to 32(ELC-18)/64(ELC-12) can be used when programming. In your block configuration, you can assign a new number to the flag, provided this flag number does not already exist in your circuit program.
The output always carries the signal of the previous program cycle. This value does not change if the communication were failed .

Notes: 1.The address of "AF" can be found in the modbus communication protocol file . Not all the versions hardware of ELC-18 support the write property of AF , only the hardware version number which is not less than "C7"(HEX) can support the function , in addition, the F block figure must have the input pin in the xLogicsoft showing.



2. Keep the input pin of AF NULL(do not connect with other blocks) ,if you want to use the write property.

4.12 Modbus Read



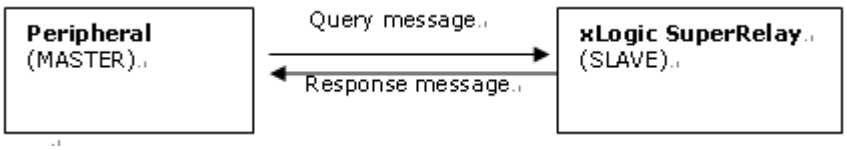
Short description:
When there is a high level at En, the Modbus Read block will be activated and the xLogic SuperRelay can communicate with a peripheral device as a master via RS232 or RS485 interface. Furthermore, the output will be switched on when communication is established successfully. Otherwise the output (Q pin) remains "off" which means communication has failed.
A signal at input R resets output Q and disables this block at the same time

Connection	Description
Input **En**	A high signal at En input will enable "Modbus Read" function block to be activated
Input **R**	Reset the value read from peripheral and set the output to 0 via the R (Reset) input. Reset has priority over En
Parameter	**Slave address**: 1 is default . **Communication protocol:** Modbus(RTU) **Communication parameter:** baud rate (BPS),Data bits, Stop bits, Parity, Overtime (response time out) **Comm Type:** RS232 or RS485(Communication interface of xLogic) **Data register Index:** High Low /Low High **Command:** 01 Read coils(0x) 02 Read Discrete Input(1x) 03 Read Holding Registers(4x) 04 Read Input Registers(3x) Register start address, count
Output **Q**	Q is set or reset depending on the communication status. Successful communication , Q=1; Failed communication ,Q=0;

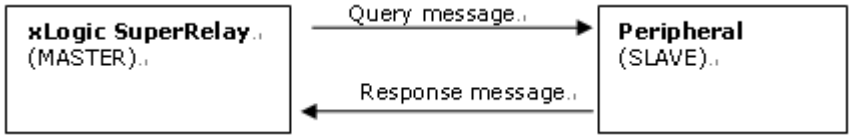
Note: 1. Data register Index: High Low /Low High
For example,when High Low index was set, one data 0x 00 12 was read and saved to AQ ,AQ= 0X0012; However ,when Low High index was set ,AQ=0x 1200

a) Regarding Modbus RTU detail, please refer to our Modbus RTU communication protocol file for it.

Description of the function:
In the configuration of our xLogic communication, the xLogic SuperRelay usually serves as a slave via Modbus RTU Protocol, and can communicate with a master directly. That's to say, any device communicating with xLogic SuperRelay sends command to it, and the its response will be sent out only when the xLogic SuperRelay has received the command, Just as the below figure shows:

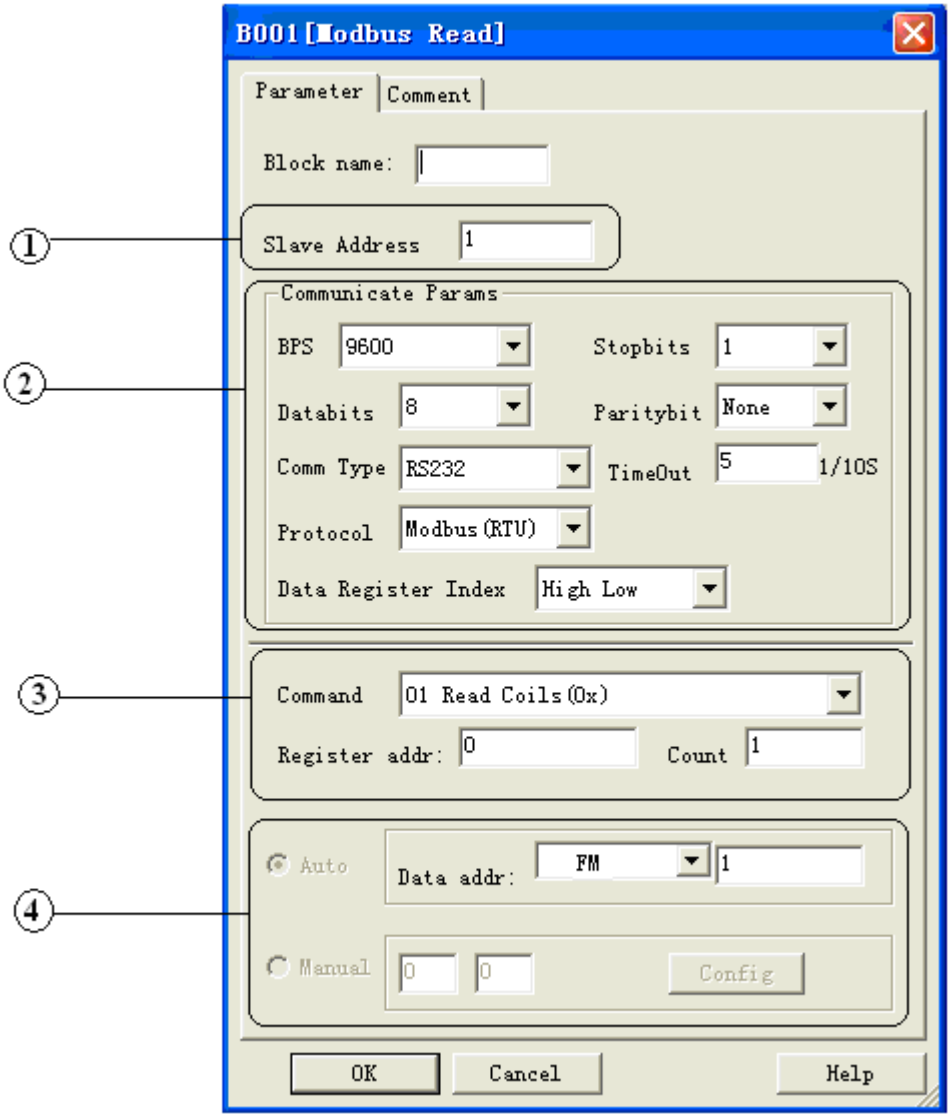


However, the "Modbus Read" or "Modbus Write"(next chapter will introduce it) function block would be utilized if xLogic SuperRelay shall be required to play a role of master to communicate with other devices. As the following figure shows:



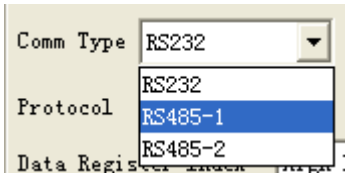
When you put Modbus read" or " Modbus Write" function block in your program and make some configurations, the function that xLogic SuperRelay serves as master will be realized.

The Property in dialog box of "Modbus Read" shows as below figure:



1. Slave Address :1 is default
2. Communication parameters: BPS is baud rate、Stopbits、Databits、Communication type: RS232、RS485 . Actually RS232 or RS485 are just interface of xLogic.
Notes: RS485 interface is only applied to ELC-18 SERIES.
3. Command , register address and register count

New feature for ELC-12 CPU :



RS485-1 is the extension port of ELC-12 CPU(new version) named COM2 ,it usually servers as extension port , but it also can be used as common RS485 port if no any extensions need use in your application and at the same time two RS485 ports need be used. The standard RS485 module cannot bring out the terminals of COM2 built-in ELC-12 series CPU, so one customize RS485 module should be used for this case.

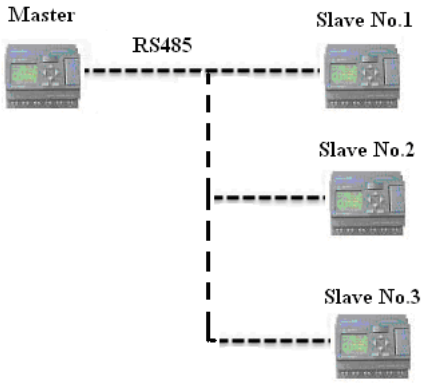
RS485-2 is the common RS485 port built-in ELC-12 series CPU ,ELC12-E-RS485 module is used to bring out the terminals from this port named COM3. Additionally ,ELC12-E-ETHERNET module is also using this port to connect with ETHERNET.

Command	Function description	remark
01	Read one group coil status（00000～0XXXX）	Read Coil Status（output）
02	Fetch one group data of the status of switch input（10000～1XXXX）	Read input Status (input relay)
03	Read data of multi-holding register（40000～4XXXX）	Read Holding Registers (Output register)
04	Read data of input registers (3000～3XXXX)	Read Input Registers

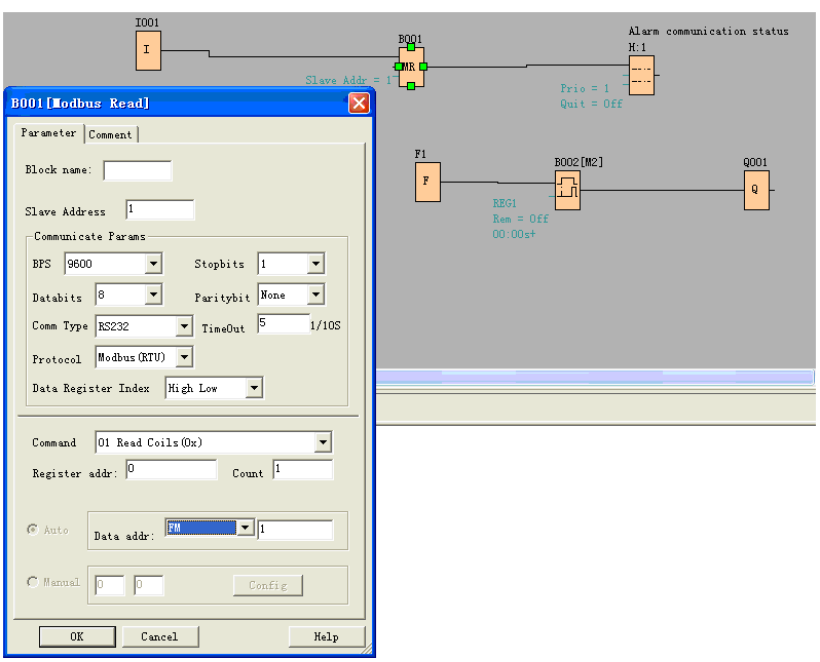
Note: Please use "03" command to read AI/AO of xLogic

4.where to save the data read from Slave.

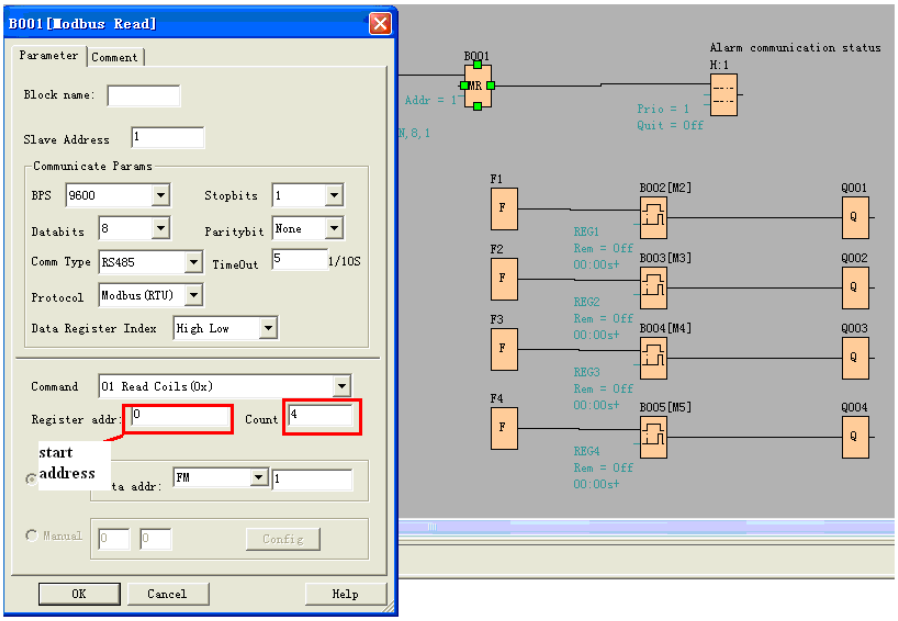
Example: The following we'll take a example that one xLogic (Master) communicate with other xLoigc (Slave) via RS485.



Example 1: Get Q1 status of SLAVE1(xLogic) and then save the bit status to FM1.

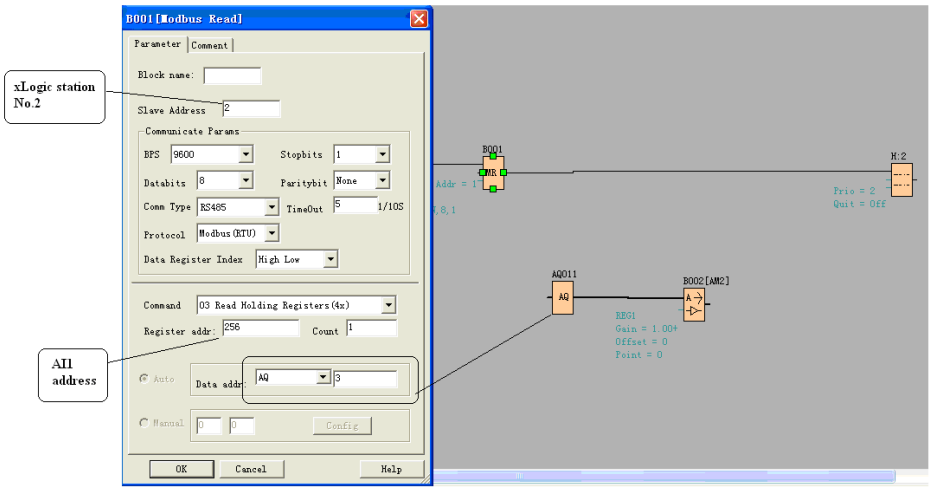


If count was set 4 ,the Q1,Q2,Q3,Q4 of xLogic (station No.1) will be read and save to F1 to F4



F is bit flag . It can be used to receive bit data from slave device.

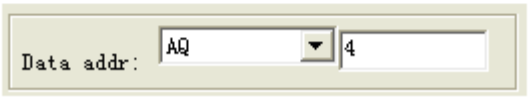
Example 2 : Get AI value from Slave 2(xLogic with station No.2) and save the data to AQ11



Note:

---AQ1

---AQ2



---AQ12

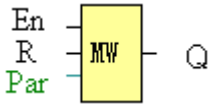
The number setting of Q,I,AQ are continuous .AQ12 can not be set as AQ 12 and should be set AQ 4 as above figure shows.
The following table shows how to set.
Note : this table also can be applied for the configuration of Modbus Write function block.

MODEL	I,Q,AI,AQ	DIALOG BOX SET
CPU	I1-IC	I1-I12
	Q1-Q6	Q1-Q6
	AI1-AI8	AI1-AI8
	AQ1-AQ2	AQ1-AQ2
Expansion 1 (Address is 1)	I11-I18	I13-I20
	Q11-Q18	Q7-Q14
	AI11-AI18(AI15-AI18 are reserved)	AI9-AI12
	AQ11-AQ12	AQ3-AQ4
Expansion 2 (Address is 2)	I21-I28	I21-I28
	Q21-Q28	Q15-Q22
	AI21-AI28	AI17-AI24
	AQ21-AQ22	AQ5-AQ6
.....		

Data format instruction

Name	Data format
F, I,Q	BIT
AF, AI, AQ,	Signed Short

4.13 Modbus Write

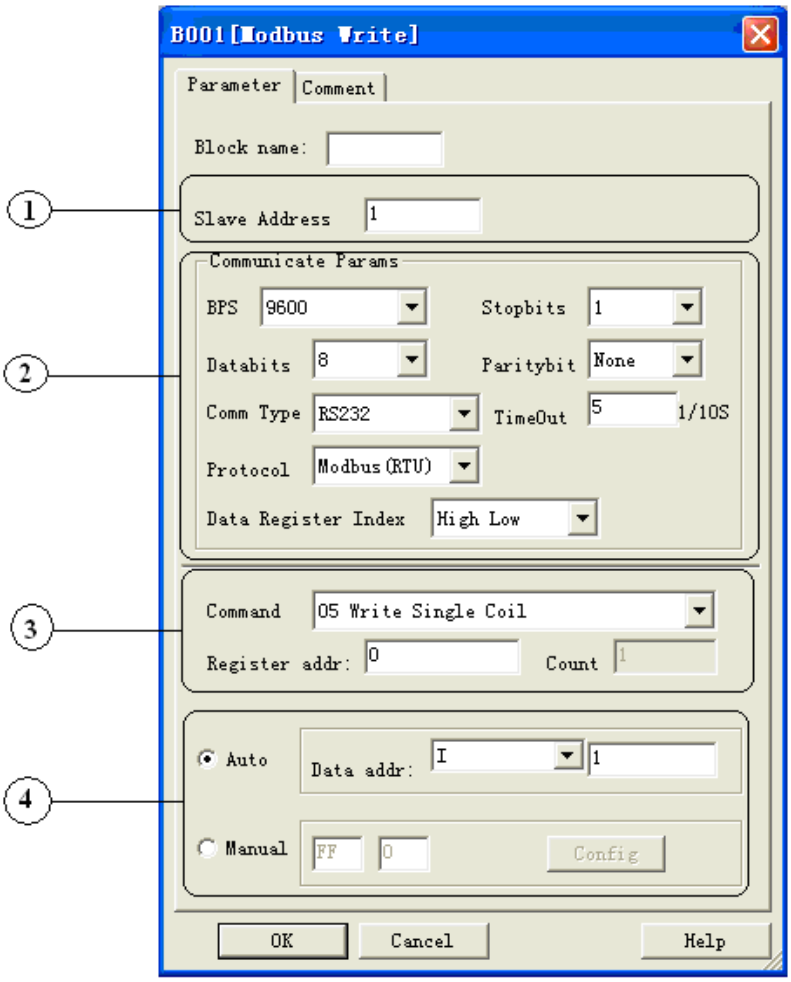


Short description:

When a high level in En, the Modbus Write block will be activated and the xLogic SuperRelay could communicate with peripheral as a master via RS232 or RS485 interface, further the output will be switched on when the communication is established successfully. Otherwise the output (Q pin) is keep "off" it means communication is failed.
A signal at input R resets output Q and disable, this block at the same time

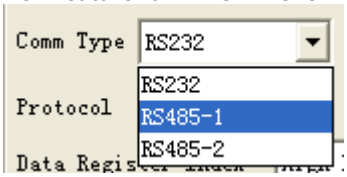
Connection	**Description**
Input **En**	A high signal at En input will enable "Modbus Write" function block to be activated
Input **R**	Reset the value read from peripheral and set the output to 0 via the R (Reset) input. Reset has priority over En
Parameter	**Slave address**: 1 is default . **Communication protocol:** Modbus(RTU) **Communication parameter:** baud rate (BPS),Data bits, Stop bits, Parity， Overtime (response time out) **Comm Type:** RS232 or RS485(Communication interface of xLogic) **Data register Index:** High Low /Low High **Command:** 05 Write Single Coil 06 Write Single Register 15 Write Multiple Coils 16 Write Multiple Registers **Register start address, count**
Output **Q**	Q is set or reset depending on the communication status. Successful communication , Q=1; Failed communication ,Q=0;

The Property in dialog box of "Modbus Write" shows as below figure:



1. Slave Address: 1 is default
2.Communication parameters：BPS is baud rate、Stopbits、Databits、Communication type: RS232、RS485 . Actually RS232 or RS485 are just interface of xLogic.
Notes: RS485 interface is only applied to ELC-18 SERIES.
3. Command, register address and register count

New feature for ELC-12 CPU：



RS485-1 is the extension port of ELC-12 CPU(new version) named COM2 ,it usually servers as extension port , but it also can be used as common RS485 port if no any extensions need use in your application and at the same time two RS485 ports need be used. The standard RS485 module cannot bring out the terminals of COM2 built-in ELC-12 series CPU, so one customize RS485 module should be used for this case.

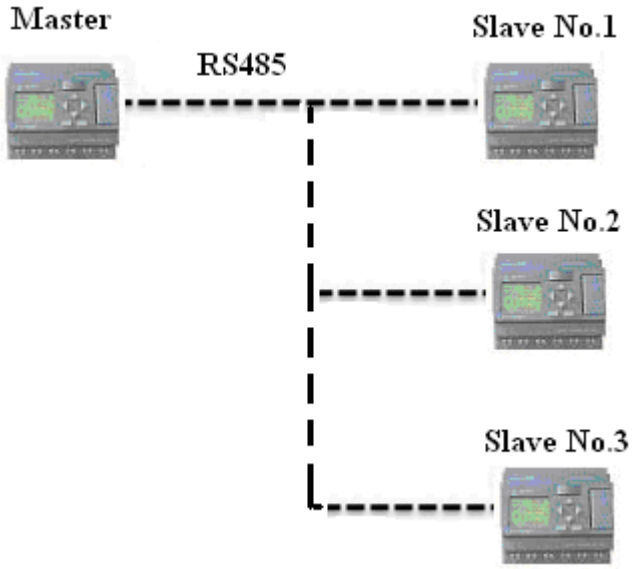
RS485-2 is the common RS485 port built-in ELC-12 series CPU ,ELC12-E-RS485 module is used to bring out the terminals from this port named COM3. Additionally ,ELC12-E-ETHERNET module is also using this port to connect with ETHERNET.

Command	Function description	remark
05	Force the switch status of single coil（00000～0XXXX）	Force Single Coil

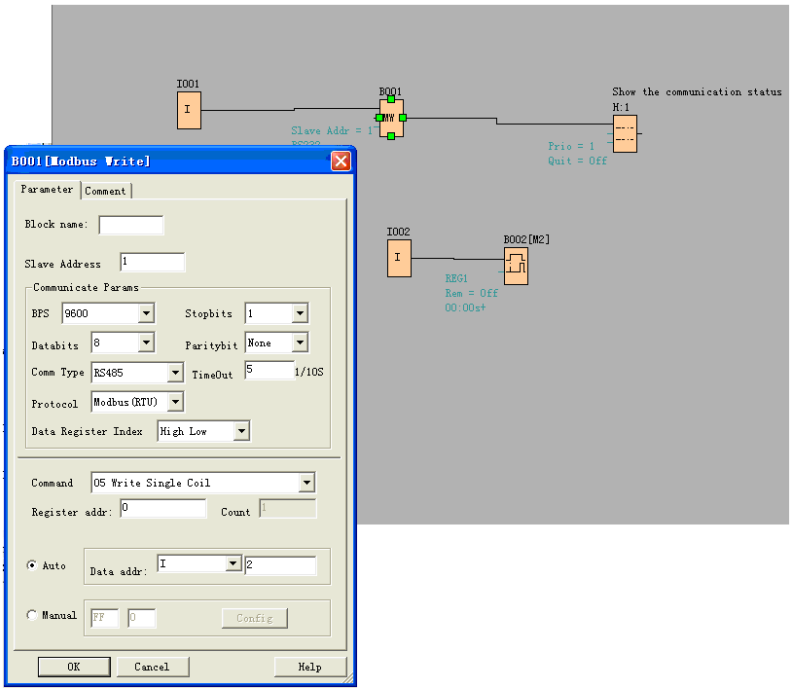
		（output）
06	Pre-set the data of single register （40000～4XXXX）	Set single output register
15	Force multi-coils on/off bit（00000～0XXXX）	
16	Write multi-holding registers data（40000～4XXXX）	

4. where is to save the pre-configuration data that would be written to Slave. It contains 2 kind ways to pre-configuration. One is auto mode ,this data uses the flags in the program ,such as FM, AFM ,I, Q and AQ. The manual mode is intput a fixed value or bit status.

Example 1



Write the I2 bit status of Master xLogic to Slave xLogic with No.1 and control Q1 of Slave via RS485 port. the program of master can be made as follows:

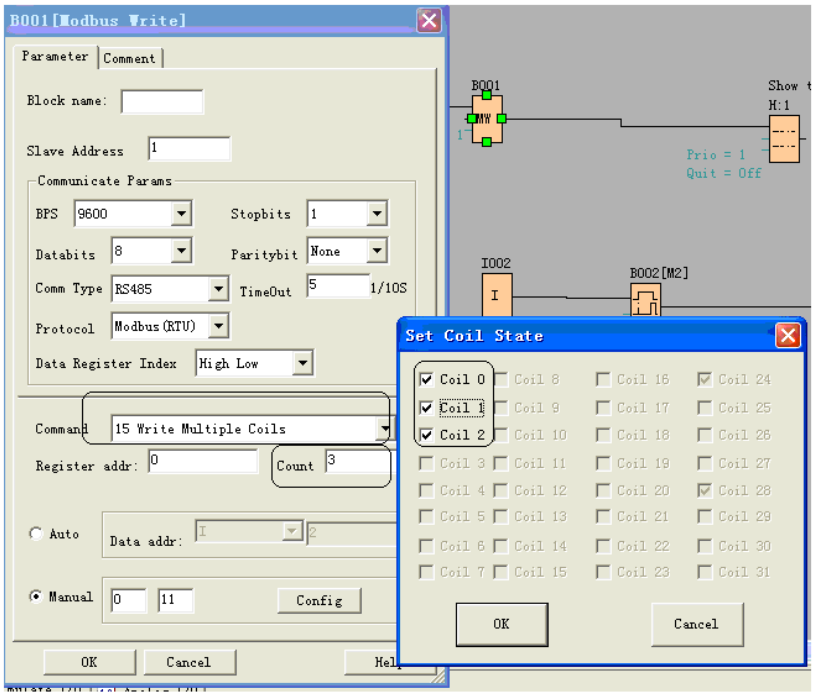


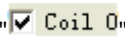
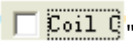
I1of master is used to control the communication .If I1 is high and the communication is

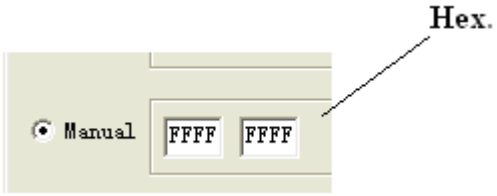
established successfully, one alarm message (text message block) will be displayed on LCD. Then the Q1 of slave No.1 will be controlled by I2 of master. If I2 is high, Q1 of slave No.1 would be ON and if I2 is low, Q1 of slave would be OFF.

Note: The Q1 must be free, it means the in the program of Slave No.1, the input pin of Q1 must be not linked to other blocks.

Example 2 , manual mode input value



The above configuration is to force Q1,Q2,Q3 of Slave No.1 ON. "  " means pre-set the BIT 1 and "  " means pre-set the BIT 0 ,"Coil 0" is corresponding to the start address ,Here is Q1.
Note: The manual input value is Hex data .it contains 4 bytes. If you want to write a decimal value to the register of SLAVE, please convert it to Hex format.



4.
The following table shows how to set.
Note: this table also can be applied for the configuration of Modbus Read function block

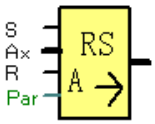
MODEL	I, Q, AI, AQ	DIALOG BOX SET
CPU	I1-IC	I1-I12
	Q1-Q6	Q1-Q6
	AI1-AI8	AI1-AI8
	AQ1-AQ2	AQ1-AQ2
Expansion 1 (Address is 1)	I11-I18	I13-I20
	Q11-Q18	Q7-Q14
	AI11-AI18(AI15-AI18 are reserved)	AI9-AI12
	AQ11-AQ12	AQ3-AQ4
Expansion 2 (Address is 2)	I21-I28	I21-I28
	Q21-Q28	Q15-Q22
	AI21-AI28	AI17-AI24
	AQ21-AQ22	AQ5-AQ6
.....		

Data format instruction

Name	Data format
F, I, Q	BIT
AF, AI, AQ,	Signed Short

For the detail information about I,AI,Q,AQ, registers address of xLogic ,refer to the RTU protocol file.

4.14 Data latching relay

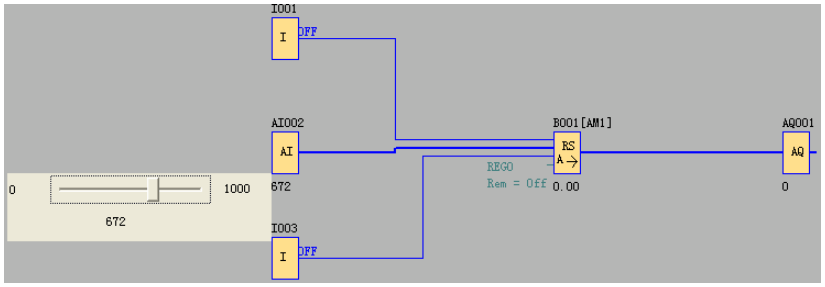


Short description

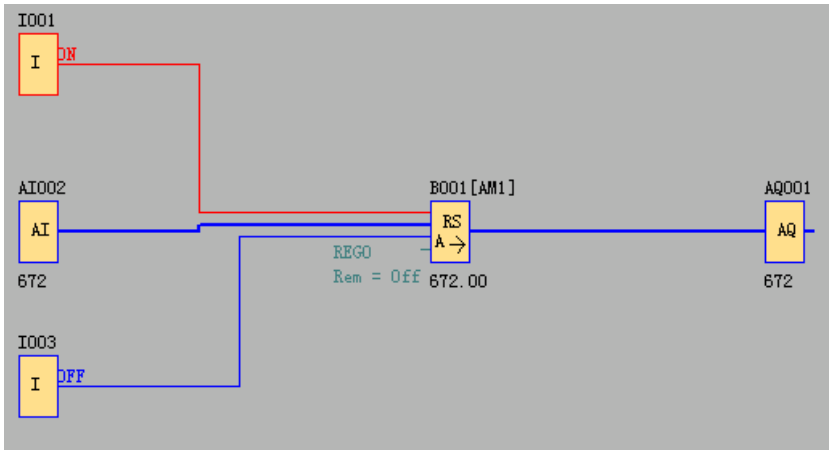
This special function saves the process variable of an analog input to memory, and returns it at the analog output.

Connection	Description
Input **S**	Save the Ax to memory and return it at the analog output with a signal at input S (Set).
Input **Ax**	Input the analog signal to be amplified at input Ax. Use the analog inputs, the block number of a function with analog output, or the analog outputs.
Input **R**	Reset analog output AQ to 0 with a signal at input R (Reset). analog Output AQ is reset if S and R are both set (reset has priority over set).
Output **AQ**	Analog output Value range for AQ: -32768...+32767

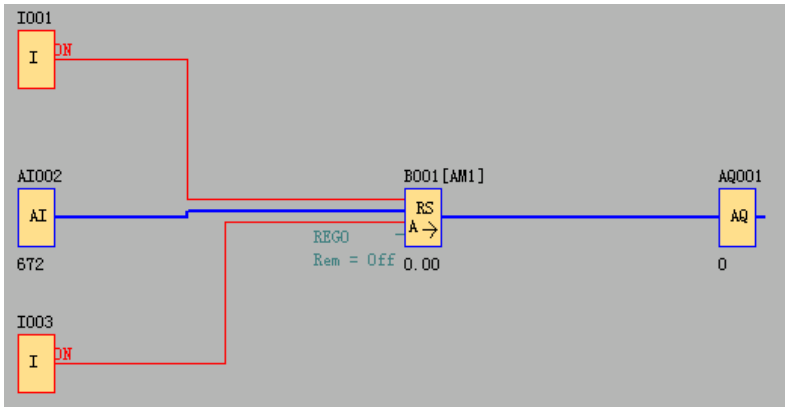
Example



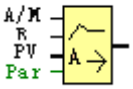
When I1 turn to HIGH, the value of AI2 will be saved to memory and return it to AQ1 as follows:



When the I3 turn to HIGH, the value of this function block will be reset to 0.



4.15 PI controller



Short Description

Proportional-action and integral-action controllers. You can use both types of controller individually or combined.

Connection	Description
Input **A/M**	Set the mode of the controller: 1: automatic mode 0: manual mode

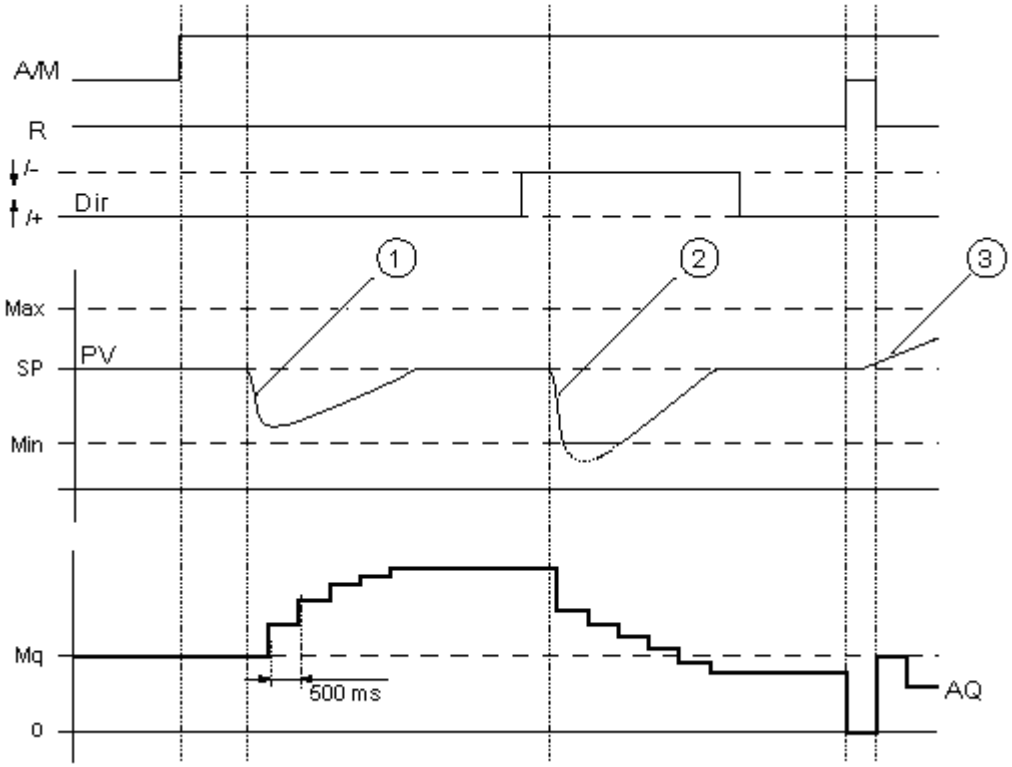
Input **R**	Use the input R to reset the output AQ. As long as this input is set, the input A/M is disabled. The output AQ is set to 0.
Input **PV**	Analog value: process value, Influences the Output
Parameter	Sensor: Type of sensor being used **Min.:** Minimum value for PV value range: -10,000 to +20,000 **Max.:** Maximum value for PV value range: -10,000 to +20,000 **A:** Gain Value range: +- 10.00 **B:** Offset Value range: +- 10,000 **SP:** Set-value assignment value range: -10,000 to +20,000 **Mq:** Value from AQ with manual mode. Value range: 0 to 1,000 **Parameter sets:** application-related presets for KC, TI and Dir (see below) **KC:** Gain value range: 00.00 to 99.99 **TI:** Integral time value range 00:01 min to 99:59 min **Dir:** Action direction of the controller value range: + or - **p:** Number of decimal places value range: 0, 1, 2, 3
Output **AQ**	Analog output (manipulated variable) Value range for AQ: 0 to 1,000

Parameter P (number of decimal places)

Only applies for portraying the values from PV, SP, Min. and Max. in a message text.

Timing Diagram

The nature, manner and speed with which the AQ changes depend on the parameters KC and TI. Thus, the course of AQ in the diagram is merely an example. A control action is continuous; therefore the diagram portrays just an extract.



1. A disturbance causes the PV to drop, as Dir is positioned upwards, AQ increases until PV corresponds again to SP.

2. A disturbance causes the PV to drop, as Dir is positioned upwards, AQ decreases until PV corresponds again to SP.

   Dir is coordinated to the basic conduct of a control loop. The direction (dir) cannot be changed during the term of the function. The change in Dir here is shown for the purposes of clarification.

3. As AQ is set to 0 by means of the input R, PV changes. This is based on the fact that PV increases, which on account of Dir = upwards causes AQ to drop.

Description of Function

If the input A/M is set to 0, then the special function issues output AQ with the value that you set with parameter Mq.

If the input A/M is set to 1, then automatic mode commences. As an integral sum the value Mq is adopted, the controller function begins the calculations in accordance with the formulas given in Control and regulate basics. The updated value PV is used to calculate in the formulas.

Updated value PV = (PV * gain) + offset

If the updated value PV = SP, then the special function does not change the value of AQ.

Dir = upwards/+ (timing diagram numbers 1 and 3)

- If the updated value PV > SP, then the special function reduces the value of AQ.
- If the updated value PV < SP, then the special function increases the value of AQ.

Dir = downwards/- (timing diagram number 2)

- If the updated value PV > SP, then the special function increases the value of AQ.
- If the updated value PV < SP, then the special function reduces the value of AQ.

With a disturbance, AQ continues to increase / decrease until the updated value PV again corresponds to SP. The speed with which AQ changes depends on the parameters KC and TI. If the input PV exceeds the parameter Max., then the updated value PV is set to the value of Max.. If the PV falls short of the parameter Min., then the updated value PV is set to the value of Min.

If the input R is set to 1, then the AQ output is reset. As long as R is set, the input A/M is disabled.

Sampling Time

The sampling time is fixed at 500 ms.

Parameter sets

In order to simplify the use of the PI controller, the parameters for KC, TI and Dir are already given as sets for the following applications:

Parameter set	Application example	Parameter KC	Parameter TI (s)	Parameter Dir
Temperature fast	Temperature, cooling control of small spaces; small volumes	0,5	30	+
Temperature slow	Heating, ventilation, temperature, cooling control of large spaces; large volumes	1,0	120	+
Pressure 1	Quick pressure change, compressor control	3,0	5	+
Pressure 2	Slow pressure change, differential pressure control (flow controller)	1,2	12	+
Full level 1	Vat and/or reservoir filling without drain	1,0	1	+
Full level 2	Vat and/or reservoir filling with drain	0,7	20	+

Characteristics when configuring

Observe the Control and regulate basics.

Control and regulate basics

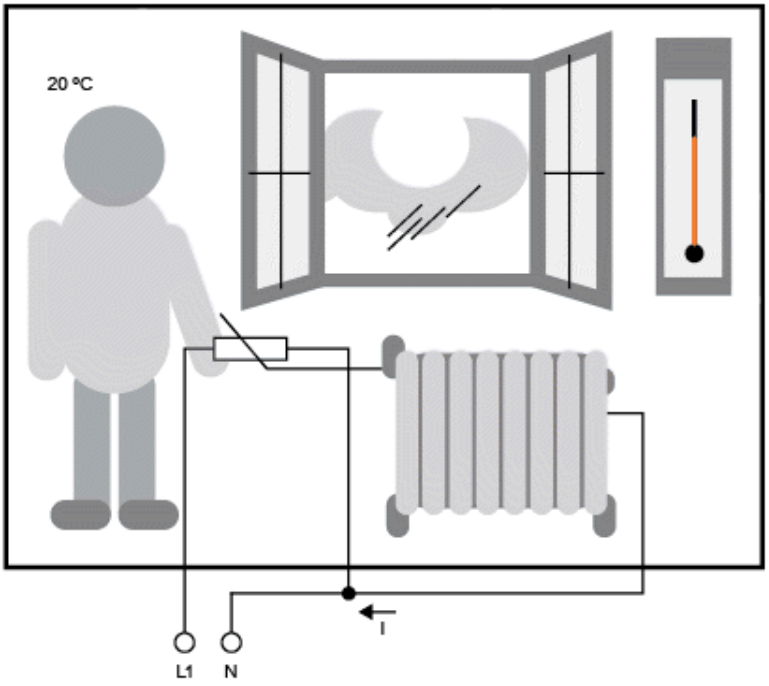
Control and regulate

In engineering, quantities can be both controlled and regulated.

When controlling, a quantity is manipulated without being able to compensate for outside influences. When regulating, a quantity is maintained at a specific value in order to compensate for outside influences.

In the following example, controlling means that the person can set the heat output at a fixed value. The heater cannot compensate for the drop in room temperature when a window is opened.

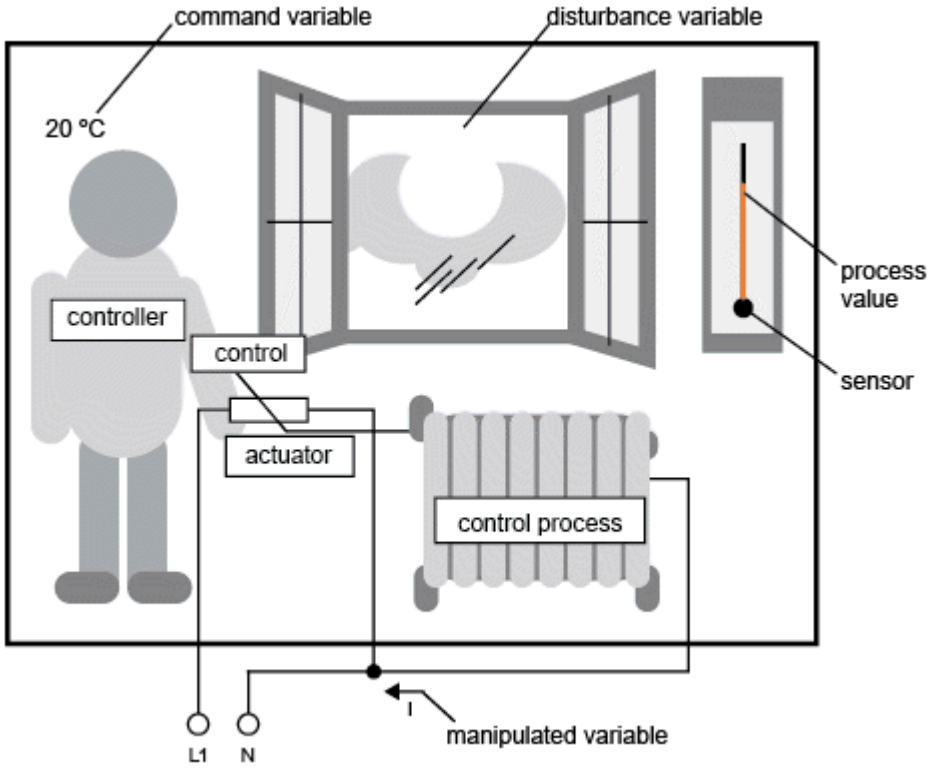
In the example below, regulating means that the person can increase the heat output if the room temperature drops to below 20 °C. If the room temperature rises above 20 °C, the heat output is reduced.



Basic concepts of regulating

In the example, the current for the electric heating is the manipulated variable. The changeable resistance is the actuator. The hand that operates the actuator is the control. The actual room temperature is the controlled variable or the process value. The desired room temperature is the command variable or the setpoint value. The electric heating is the control process. The thermometer is the sensor. The temperature loss from opening the window is the disturbance variable.

So this means that the person measures the process value (room temperature) with the sensor (thermometer), compares the process value (room temperature) with the command variable (desired room temperature) and uses the actuator (changeable resistance) to manually regulate the manipulated variable (heating current), in order to compensate for the disturbance variable (temperature drop from opening the window). The person is therefore the controller.

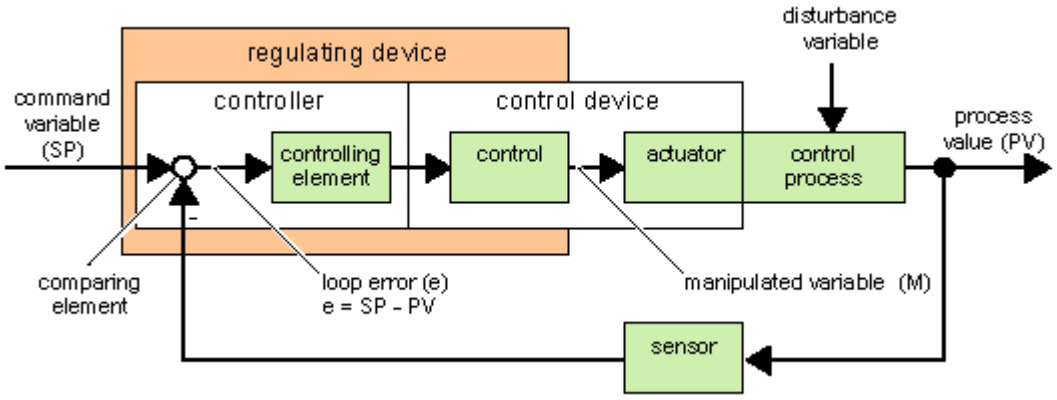


The control device is formed from the actuator and the control.

The control and controller together form the regulating device.

The following picture gives an abstract portrayal of the situation described above.

The comparing element uses the sensor to compare the command variable with the process value. If the command variables and process value deviate from one another, this results in a positive or negative loop error that in turn changes the process value.



Control loop

The process value x influences the manipulated variable M by means of the regulating device. This creates a closed circuit that is also known as a control loop.

If, in the example above, the window is opened, the temperature in the room drops. The person must increase the heat output of the heater. If the heat output is increased too much, it will get too hot. The person must then reduce the heat output.

If the heat output is increased or reduced too quickly, then the control loop starts to sway. The room temperature fluctuates. It is either too hot or too cold. To prevent this, the person must carefully and slowly reduce or increase the heat output.

Loop error

The loop error is the difference between the command variable and the process value. In other words: the deviation of a process value from a set value.

e = SP – PV

The loop error e brings about a change to the manipulated variable M.

The example above illustrates this very well: if, with a desired temperature of 20 °C (= command value w), the room temperature is 22 °C (= process value PV), this results in the loop error:

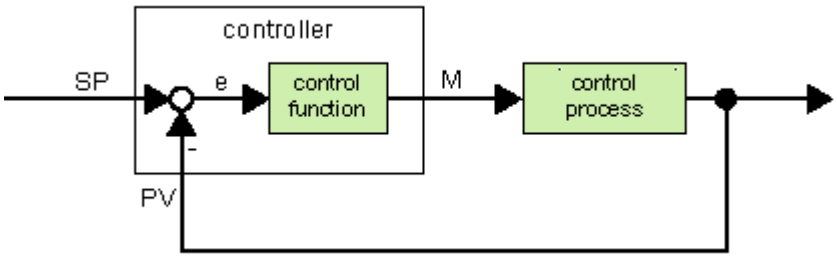
e = SP – PV = 20 °C - 22 °C = -2 °C

In this case, the negative sign indicates a reversing action: the heat output is reduced.

In a control loop's state of equilibrium, the loop error is zero or very small. If the command variable changes or there is a disturbance, a loop error arises. The loop error is corrected by means of the manipulated variable M.

Controller basics

A controller can be simply portrayed as follows:



The comparing element and the controller function describe the conduct of the controller.

The following describes the most important types of controller. A controller's step response tells us a lot about its conduct. The step response describes how a controller reacts to the erratic change in the process value.

There are 3 important basic types of controller:

Proportional-action controller (P controller)

Integral-action controller (I controller)

Differential-action controller (D controller – we're not touching on this here)

These are combined for a real controller. For instance, the PI controller:

P Controller

A proportional-action controller (P controller) changes the manipulated variable M proportional to the loop error. The P controller works immediately. By itself it cannot drive the loop error to zero.

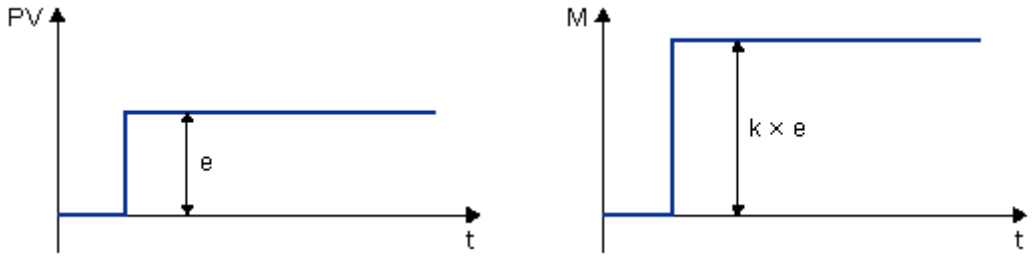
$$M_{Pn} = k_P \times e_n$$

M_{Pn} :Manipulated variable of the P controller at the time n

k_P :Gain of the P controller

e_n: Loop error at the time n

The following picture shows a jump in process value and step response of the controller:



Summary

The P controller has the following characteristics:

It cannot correct faults with the control process > lasting loop error.

It reacts immediately to a change in the process value.

It is stable.

I Controller

An integral-action controller (I controller) changes the manipulated variable M proportional to the loop error and to the time. The I controller works by delayed action. It completely remedies a loop error.

In order to calculate the value of the manipulated variable at a period of time n, the time up until this period of time must be divided into small time slices. The loop errors at the end of each time slice must be added up (integrated) and they are then entered in the calculation.

$$M_{In} = k_I \times (T_S / T_I) \times (e_n + e_{n-1} + e_{n-2} + e_{n-3} + \ldots + e_0) = k_I \times (T_S / T_I) \times e_n + M_{In-1}$$

M_{In}: Manipulated variable of the I controller at the time n

M_{In-1} :Manipulated variable of the I controller at the time n-1; also called integral sum

k_I :Gain of the I controller

T_S: Sampling time, duration of a time slice

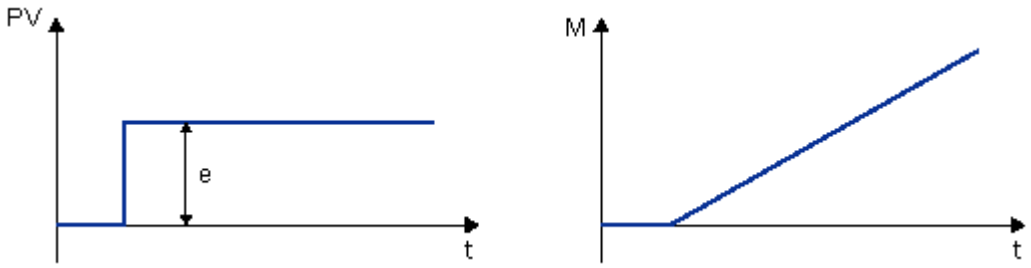
T_I: Integral time: by means of this time, the influence of the integral part is controlled on the manipulated variable, also known as integral-action time

e_n: Loop error at the time n

e_{n-1}: Loop error at the time n-1; etc.

e_0 :Loop error at the beginning of the calculations

The following picture shows a jump in process value and step response of the controller:



Summary

The I controller has the following characteristics:

It sets the process value exactly to the command variable.

By so doing, it tends to oscillate and is unstable.

It requires more time to carry out the control action than the P controller .

PI controller

A PI controller reduces the loop error immediately and will eventually drive the loop error to zero.

$$M_n = M_{Pn} + M_{In} = k_P \times e_n + k_I \times (T_S / T_I) \times e_n + M_{In-1}$$

M_n: Manipulated variable at the time n

M_{Pn}:Proportional part of the manipulated variable

M_{In} :Integral part of the manipulated variable

M_{In-1}:Manipulated variable of the I controller at the time n-1; also called integral sum

k_P :Gain of the P controller

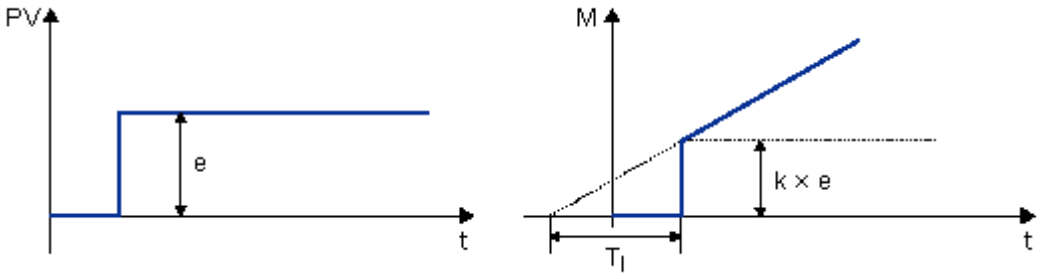
k_I : Gain of the I controller

T_S : Sampling time, duration of a time slice

T_I : Integral time; by means of this time the influence of the integral part is controlled on the manipulated variable, also known as the integral-action time

e_n : Loop error at the time n

The following picture shows a jump in process value and step response of the controller:



Summary

The PI controller has the following characteristics:

The P controller components quickly intercept an occurring loop error.

The I controller components can then remedy the remaining loop error.

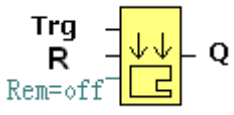
The controller components supplement each other so that the PI controller works quickly and precisely.

Description of the individual parameters

Controller parameters	Portrayed in xLogic	Possible value range in the xLogic
Mn Manipulated variable at the time n	Output of the PI controller block	0 to 1,000
kP Gain of the P part kI Gain of the I part	In the xLogic, the parameter KC applies as an increase for the I part and the P part of the controller equally. Should you enter KC=0, then the P part of the controller switches off. In this special case, k is automatically set to 1 for the I part. If KC = 0: kP = 0 and kI = 1 If KC <> 0: kP = kI = KC	0.00 to 99.99

Ts Sampling time, duration of a time slice	Fixed	500 ms
TI Integral time	Parameter TI, if you set this parameter to 99:59 min, then you switch off the I part of the controller.	00:01 min to 99.59 min
en Loop error at the time n; generally applies: e = SP – PV	Refer to SP and PV	"
SP	The parameter SP is the set-value assignment w. For this parameter you can use the analog output of a different special function.	-10,000 to +20,000
PV	PV is the process value x and is calculated as follows: PV = (analog value on input * gain) + offset. You can connect the input for example by means of an analog input with a PT100 sensor.	"
	The gain parameter has an effect on PV	0.0 to 10.0
	The offset parameter has an effect on PV	-10,000 to +20,000
	PV is restricted by the parameters Min. and Max.	In each case: -10,000 to +20,000
	The Dir parameter gives the action direction of the controller. Positive means: If set value > process value then the process value is increased; if set value < process value then the process value is reduced. Negative means: If set value > process value then the process value is reduced; if set value < process value then the process value is increased. e.g. heat regulation: if the set value is greater than the process value (room is too cold), the manipulated variable increases the process value.	- or +

4.16 Memory write

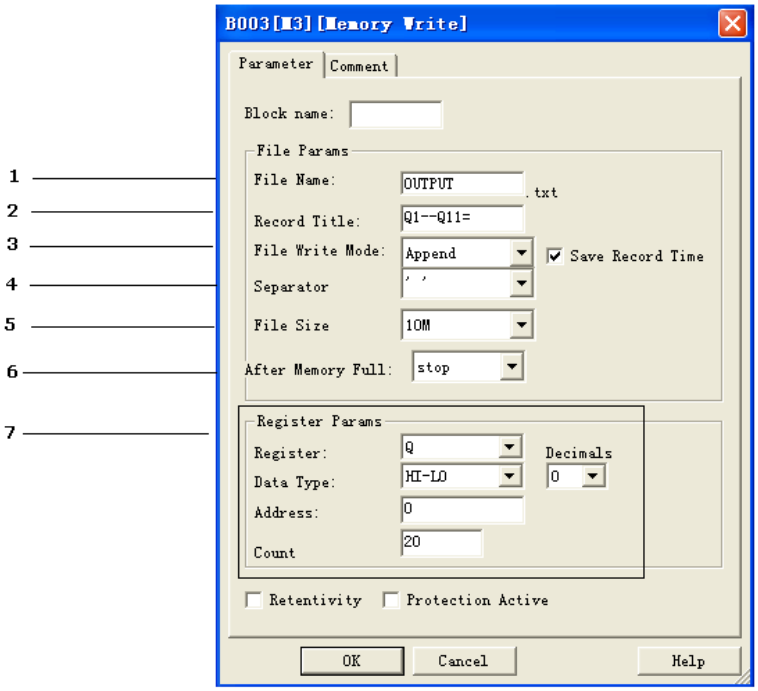


Short Description

Only when there is a low to high trigger at Trg pin, the Memory Write block will be activated and the pre-configured record action will be performed, at the same time the output will switch on if the record action had been done successfully.

Connection	Description
Trg input	Only when there is a low to high trigger at Trg pin, the Memory write Read block will be activated and the pre-configured record action will be performed. Each trigger ,only write once.
Input **R**	Reset the Memory Write block and set the output to 0 via the R (Reset) input.Reset has priority over Trg
Output **Q**	Q switches on only after Write function had been executed correctly.

Description of Memory write block's property dialog box :



1. File name

Place where you can set the name of the file used to save the registers' data

file in SD card of ELC-MEMORY

2. Record title

Below is an example in the "OUTPUT.TXT"

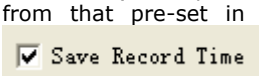
```
2011-01-30 13:52:25 Q1--Q11=1111000010000000000
2011-01-30 13:52:31 Q1--Q11=1111000010000000000
2011-01-30 13:52:37 Q1--Q11=1111000010000000000
```

The above range circled in red is just pre-set contents in the "Record title" of the Memory write block's property dialog box .

3. File write mode

Two options available: Option A. Append (This option would be selected if a certain file is already existed in the Mini SD card inserted in ELC-MEMORY)

B . Create (This option shall be chosen, if no any file existed or existed file has different name from that pre-set in the "file name" in the Mini SD card inserted in ELC-MEMORY



If such box has been ticked ,the file content will show the time when the data starts to be recorded.

2011-01-30 13:52:37 Q1--Q11=1111000010000000000

4. Separator

Such separator shall be required while more than one analog values would be stored and displayed for easier observation and convenient analysis.

5. File Size

It is an option for you to set the size of file to be stored.

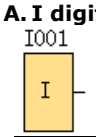
6. After memory Full

Two options can be selected after memory is full (it means the relative file has reached its pre-configured size), one is to overlay and the other is to stop recording.

7. Register params:

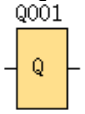
This section is for register's parameters setting. The register includes following sorts:

A. I digital inputs



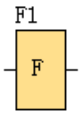
Name	Address:
I1-I8	0….7
I11-I14	8---15
I21-I24	16…23
…..	……

B. Q digital outputs



Name	Address:
Q1-Q4	0….7
Q11-Q14	8---15
Q21-Q24	16…23
…..	……

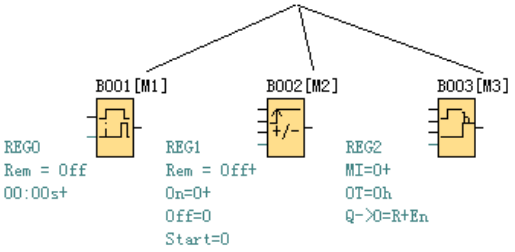
C. F digital flag



Name	Address:
F1-F64	0….63

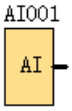
D. M

M status ,in fact it can be used to show the status of the function blocks "Hi" or "Low



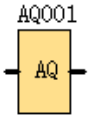
Name	Address:
M1-M512	0….511

F. AI analog inputs



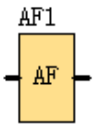
Name	Address:
AI1-AI8	0….7
AI11-AI14	8---15
AI21-AI24	16…23
…..	……

G**. AQ analog outputs**



Name	Address:
AQ1-AQ2	0….1
AQ11-AQ12	2---3
AQ21-AQ22	4…5
…..	……

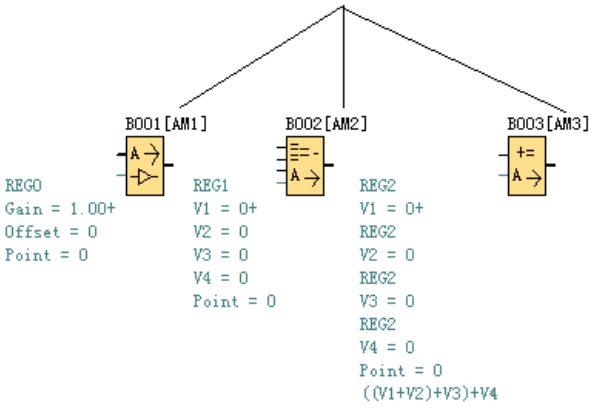
G. AF analog flag



Name	Address:
AF1-AF64	0….63

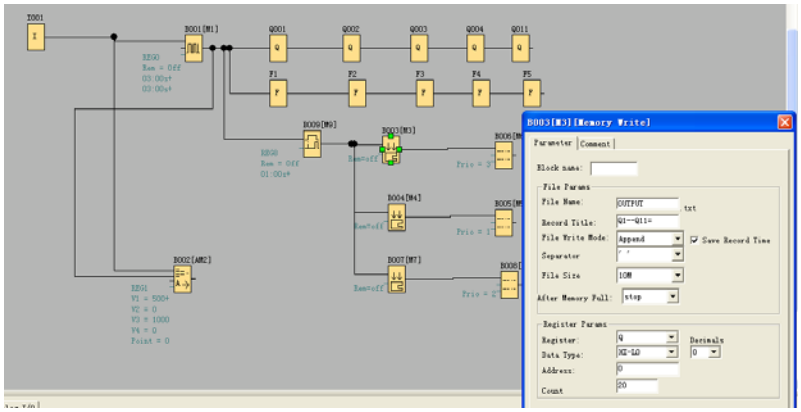
I. AM

AM value ,it can be used to display the current value of analog blocks which has analog output pin .



Name	Address:
AM1-AM512	0….511

EXAMPLE:



Please refer the property dialog box of B003 , it can record the output status .The start address is from 0 and it must record the 20 outputs with continuous addresses.

And the record file shows below:

2011-01-30 13:52:25 Q1--Q11=11110000100000000000↵
2011-01-30 13:52:31 Q1--Q11=11110000100000000000↵
2011-01-30 13:52:37 Q1--Q11=11110000100000000000↵
2011-01-30 13:52:43 Q1--Q11=11110000100000000000↵
2011-01-30 13:52:49 Q1--Q11=11110000100000000000↵
2011-01-30 13:52:55 Q1--Q11=11110000100000000000↵
2011-01-30 13:53:01 Q1--Q11=11110000100000000000↵

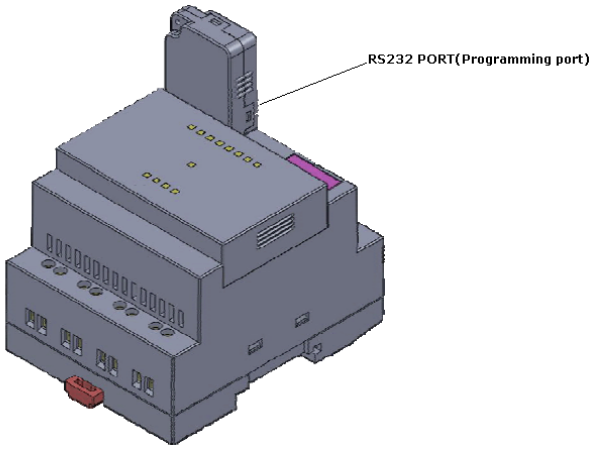
Per the program, every 6 seconds the record will do once , and the Q1,Q2,Q3,Q4,Q11 will be all

"ON". You can see the record file and you'll see the recording time and the status of the output.

Q1--Q11=1111000010000000000

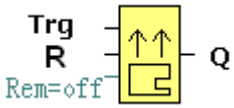
Q1-Q4 $\overline{Q11}$

Note: 1.The ELC-MEMORY only can be inserted into the RS232 port(programming port) of ELC series CPU(cannot applied to standard/economic ELC-18).



2.If this function block is working ,the RS232 port (programming port) will be occupied ,some data will be being transferred ,if you want to use the programming port for some purposes (for example download or upload program) ,you must make sure the Trg pin of this block keeps at Low status or stop the CPU by panel key.

4.17 Memory Read

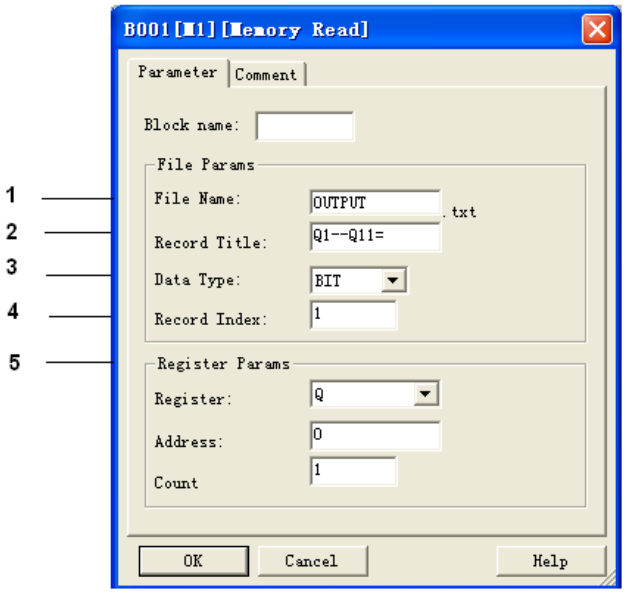


Short Description

Only when there is a low to high trigger at Trg pin, the Memory Read block will be activated once and xLogic CPU will read correlative data (bit or short) to set pre-configured register from the file in the SD card of ELC-MEMORY module, at the same time the output will switch on if the read action had been done successfully.

Connection	Description
Trg input	Only when there is a low to high trigger at Trg pin, the Memory Read block will be activated and xLogic CPU will read some data (bit or short) to set pre-configured register from the file in the SD card of ELC-MEMORY module. Each trigger ,only write once.
Input **R**	Reset the Memory Read block and set the output to 0 via the R (Reset) input. Reset has priority over Trg
Output **Q**	Q switches on only after the Read function had been executed correctly, provided .

Description of Memory write block's property dialog box :



1. File name

The name of the file which you want to access in the mini-SD card of the ELC-MEMORY module.

file in SD card of ELC-MEMORY

2. Record Title

Below is an example in the "OUTPUT.TXT"

```
2011-01-30 13:52:25 Q1--Q11=1111000010000000000
2011-01-30 13:52:31 Q1--Q11=1111000010000000000
2011-01-30 13:52:37 Q1--Q11=1111000010000000000
```

3. Data Type:

Two options available: Option A. BIT (0 or 1 ,this is used to be set the status of Q or F)
B . WORD(this is used to be set the value of AQ or AF)

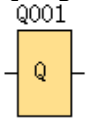
4. Record Index:

Here is used to set which line the CPU will access via this Memory Read block

5. Register Params

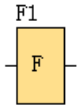
Here is to set the register parameters , all these registers have "write" property.
BIT data can be used to set the register "Q" and "F" .

Q: digital outputs



Name	Address:
Q1-Q4	0….7
Q11-Q14	8---15
Q21-Q24	16…23
…..	……

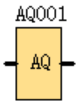
F: digital flag



Name	Address:
F1-F64	0….63

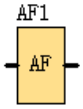
WORD data can be used to set the register "AQ" and "AF"

AQ analog outputs



Name	Address:
AQ1-AQ2	0….1
AQ11-AQ12	2---3
AQ21-AQ22	4…5
…..	……

AF analog flags

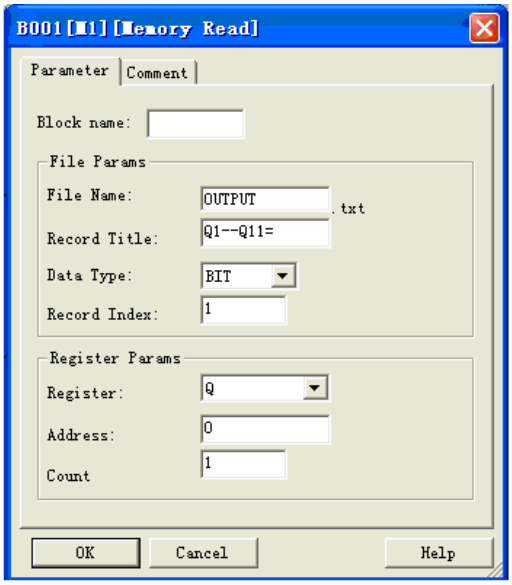


Name	Address:
AF1-AF64	0….63

Count

Here is to set how many register you want to set once.

For example

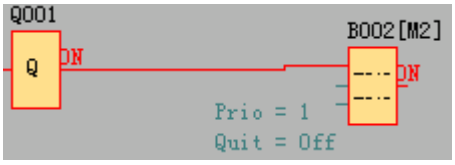


file in SD card of ELC-MEMORY

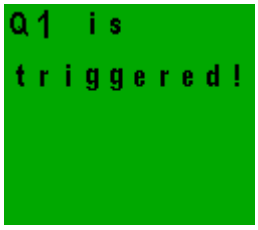
This bit would be used to set the Q1 status .

```
2011-01-30 13:52:25 Q1--Q11=1111000010000000000
2011-01-30 13:52:31 Q1--Q11=1111000010000000000
2011-01-30 13:52:37 Q1--Q11=1111000010000000000
```

If the Memory Read block had been triggered , the Q1 of ELC-12 CPU will be set "1" .



,



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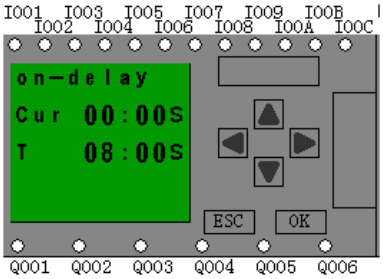
Chapter 5. Configuring xLogic

The difference between LCD message of xLogic and the traditional LCD message

In the use of traditional LCD message, it can only display some fixed and simple message such as time, I/O status etc. It can not display the counter value, timer value and analogue value. And all the LCD message screens are set and programmed by the program engineer, so users can not change, add, or remove any message screen. The operation of the traditional LCD message screen is not easy to use for the end users. Regarding the above short comings of the traditional HMI module, we have adopted a new method to develop the xLogic, and offer to user a free, and easily LCD instruction. The powerful function of the LCD (can be called HMI) is as follows:

1. Providing 32 Human-Machine Interfaces

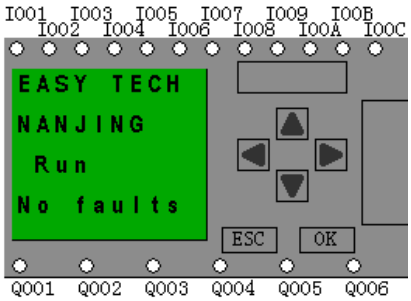
When using **xLogicsoft**, users can add HMI according to demand not more than 32. And the non-alarming interfaces can be seen on LCD panel.



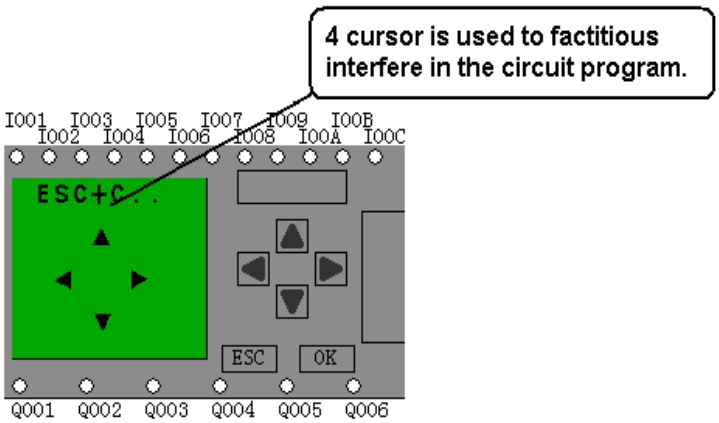
2. Providing Several HMI Blocks
Besides the system cover and system input/output blocks, the message text block can supply a large number of messages about your program. And the function of those blocks is described in chapter 3. Please read it in details.

5.1 Instruction of xLogic-HMI

After being powered on, xLogic shall self-check program stored in the main module.
If the program is accurate, then the main module will be running, meanwhile the system cover will show as follows:
In **xLogicsoft**, this interface is defaulted as its initialization screen.



If there are several parameter pages, users may press ◀ or ▶ key to go to the page you would like. The last page is the cursor mode:

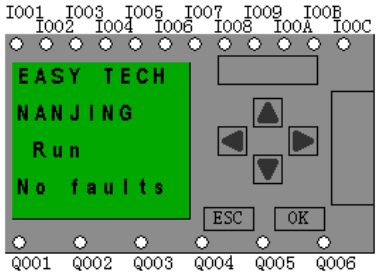


If xLogic has several alarm interfaces in the same period and it only displays the message with highest priority in the block, also you may go through all alarm messages by pressing ▲ or ▼ key.

Note:
The message text block would be treated as parameter page only when it has no input, otherwise, it may be regarded as alarm page. When input has high pulse, LCD shall display alarm message.

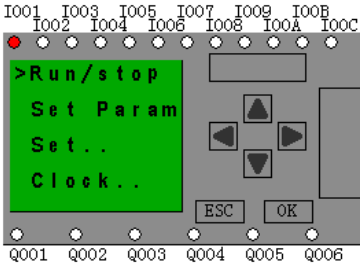
5.2 Select function page

Press ESC key to change from running mode to function page.



Press ESC...

After pressing ESC key, xLogic would be switched to function page and meanwhile open function menu as figure below shows.



Brief introduction on four options of function page:

- **Run/stop**

Select this menu to switch over xLogic status between RUN and Stop. Refer to chapter5.2.1 for details.

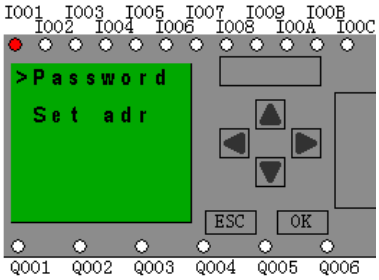
- **Set Param**

To set function block parameter. Refer to chapter 5.2.2 for details.

- **Set...**

Used to set /modify password and set address of expansion module, refer to chapter 5.2.3 and 5.2.4 for details please.

1. "Press ▲ or ▼ key to move the cursor to "**Set...**.
2. Then press **OK** key ,xLogic will display as follows:



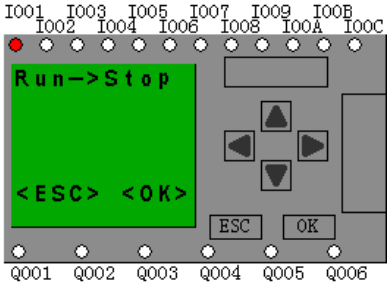
- **Clock**

To set and modify date and time .Refer to chapter 5.2.5 for details.

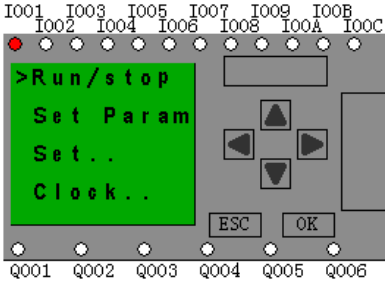
5.2.1 Run/Stop(Switch Over between Run & Stop Mode of the Main module)

You should first select FUNCTION PAGE. (Read 5.2)

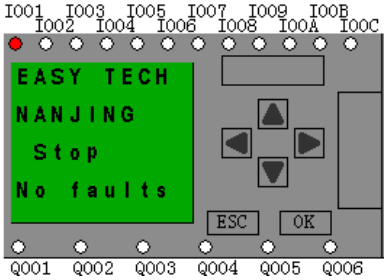
1. Move the cursor to "Run/stop": Press ▲ or ▼ key.
2. Confirm "Run/stop": Press OK key.



3. Then press the OK key.



After pressing ESC key, you'll find out your circuit program has changed to "stop" status as figure below shows:

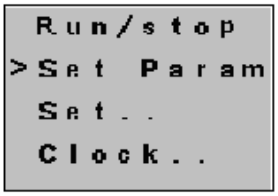


Summary:
You can transform the state (RUN or STOP) of the program via the three steps mentioned above.

5.2.2 Set parameter

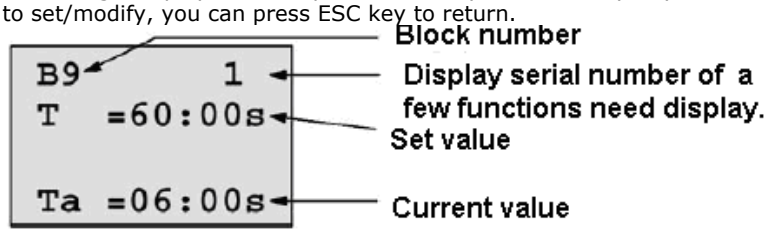
If you want to select a parameter, you may follow the following procedures:

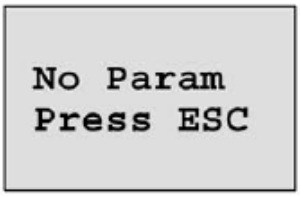
1. Under the FUNCTION PAGE, select
"Set parameter": Press ▲ or ▼ key



2. Confirm by pressing **OK** key.

Then xLogic displays the first parameter, so you can modify as you like. If there is no parameter to set/modify, you can press ESC key to return.





No parameter to edit ,press ESC key to return.

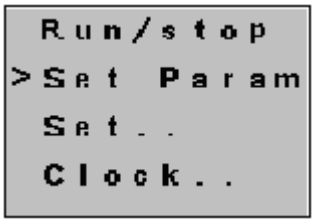
3. Select parameter you intend to modify.

4. Select certain specific value of that parameter which you want to edit, then press OK key.

How to modify parameter?

A. First select certain parameter which you need to edit by following the below procedures:

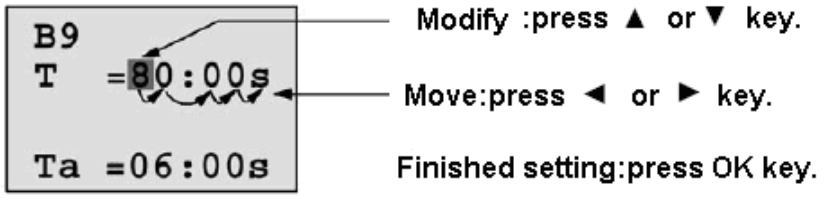
1. Under the FUNCTION PAGE, select
"Set parameter": Press ▲ or ▼ key



2. Confirm by pressing **OK** key.

B. then you can perform the below actions to modify parameter:

1. Move the cursor to the parameter to be modified: press ◀ or ▶ key.
2. Modify value: press ▲ or ▼ key.
3. Confirm the value after modification: press OK key.

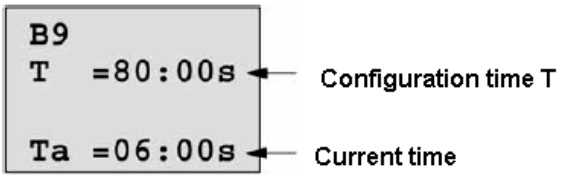


Note:

When xLogic is running, not only time value but also time unit(S,M,H) can be altered , but Besides alter time parameter at RUN time ,you can alter time base(s=second, m=minute ,h=hour).

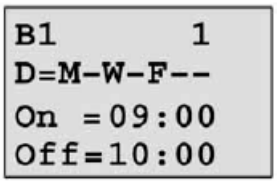
Current value of time T

View time T in parameter mode:



You are allowed to modify configuration time. Switch on/off time for a time segment.

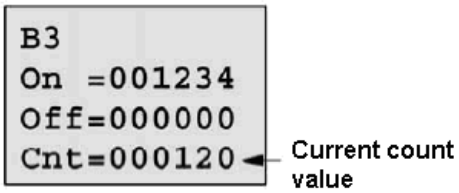
In parameter mode, time segment figure of a timer:



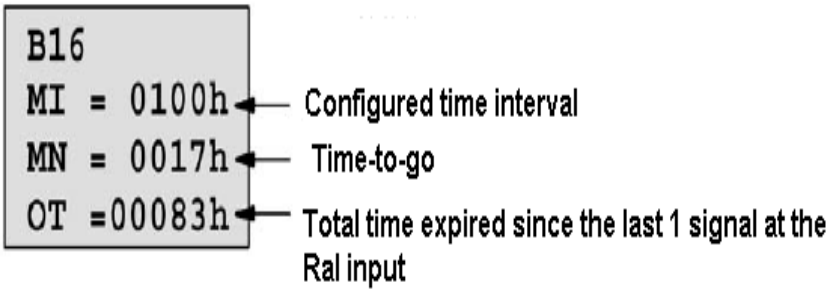
You can alter the time and date of switch on/off.

Current value of counter

In parameter mode, the parameter view of a counter:



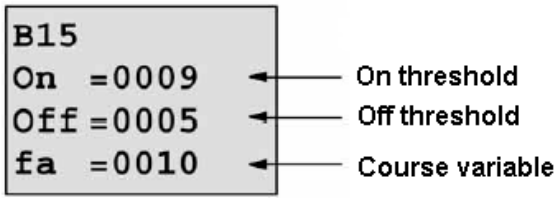
Current value of hour counter
In parameter mode, the view of hour counters:



You can edit configured time interval (MI).

Current value of threshold trigger

In parameter mode, the view of threshold trigger:



You can alter the threshold value of switching on /off.

5.2.3 Set password

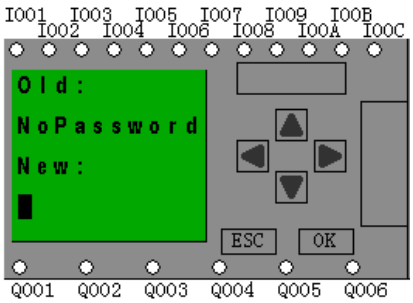
xLogic supply password protection function for your program. You can choose according to your need. See the following instruction; you'll understand the method of setting password.

Set one password

A password contains less than or equal to 4 characters and each character is Arabian number from 0 to 9 .It is easy to specify, edit or remove the password directly on the xLogic in the "Password" menu of the function page:
You should first select the FUNCTION PAGE. (Read 5.2)

1. Move the cursor to "Password": Press ▲ or ▼ key.
2. Confirm "Password": Press OK key.

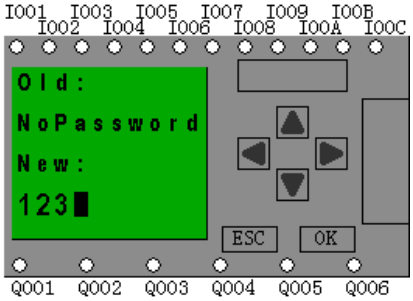
Example: let us set "1234" as password for a program. Now the LCD displays the following interface:



Select "**New**" option, and then edit it.

3. Select "1": press ▲ key once.
4. Move the cursor to the next character: press ▶ key.
5. Select "2": press ▲ key twice.
6. Move the cursor to the next character: press ▶ key.
7. Select"3": press ▲ key three times.
8. Move the cursor to the next character: press ▶ key.
9. Select "4": press ▲ key four times.

Now display:



10. Confirm password: press OK key.
Now, the program is protected by the password of "1234", and then you can return to the main menu.

Note:
You can cancel a password newly-set via ESC key. In this instance, xLogic will return to main menu and not reserve that password. You also can use **xLogicsoft** to set your password. You are not allowed to edit the program protected by password or transfer it to **xLogicsoft** unless you input a true password previously.

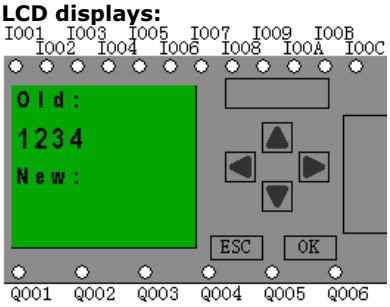
Modify password:

In order to modify password, you are required to present current password.

In the menu of the FUNCTION PAGE.**:**

1. Move the cursor to "Password": Press ▲ or ▼ key.
2. Confirm "Password": Press OK key.
Select "Old" and input primary password (in our instance is "1234"), the process is the same as the step 3 to step 10 mentioned above.

LCD displays:

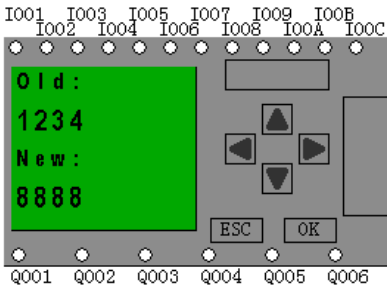


Thus, you could select "New" to input new password such as "8888":

3. Select "8": press ▲ key.

4. Move the cursor to next character: press ▶ key.
Repeat the step 3 and 4 to realize the third and fourth character.

LCD displays:



4. Confirm new password: press OK key.
So you have set the new password and then return to main menu.

How to remove the password:

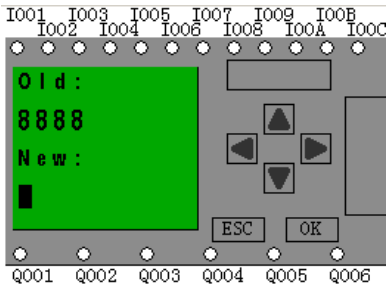
In case you need to remove password .e.g. allow the other users to edit your program, then you must know the current password. The process of removing password is the same as that of modifying password.

In the menu of the FUNCTION PAGE. :

1. Move the cursor to "Password": Press ▲ or ▼ key.
2. Confirm "Password": Press OK key.

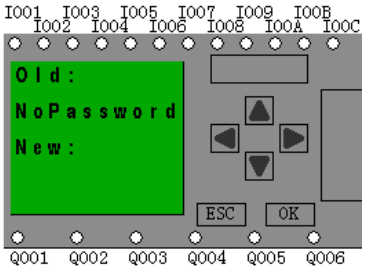
Select "Old" and input primary password (in our instance is "1234"), the process is the same as the step 3 to step 10 mentioned above.

LCD displays as follows:



Input nothing under the "New", and let it keep blank to clear password.

5. Confirm "blank" password: press OK key. Now you have cleared password and return to main menu. If you want to set password next time ,the LCD will display:



Note:
This action (removal) make password alert be prohibited, so your program can be reserved without password.
If users input wrong password and press OK key, xLogic doesn't enter edit page and return to main page. This process repeats again and again until you input the correct password.

5.2.4 How to set address of expansion module

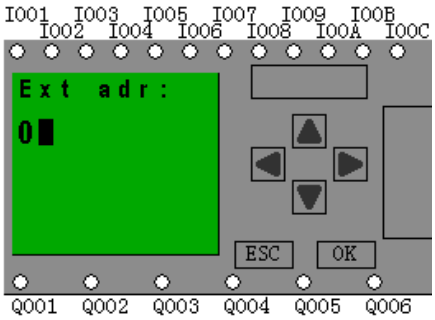
You may set expansion module address to realize communication with main module of xLogic.

Part 1 ： Set address of ELC-18 CPU'S extensions.(ELC-E-16DC-D-R etc..)

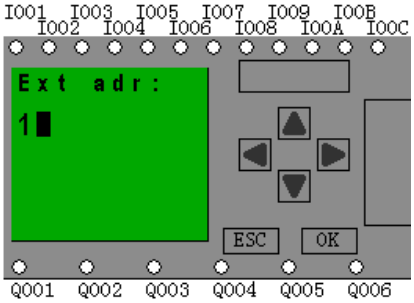
Notes: 1.Only one expansion module shall connect to CPU when you set expansion module address via panel key of CPU.
2.Up to 9 expansion modules(9 digital/analog extensions) can be linked together.
3. If more than one expansion module connect to CPU at the same time ,the address of

expansion module must be different each other, otherwise the system(CPU+expasnions) would run abnormal.
You shall first select the FUNCTION PAGE. (Read 5.2)

1. Press ▲ or ▼ key to move the cursor to "Set address":
2. Press OK key to confirm "Set adr":
3. Press ▲ or ▼ key to move the cursor to "Set E adr:.
4. Press OK key to confirm "Set E adr":

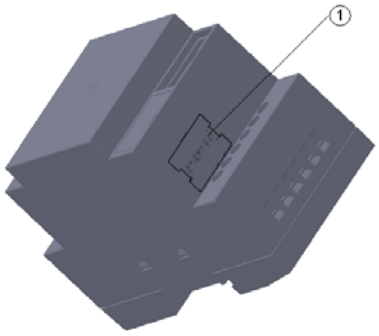


3. Move the cursor to the place of parameter to be modified by pressing ◀ or ▶ key.
6. Modify value by pressing ▲ or ▼ key.



7. Confirm the value after modification by pressing OK key.

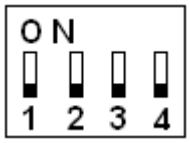
Part 2 ： Set address of ELC-12 CPU'S extensions.(ELC12-8DC-DA-R etc..)



1. Plastic slice

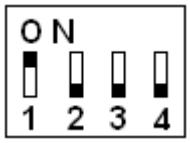
Step 1: Using a screwdriver , take the plastic slice down and we'll find a dial swith.
2: Dial the switch as the below instructions to set the address what you need.

Up to 8 extensions (includes IO ,AQ,AI,PT100 modules) can be connect with the CPU .
The default address of ELC12 extensions is 1 and the dial switch as below:
Address 1:

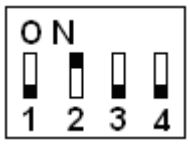


■ means the swith position

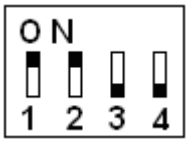
Address 2:



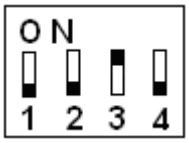
Address 3:



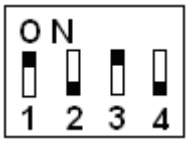
Address 4:



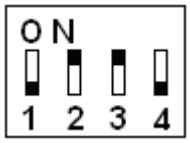
Address 5:



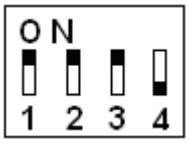
Address 6:



Address 7:



Address 8:

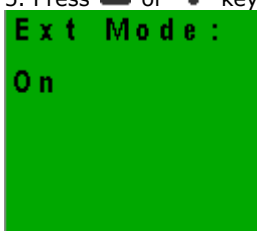


The expansion port of CPU must be open when the using extensions . Following is tell you how to open the expansion port:

You shall first select the FUNCTION PAGE. (Read 5.2)

1. Press ▲ or ▼ key to move the cursor to "Set address":
2. Press OK key to confirm "Set adr":
3. Press ▲ or ▼ key to move the cursor to "Set E adr:.
4. Press OK key to confirm "Set E adr":

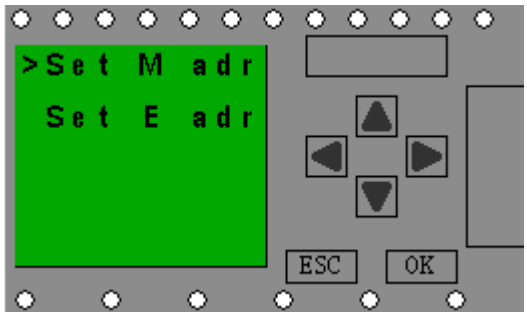
5. Press ▲ or ▼ key to select "on"



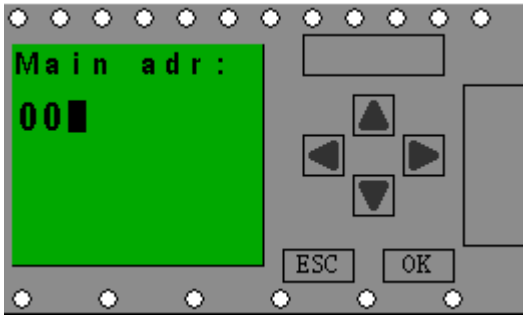
Notes: 1. The address setup of the extension module must be before powering on. Modification after powering off will be ineffective.
2. Freely connection , need not care the power type between CPU and extensions ,that means the AC type module also can be connected with the DC type module.
3.Power on the CPU and the extensions on the same time ,in the other words the power on time of extension module do not later than the CPU's ,otherwise ,the communication between CPU and extension may not be established.
4. If the communication is establised between CPU and extensions , the indicator on the top of the extensions' house will turn to RUN(green colour).
3. If more than one expansion module connect to CPU at the same time ,the address of expansion module must be different each other, otherwise the system(CPU+expasnions) would run abnormal.

5.2.5 Set CPU address

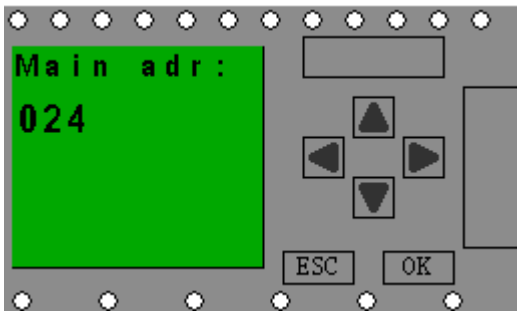
If more than one CPU in a network and the address of CPU would be set differently each other. Besides you can change the address of CPU via xLogicsoft, it can be set via panel key also. The address range is from 1 to 247



Click "ESC" to cancel and click "OK" to confirm



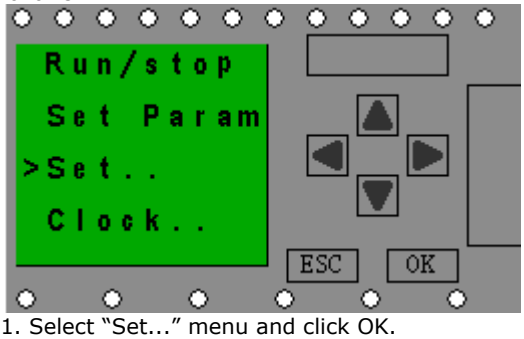
Set with pressing ▲ or ▼ key.



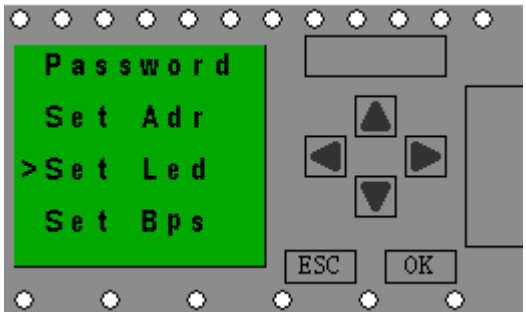
Click "ESC" to cancel and click "OK" to confirm

5.2.6 Set backlight

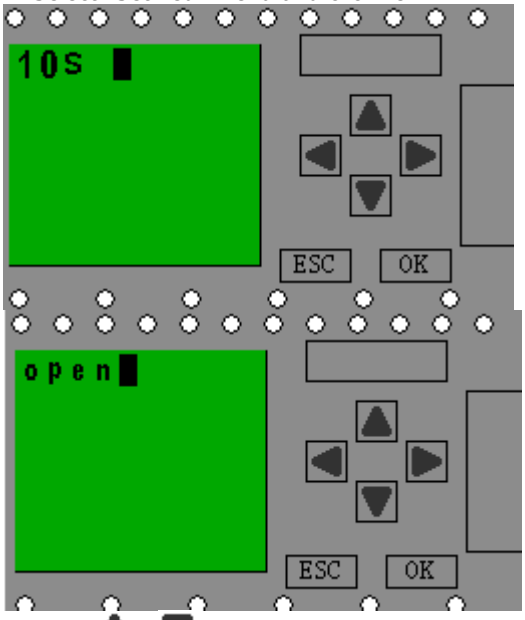
The backlight of CPU can be set "ON" time as 10 sec or "ON" all the time. The setting way as follows:



1. Select "Set..." menu and click OK.

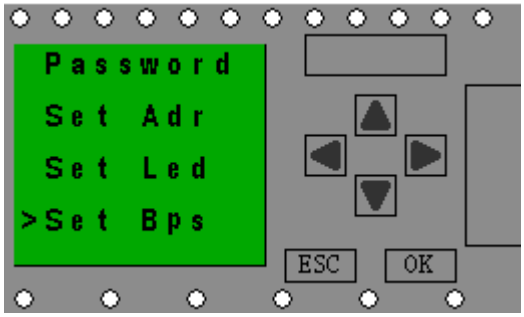


2. Select "Set Led" menu and click "OK"

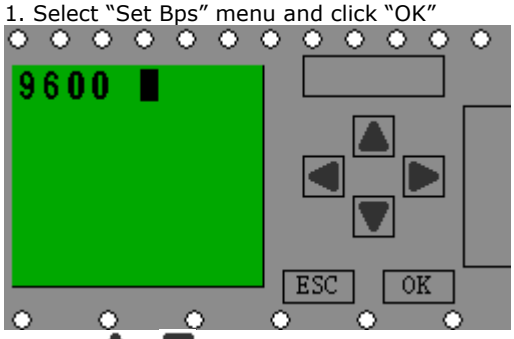


3. Press ▲, ▼ key, two options can be selected, Confirm your selection by pressing "OK"

5.2.7 Set baud rate of CPU



1. Select "Set Bps" menu and click "OK"



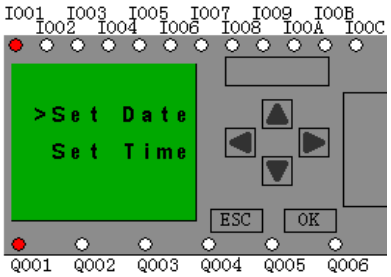
2. Press ▲ , ▼ key."4800", "9600","19200","38400" can be selected and the default is "9600"
3. Confirm your selection by pressing "OK"

Note: The ELC-RS232 cable cannot support "38400" ,so if your communication network need use ELC-RS232 cable , then the baud rate need less than "38400".

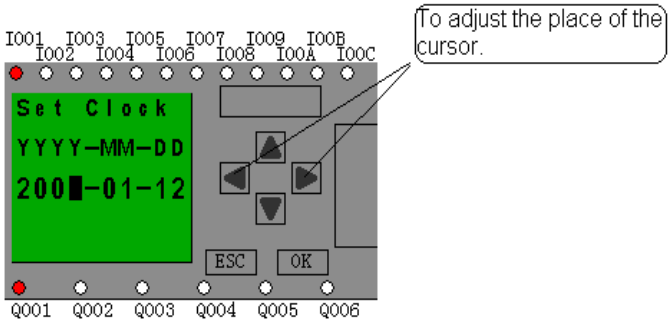
5.2.8 Modification Methods of System Time

You should first select the FUNCTION PAGE. (read 4.2)

1. Move the cursor to "Clock": Press ▲ or ▼ key.
2. Confirm "Clock": Press OK key.

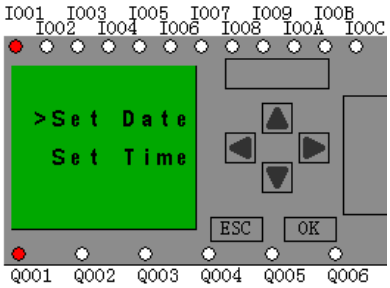


Press OK key to set and modify date.

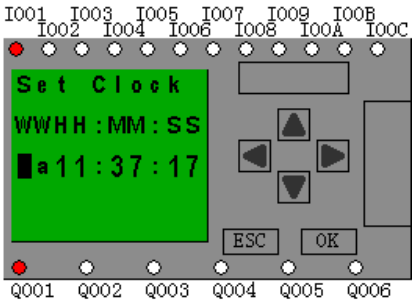


Press ▲ or ▼ key to realize the date which you want to set .After you finished your setting,

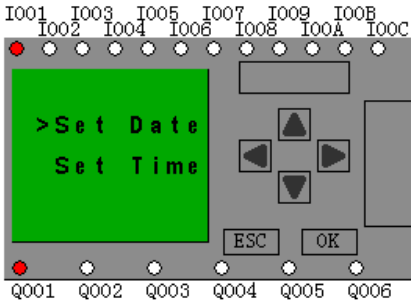
press OK key to return to:



If you want to set the time further, please move the cursor to" Set Time" menu, then press OK key:



Here you can set week day (From Monday to Sunday) and the clock. The method is similar to above. After completion of your setup, press OK key:



Press ESC key and return to FUNCTION PAGE.

Chapter 6. Application

In order to let users know the far going application field of xLogic, we present a set of application example. Each instance includes the circuit program of its original solution and the compare of solution in which xLogic has been applied.

You can find the following solution:
Dual-function switch
Automatic gate
Ventilation system
Industry door
Daylight lamps
Rain water pump

Note:
The application example of xLogic is available free of charge to our clients, but we can't make any promise, it is only to explain the general rule of using xLogic. It is possible that these instances can be different from user's specific application, so user should take all related responsibility of running those instance systems, and we sincerely suggest user shall refer to relevant nation standard and installation rules related to systems. Also, we have to point out that error is unavoidable, and we reserve the according modification rights.

6.1 Dual-function switch

Requirements for stairway lighting systems

The basic requirements for a stairway lighting system are as follows:

- When someone is using the stairs, the stairway lights should be on.
- If no one is in the stairway, the lights should go out in order to save energy.

6.1.1 Standard solution

Up to now two methods were known to control such a lighting system:

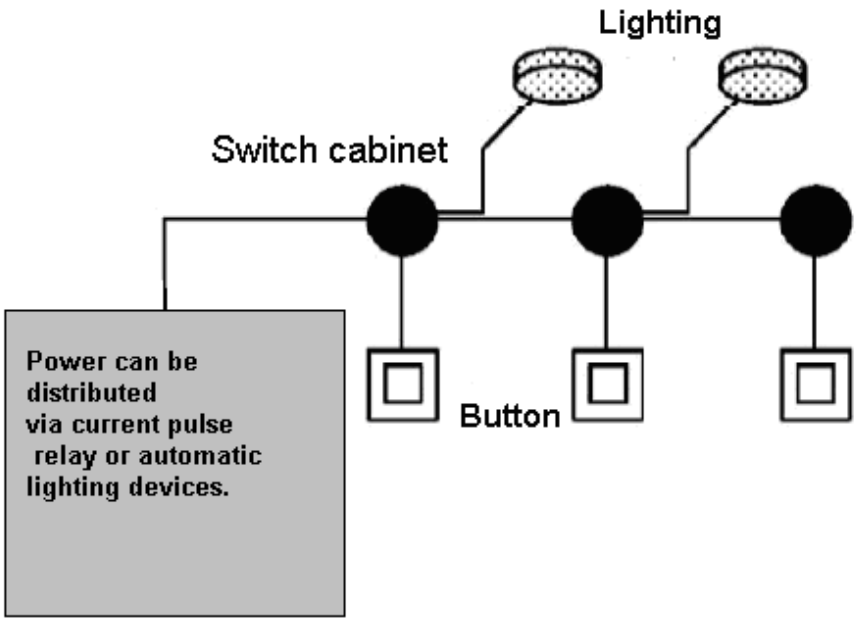
- Pulse relay: When the lights are off, press any of the pushbuttons to switch on the lights. When the lights are on, press any of the pushbuttons to switch off the lights again.

  Disadvantage: People often forget to switch off the lights

- Automatic stairway light switch: Press any one of the pushbuttons to switch on the lights. The lights switch off again automatically when a preset off delay time has expired.

Disadvantage: You can't keep the lights switched on over an extended period of time. The permanent on switch, usually installed inside the stairway lighting timer unit, may be difficult or impossible to access.

The wiring is the same for both systems.



xLogicSoft solution

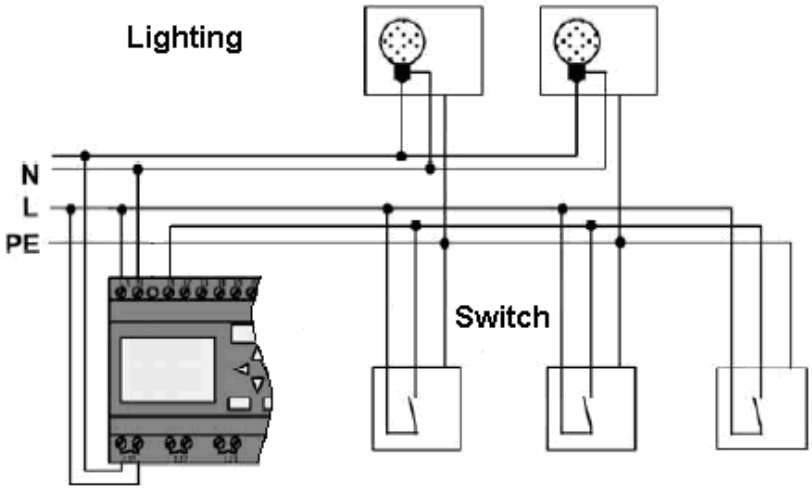
The xLogic system can replace the automatic stairway light switch or the pulse relay.

xLogic also lets you create a simple automatic stairway light switch via the stairway light switch SFB.

You can also implement both functions (off delay timer and pulse relay) in a single unit. What is more, you can incorporate extra functions without making any alterations to the wiring. In our example program, we have combined the advantages of both the current impulse relay and the automatic stairway lighting timer as follows:

- Actuate the pushbutton → The light is switched on and switched off again on expiration of a predefined time.
- Hold the pushbutton down → Switches on continuous lighting
- Press the pushbutton once more → Switches off the lighting

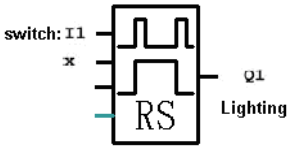
6.1.2 The scheme of ELC18AC



The wiring of a lighting system with xLogic is the same as standard corridor or stairway lighting systems. Only the automatic lighting timer/pulse relay is replaced.

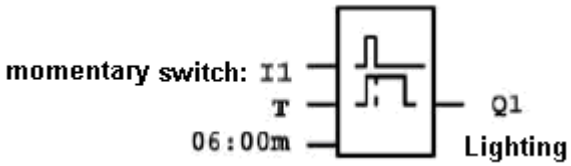
xLogic lets you quickly and easily combine all those functions in a single dual-function switch SFB, without additional wiring and expenditure.

Apply pulse relay of xLogic



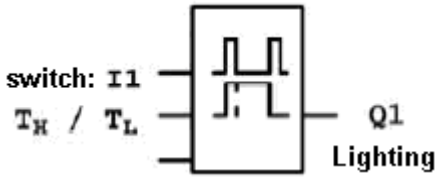
When the input "I1" has a pulse, the output "Q1"will be on or off.

Automatic stairway lighting system



If input "I1"has a pulse, the output Q1will be on and keep 6 minutes, then be off.

Apply xLogic to realize multiple switches



When the input "I1" has a pulse, the output "Q1" will be on and not off until the period"TH"be over.
Keep the momentary switch holding down in period "TL", the light will be on all the time.

Select special function

The following selection can be done as special function or saving energy sources:

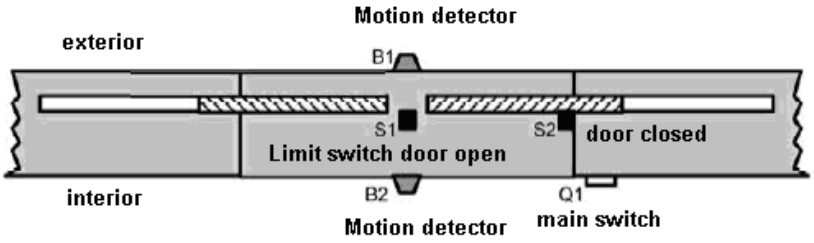
- The lighting flicker before it gets off automatically.
- You can integrate different central control functions:
  - Central control off
  - Central control on(emergency button)
  - Control all lighting or certain single circuitry by lighting control switch.
  - To control by integrated timer.

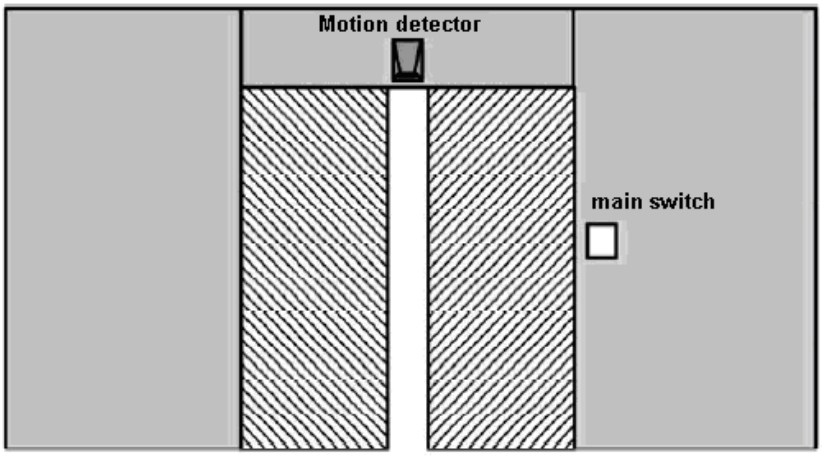
6.2 Automatic gate

In the entrance of supermarket、public building、bank、hospital etc，automatic gate is often used.

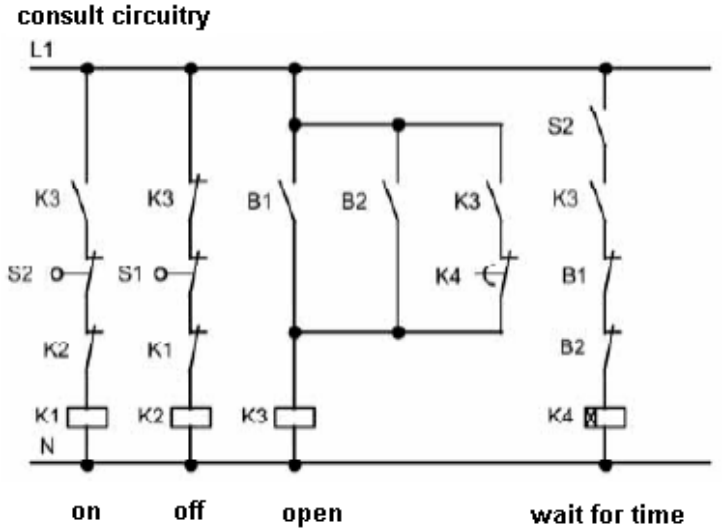
The requirement of automatic gate

- If some people approach to gate, it will be opened automatically.
- The gate must remain open until there is no person on the passageway.
- If there is no person on the passageway, the gate must be off automatically in a minute.



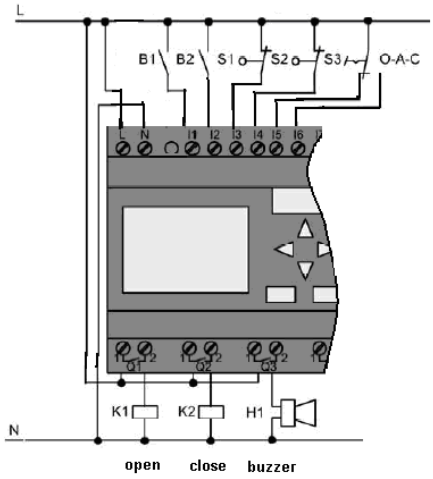


6.2.1 Standard solution



As long as the detector B1 or B2 detects someone approach, the switch K3 will be on and open the door.
If the two detectors don't detect person in a short time, trigger K4 and close the door.

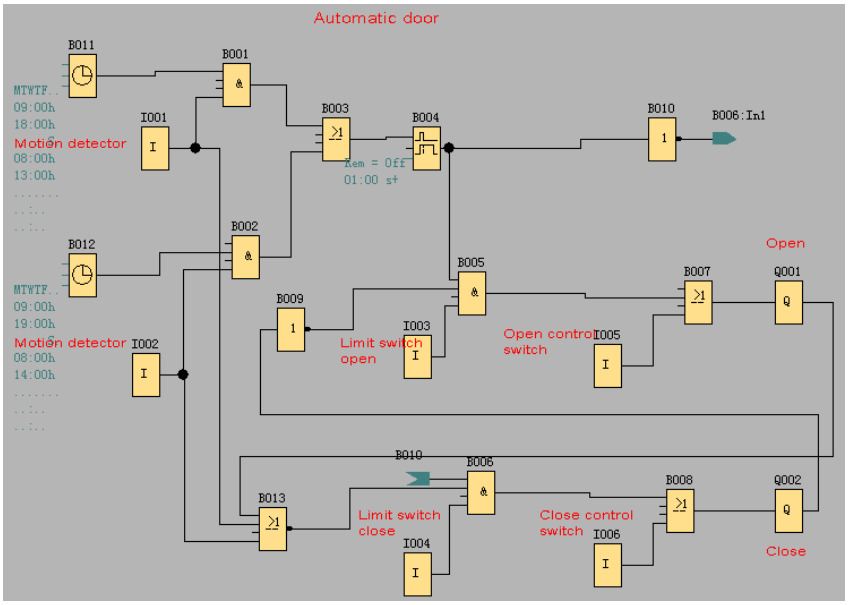
6.2.2 The scheme of ELC 18 AC



Required components:

- K1 open contactor
- K2 close contactor
- S1(break contact) close limit switch
- S2(break contact) open limit switch
- B1(make contact) outdoor infrared action detector
- B2(make contact) infrared action detector inside

xLogic function block circuit program:



Motion detector

During business hours, if someone enters store, the detector B1 will trigger electric motor to open the door, vice versa.
At closing time, the detector B2 make electromotor keep running for an hour to make more time for customer to leave.

Trigger electromotor for opening door

The output Q1 is switched on and triggers electromotor, when:
- Operate control switch I5(the door is open all the time)
- The detector indicates that somebody is approaching to the door.

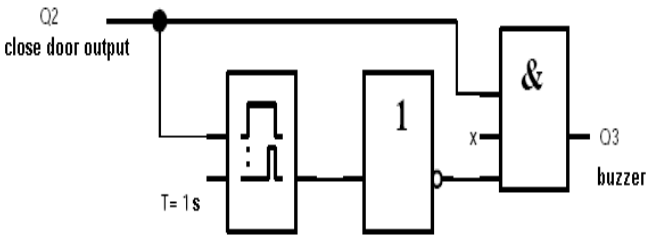
- The door has not been opened entirely (I4 limit switch is not off.).

Trigger electromotor for closing door

- Operate control switch I6(the door is closed all the time)
- The detector indicates that nobody is approaching the door.
- The door had not been closed entirely (I3 limit switch is not off).

Buzzer

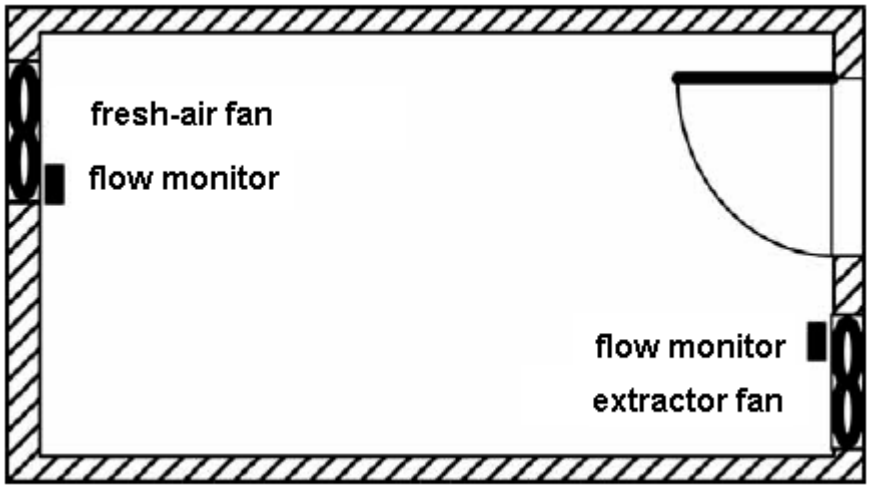
Connect the buzzer to output Q3.When the door is going to be closed; the buzzer gives off sounds for a short time (1s in this example). To attach buzzer, need to connect the following circuit program to output Q3.



6.3 Ventilation system

Requirements for a Ventilation system

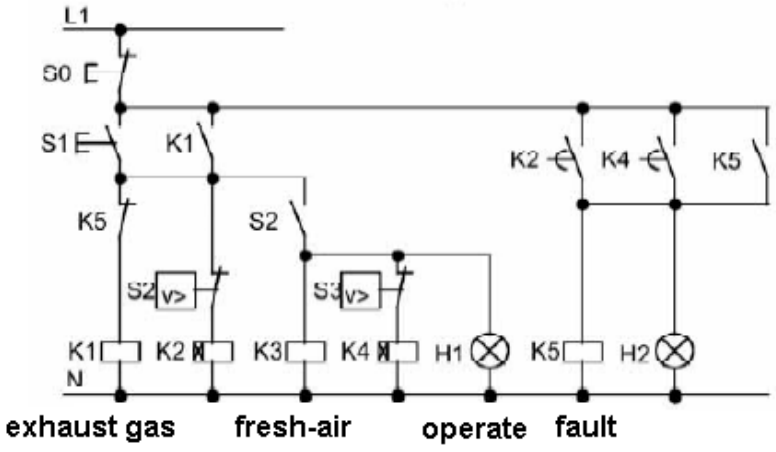
A Ventilation system supplies fresh air into a room and exhausts the contaminated air. Let us look at the following sample system:



- A room contains an extractor fan and a fresh-air fan.
- Each fan is monitored by means of a flow sensor.
- The pressure in the room may rise above the atmospheric pressure.
- The fresh-air fan may only be switched on if the flow sensor signals the safe operational state of the extractor fan.
- A warning lamp indicates failure of one of the fans.

6.3.1 Standard solution

The control circuit diagram of Ventilation system formerly as follows:

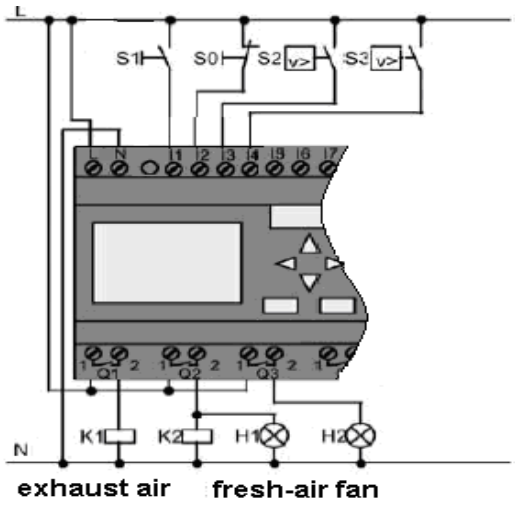


The fans are monitored by means of flow sensors. If no air flow is registered after a short delay time has expired, the system is switched off and an error message is generated, which can be acknowledged by pressing the off button.

Fan monitoring requires an analyzer circuit with several switching devices, in addition to the flow sensors. A single xLogic device can replace this analyzer circuit.

6.3.2The scheme of ELC-18 AC

The circuit diagram of ventilation system:



Required components:

- K1 Main contactor
- K2 Main contactor
- S0(make contact) Off button
- S1(make contact) On button
- S2(break contact) Flow monitor
- S3(break contact) Flow monitor

- H1 Flashing lamp
- H2 Flashing lamp

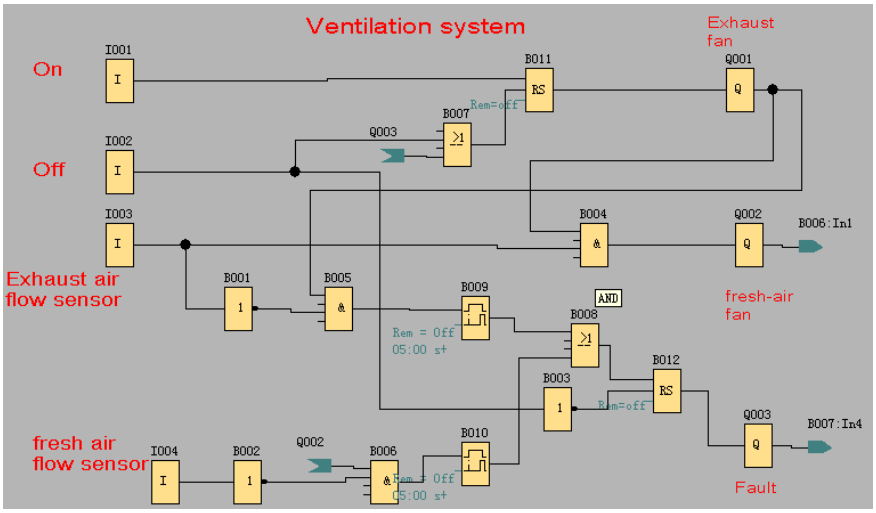
xLogicSoft solution

The use of xLogic reduces the amount of switchgear. Thus, you save installation time and space in the control cabinet. You may even be able to use as a smaller control cabinet.

With xLogic you can also switch off of the fans sequentially after the system is switched off.

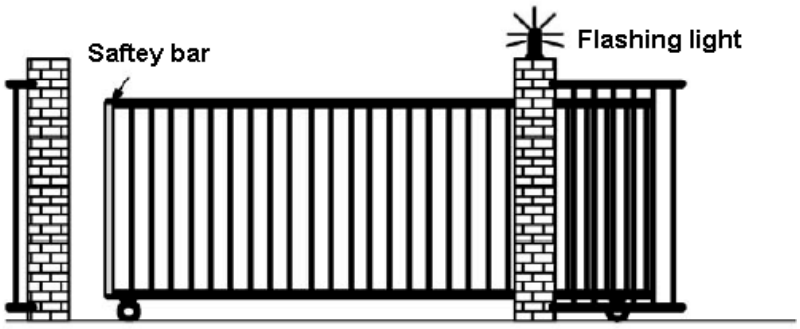
The circuit in xLogicSoft

The system is switched on and off at the inputs I1 and I2. The fans are connected to outputs Q1 and Q2, the flow sensors are connected to the inputs I3 and I4. Blocks B07 and B10 are used to set the watchdog times after which the flow sensors should send a signal to the fault output Q3.



You can invert output Q3 to use output messages at Q4. Relay Q4 only drops out if main power is lost or if there is a fault in the system. The output can then be used for a remote message.

6.4 Factory door



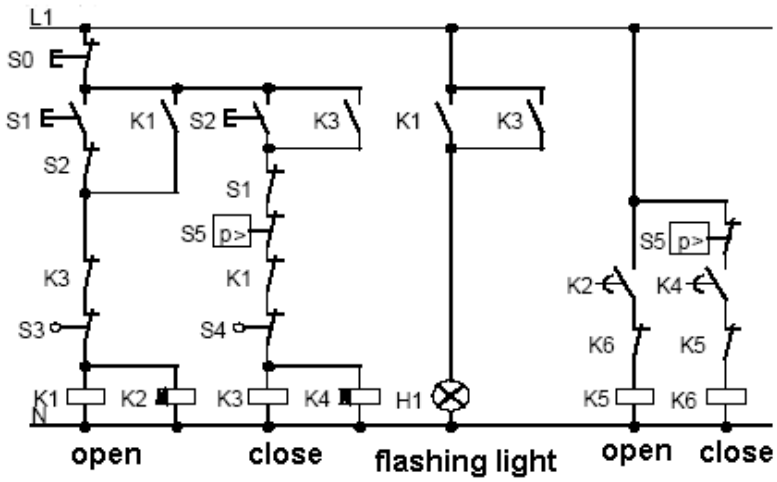
Requirements for a gate control system

In many cases a factory entrance is closed with roll gates. Those gates are only opened when vehicles need to enter or leave the factory grounds. The gate is controlled by the porter.

- The sliding gate is opened and closed by means of a pushbutton control in the gatehouse. The porter can monitor the gate operation.
- The roll gate is normally fully opened or it is closed. However, gate movements can always be interrupted.
- A flashing light is activated five seconds before the gate moves, and while the gate is in motion.
- A safety pressure strip ensures that people are not injured and that no objects are trapped and damaged when the gate is closing.

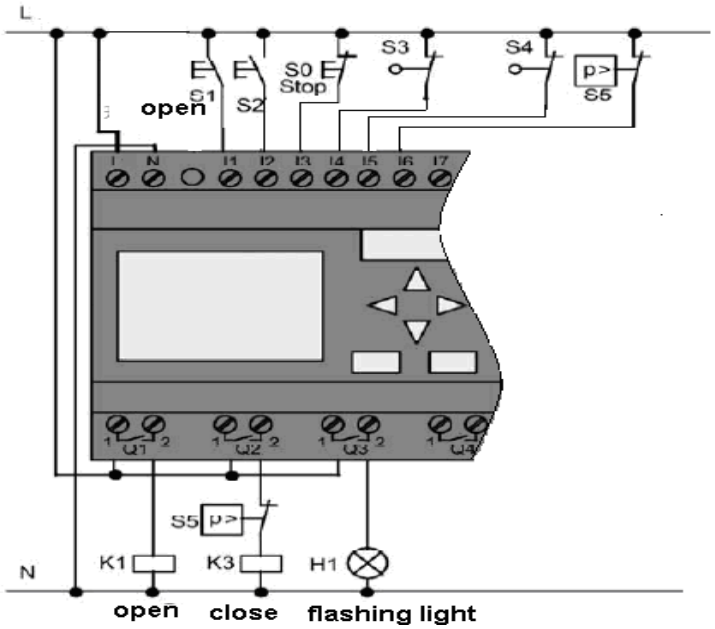
6.4.1 Standard solution

There are many different control systems for operating automatic gates. The OPEN and CLOSE buttons initiate gate movements into the relevant direction, provided it is not already moving in the opposite direction. Movement of the gate is terminated either by means of the STOP button or the relevant limit switch.



6.4.2 The scheme of ELC-18 AC

The circuit diagram of industry gate:



Required components:

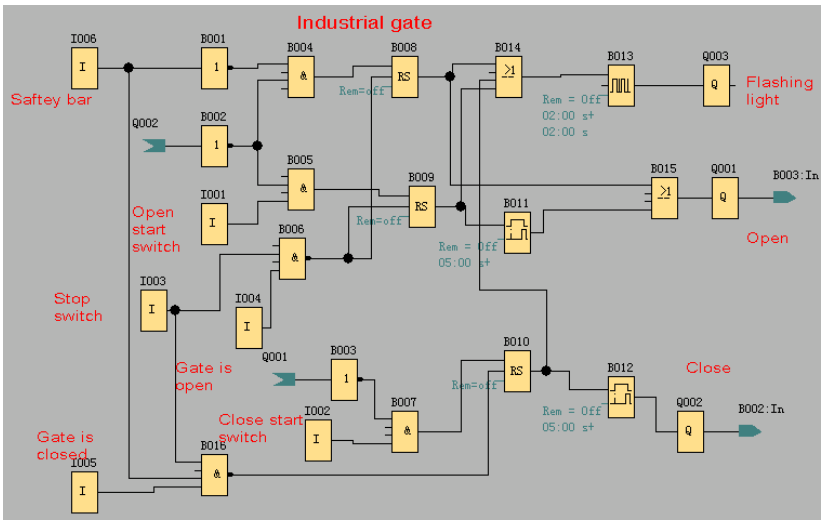
- K1 Main contactor
- K2 Main contactor
- S0 （break contact） Off button

- S1 （make contact） Open button
- S2 （make contact） Shutdown button
- S3 （break contact） Open position sensor
- S4 （break contact） Shutdown position sensor
- S5 （break contact） Safety bar

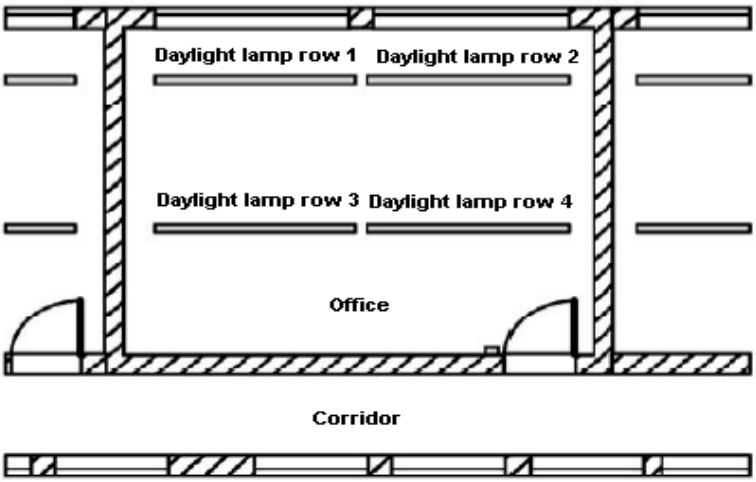
xLogicSoft solution

A xLogic circuit provides a further feature compared to standard controls: The actuation of a safety bar interrupts the closing motion of the gate. Five seconds before the gate is opens or closes, a flashing light is activated and signals the start of the movement. It continues flashing until the gate has stopped.

In contrast to standard solutions, xLogic offers an easy and economic means of modifying the control system.



6.5 Daylight lamp system

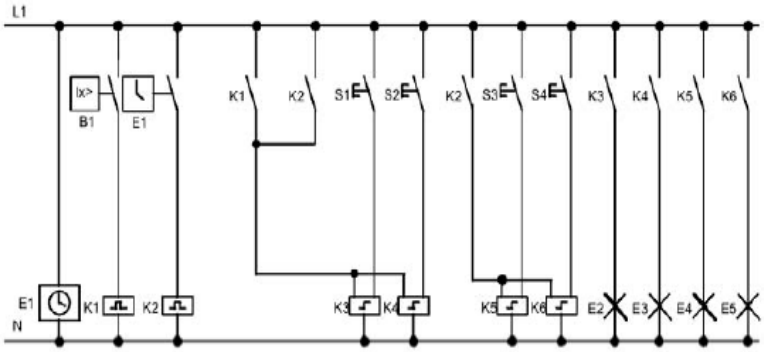


Requirements for lighting system:

- Different daylight lamp rows should be able to be switched on and off handily.

- If window at one side has enough light, the light will be switched off automatically via lightness sensitivity switch.
- The light would be switched off automatically at 8:00 p.m.
- The light can be switched on and off manually at any time.

6.5.1 Standard solution



Lighting lamp can be operated by pulse relay controlled by button besides the door. Pulse relay can be repositioned by means of timer and lightness sensitivity switch. Pulse relay may shorten pulse width of "off command".

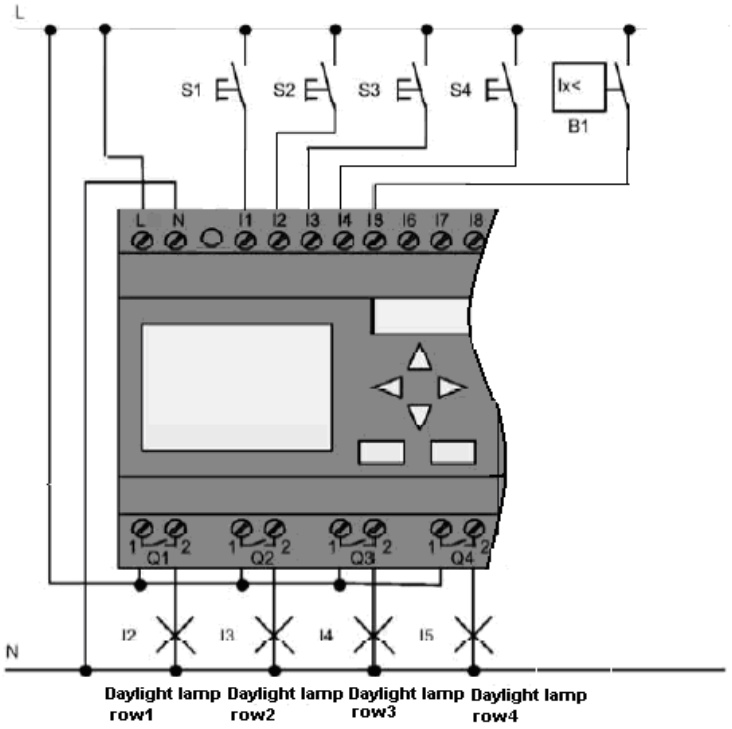
Required component:

- Button S1—S4
- Daylight control switch B1
- Timer T1
- Pulse relay K1 and K2
- pulse switch K3—K6 able to be switched off collectively

Disadvantages of traditional solution:

- In order to realize function, it needs plenty of wiring.
- Vast mechanical parts will result in obvious abrasion and high maintenance costs.
- Modification function can cause much work.

6.5.2 The scheme of ELC-18AC

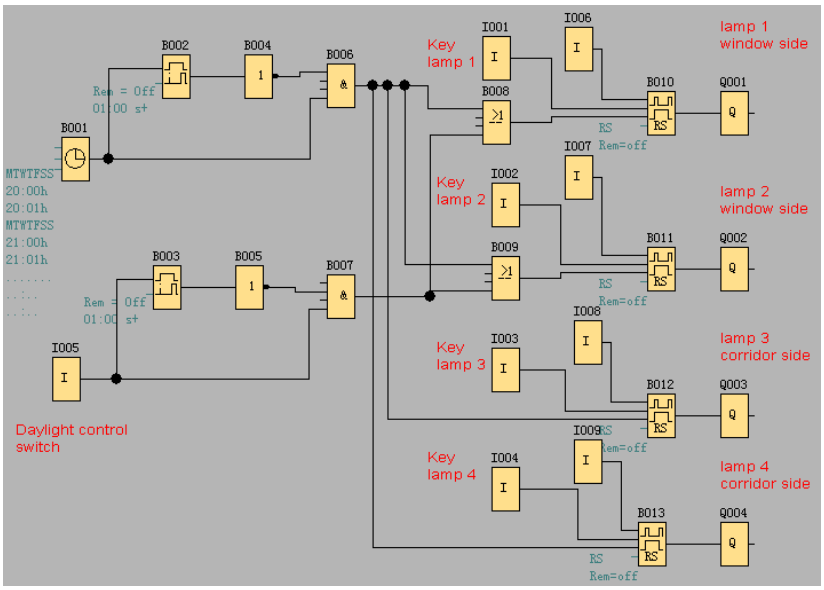


Components:

- S1—S4(make contact) Momentary switch

- B1(make contact) Daylight control switch

Circuit diagram by xLogicsoft:



Benefits:

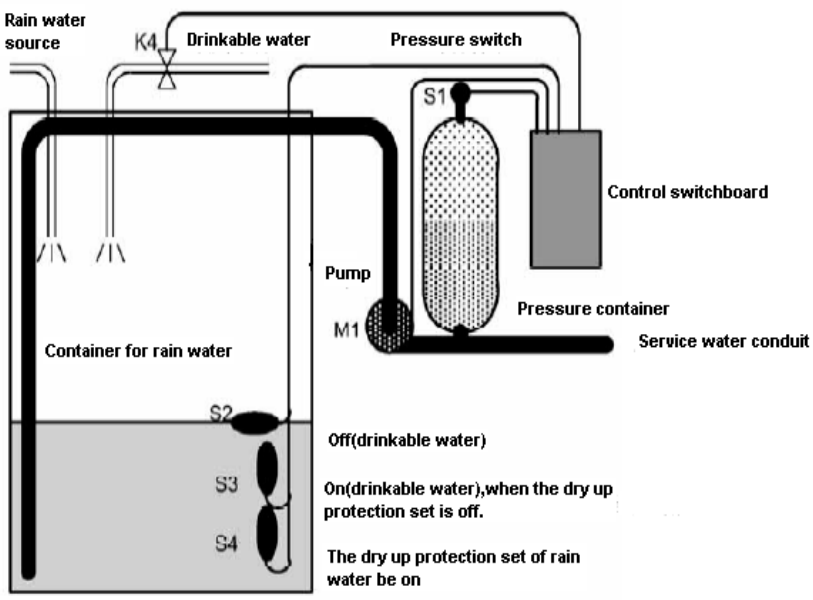
- While power consumption of load does not exceed output of switch's voltage range, lamp can be directly connected to xLogic main module; however, if power consumption of load exceeds output of switch's voltage range, then power contactor would be required.
- You can connect directly lightness sensitivity switch to the input of xLogic.
- Don't need external timer, as this function has been integrated in xLogic.
- It can be installed in a small-sized cabinet, so quite space-saving.
- Less equipment
- Quite easier to modify lighting system
- According to your demand, you may setup supplementary on/off timing (lamp can be switched off in order at the end of day.
- Easier to apply role of lightness sensitivity switch to lamp or already modified lamp row.

6.6 Rainwater pump

Nowadays besides drinkable water in family, rainwater applications is gradually increasing. In this way much costs can be saved, also environment can be improved as well. The application of rain water as follows:

- Wash clothes
- Water system in garden
- Potted plant water
- Wash car
- Scour W.C.

The following figure is to tell you how to run the rainwater application system:

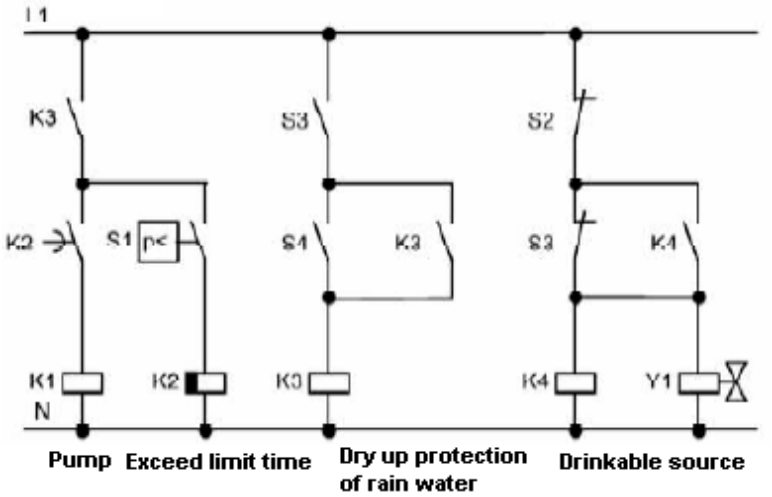


The rain water is collected in the container and then pumped to service water ductwork through pumping station. So you can apply rain water as drinkable water. If the rain water in the container dried up, this system can supply drinkable water.

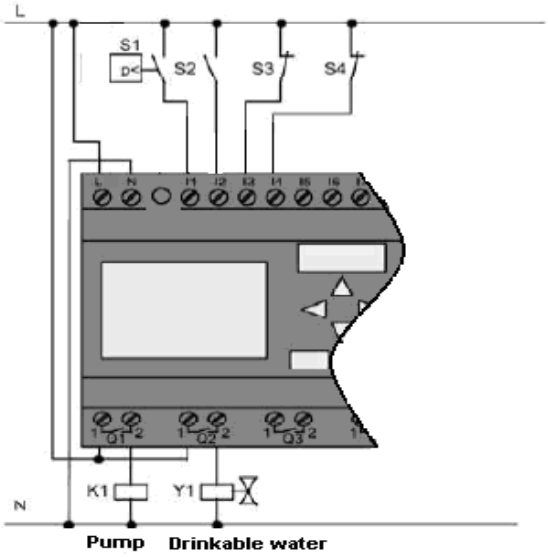
Requirements for the control system of service water pump

- It can provide service water all day, under the contingency instance, the control system must be able to be switched over to drinkable water system automatically.
- When switching to drinkable water system, it can't interlard rain water.
- If rain container has not enough rain water, service water pump can't be on (rain water dry-up protection).

6.6.1 Standard solution



6.6.2 The scheme of ELC-18AC

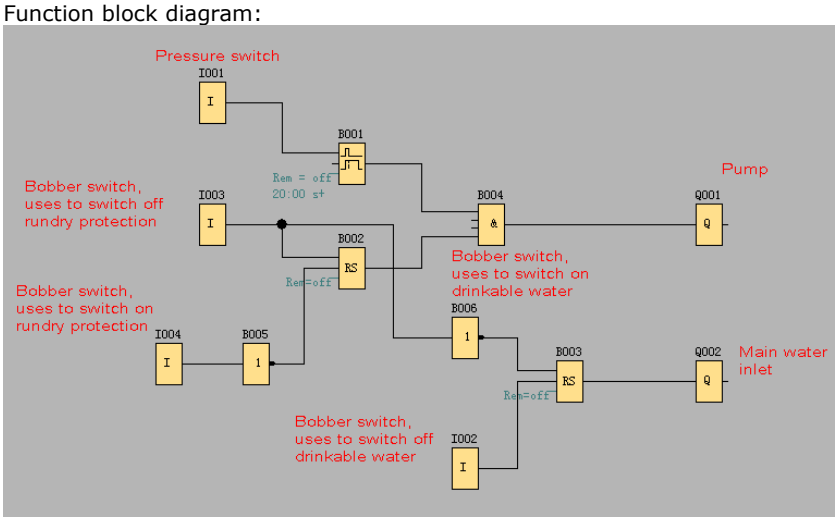


Components:

- K1 main contactor
- Y1 Solenoid valve
- S1 Pressure switch

- S2(make contact) Bobber switch(water level)
- S3—S4(break contact) Bobber switch(water level)

Function block diagram:



Chapter 7 xLogic Software

xLogicsoft is available as a programming package for the PC. This software provides many features, for example:

- A graphic interface for offline creation of your circuit program by means of Function Block Diagram (function chart)
- Simulation of your circuit program on the PC
- Generating and printing of an overview chart for the circuit program
- Saving a backup of the circuit program on the hard drive or other media
- Easy configuration of blocks
- Transferring the circuit program
  - from the xLogic to the PC and
  - from the PC to xLogic
- Setting the TOD
- Online test: Display of status changes and process variables of xLogic in RUN mode:
  - Status of a digital I/O, shift register bits and cursor keys
  - The values of all analog I/Os
  - The results of all blocks
  - The current values (including the times) of selected blocks
  - Change the output(the input pin of the output cannot be connected) status via xLogicsoft
- Starting and stopping circuit program execution via the PC (RUN, STOP).

The xLogic alternative

As you can see, xLogicSoft represents an alternative to conventional engineering methods:

1. You start by developing the circuit program on your desktop.
2. You simulate the circuit program on your computer and verify its functions, before you actually implement it in your system.
3. You can add comments to the circuit program and create hardcopies.
4. You save a copy of your circuit program to the file system on your PC, to make it directly available for any modifications.
5. It takes only a few key actions to download the circuit program to xLogic.
6. Under Simulate mode in xLogicsoft ,you can study how to program via the panel key

xLogicSoft

xLogicsoft runs under Windows 95/98,
Windows NT 4.0, Windows Me®, Windows 2000®, Windows XP®,windows Vista xLogicsoft is capable of client/server operation and offers you a high degree of freedom for creating your circuit program.

xLogicSoft: xLogicV2.8.5.7

This is the current version of xLogicsoft. You will find all the functions and the functionality of the devices described in this manual in the version 2.8.5.7 and later.

Note

If a full version is not installed, you can carry out an upgrade as follows:

- Install the software from the CD.
- When the system prompts you for the previous version, place the old xLogicsoft CD in CD drive.
- Point your browser to the "...\Install" directory on the CD.

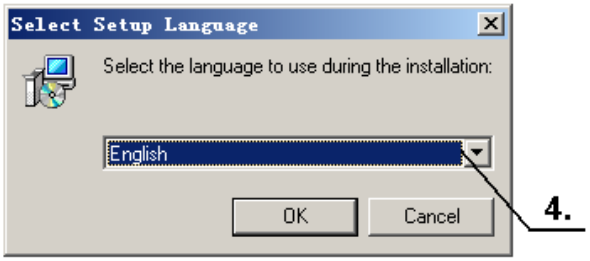
Updates and info

You can download demo versions of the software free of charge from the Internet address specified in the preface.

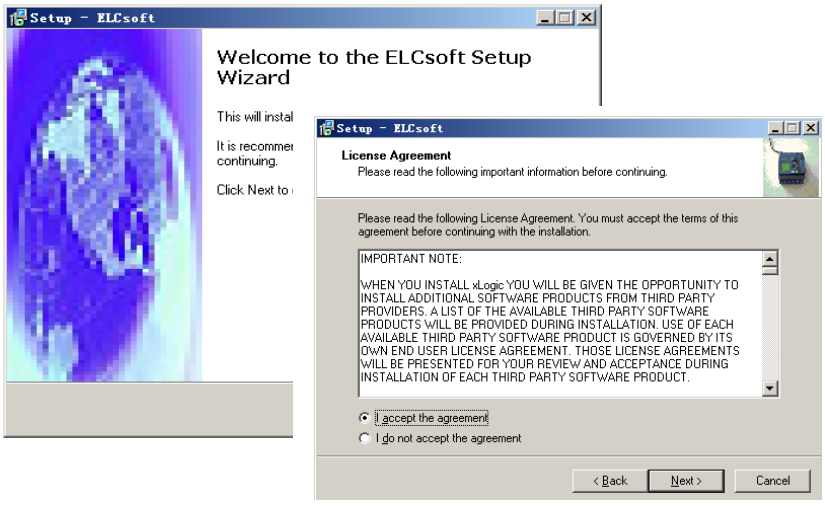
Installing xLogicsoft(xLogic):

1. Double-click on Setup.exe or left-click the "INSTALL" menu directly.

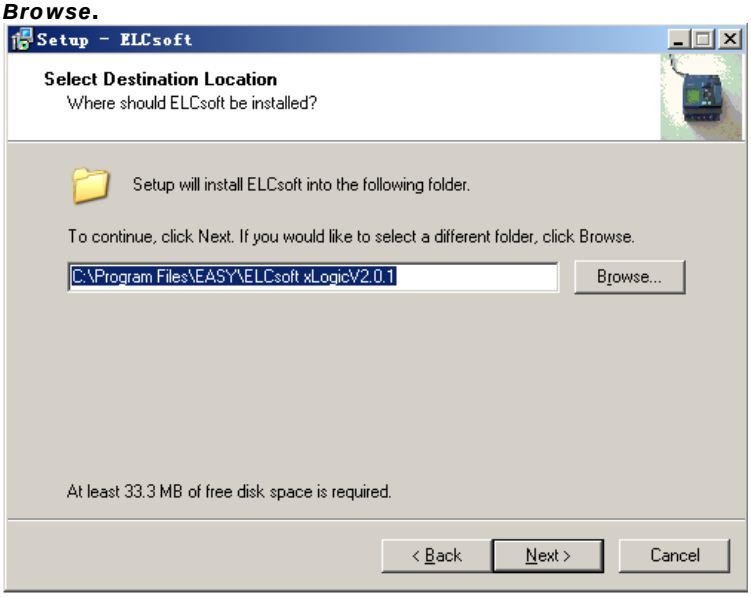
2. Select the language you would like and click *OK* to confirm



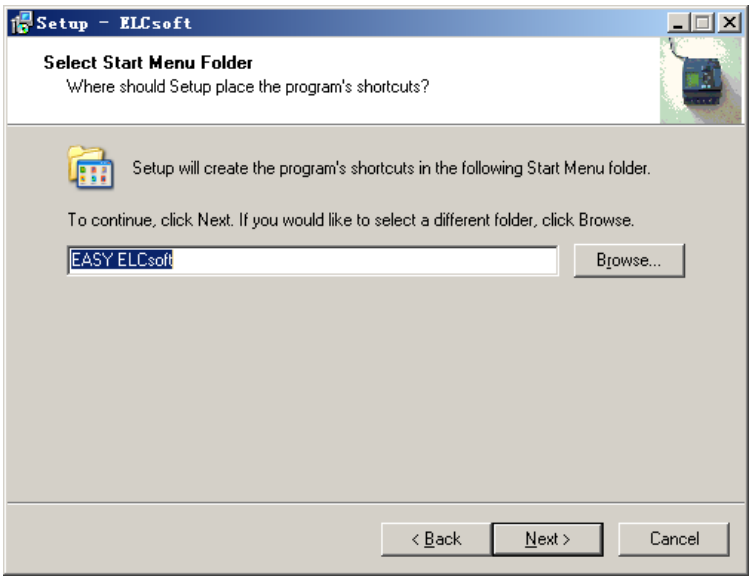
3. If you consent to the license agreement, click Next to confirm.



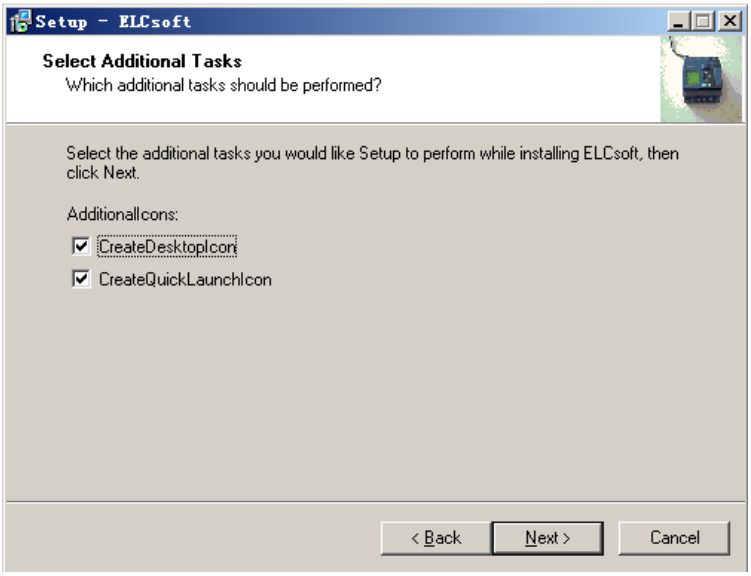
4. Where is the program to be installed? If you do not want to accept the recommended file location:
***C:\Program Files\EASY\xLogicsoft xLogic_V2.0*, specify another directory using *Browse*.**

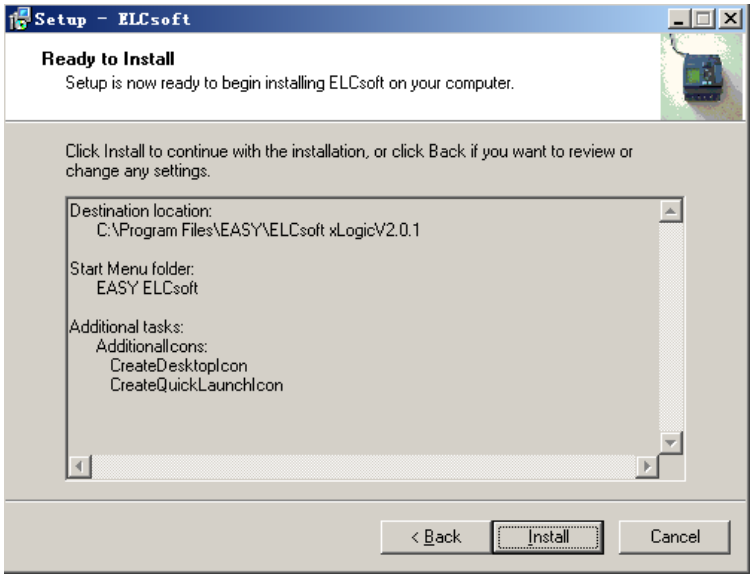


5. If you want to accept the recommended file location, click *Next* to Confirm.

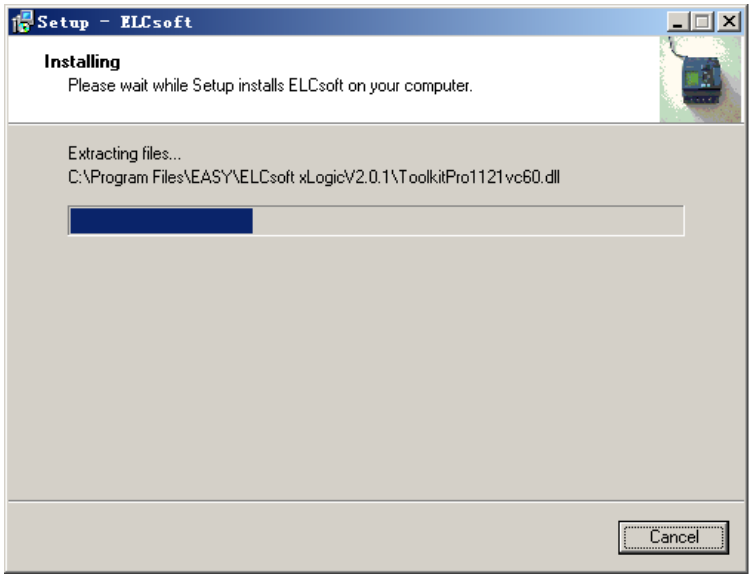


6. In this example, the program icon is to be placed on the desktop. Use *Next* to proceed.

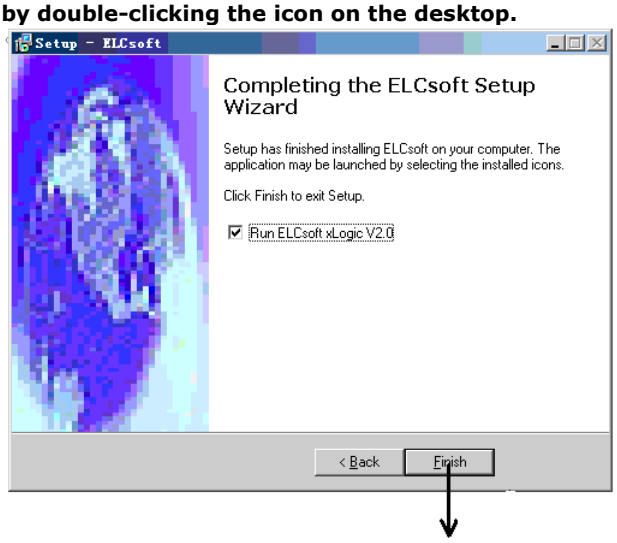




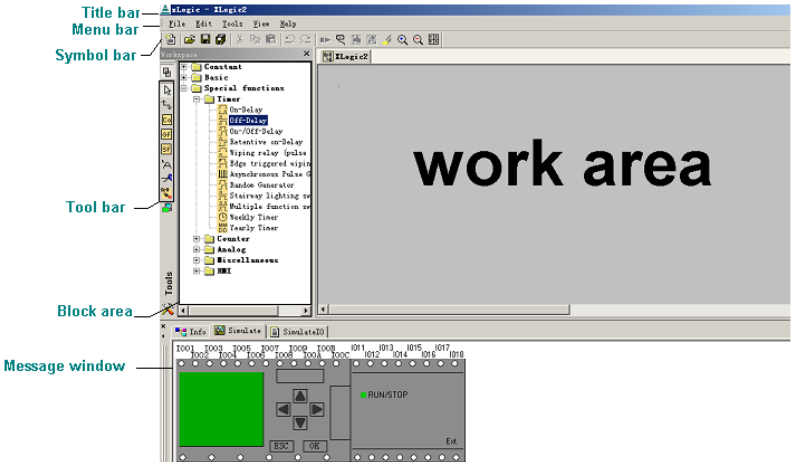
7.Click Install button to install. Program is being installed. . . . .



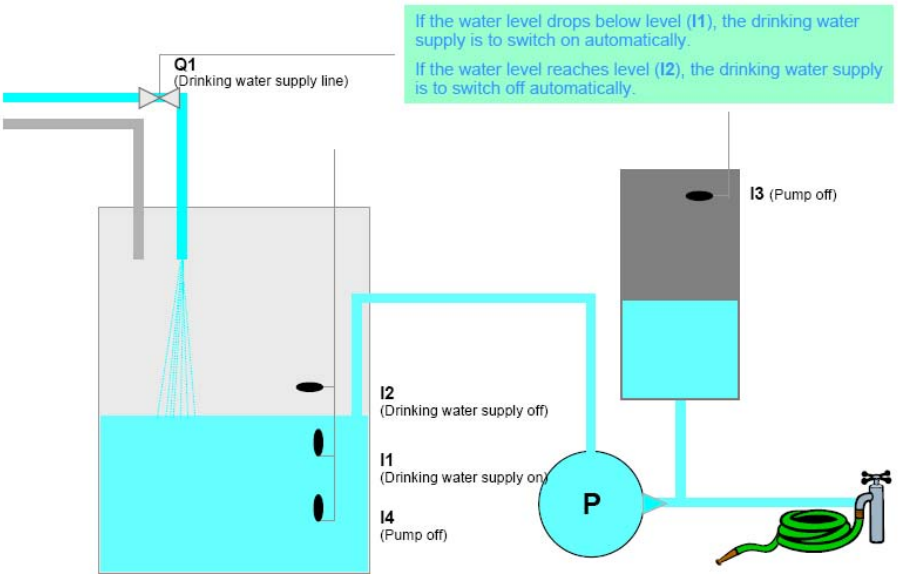
8. The installation is finished. You can start the xLogic program immediately or later by double-clicking the icon on the desktop.



double click



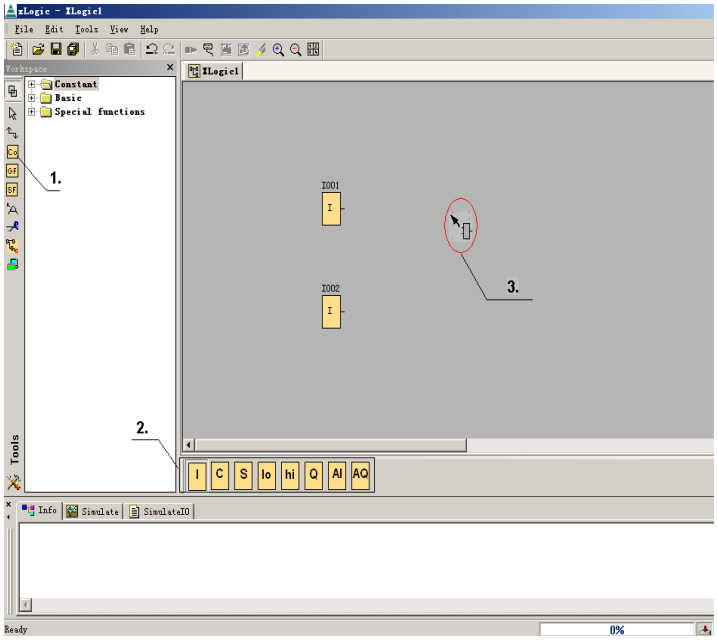
Typical xLogic Applications



Getting Started with xLogic

Step 1: Inserting Connectors (Co)

How many inputs and outputs are needed to solve this task?



1. The Co tool must be selected if you want to place input blocks, output blocks, cursor keys, shift register bits, or constants (high, low) on the programming interface.

2. Then, the specific function blocks are selected using this

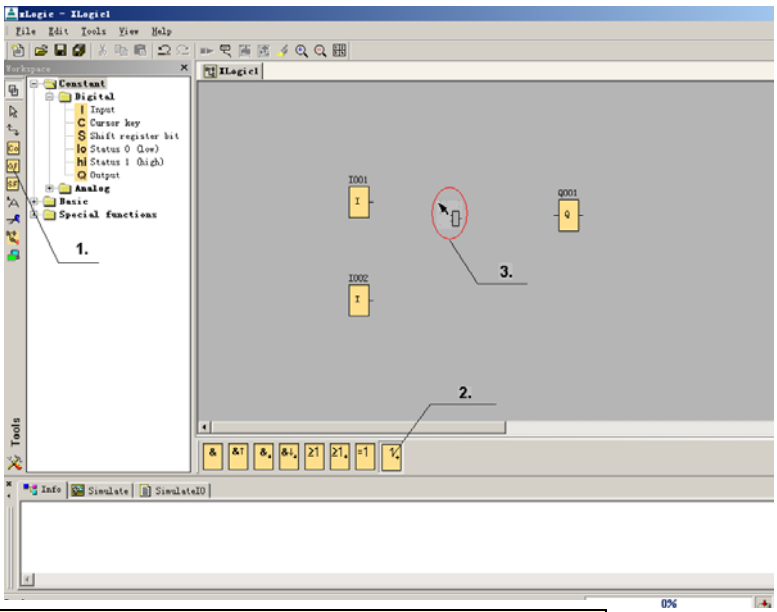
Symbol bar: I C S Io hi Q AI AQ

3. The following symbol now appears in the work area:

Now, move the mouse to the required position. The function is inserted by clicking the left mouse button.

Step 2: Inserting Basic Functions (BF)

Which basic functions are needed to solve this task?

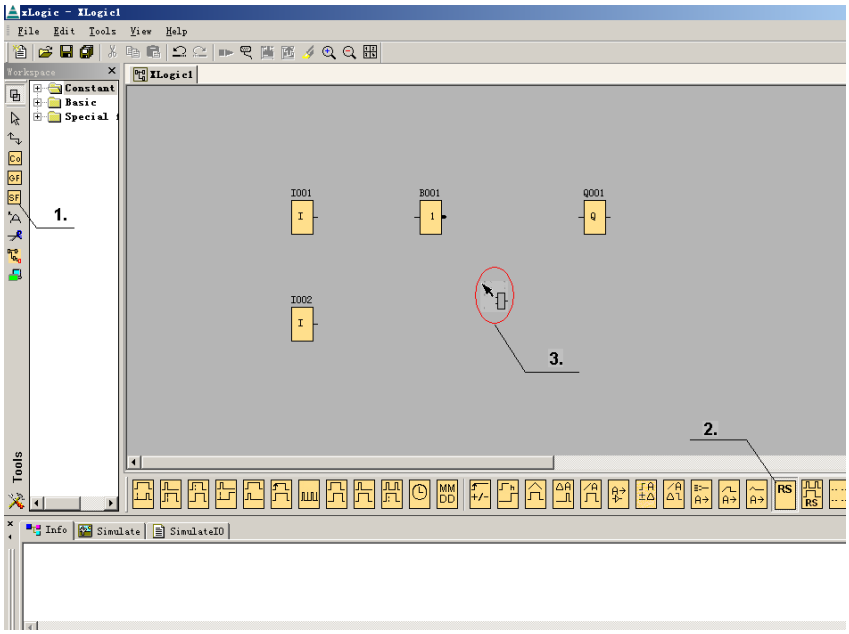


1. Select the GF tool if you want to place simple basic logic elements of Boolean algebra on the programming interface.

2. A specific function block is selected using the symbol bar.

3. The following symbol appears in the work area: Now, move the cursor to the required position. The function is inserted by clicking the left mouse key.

Step 3: Inserting Special Functions (SF)



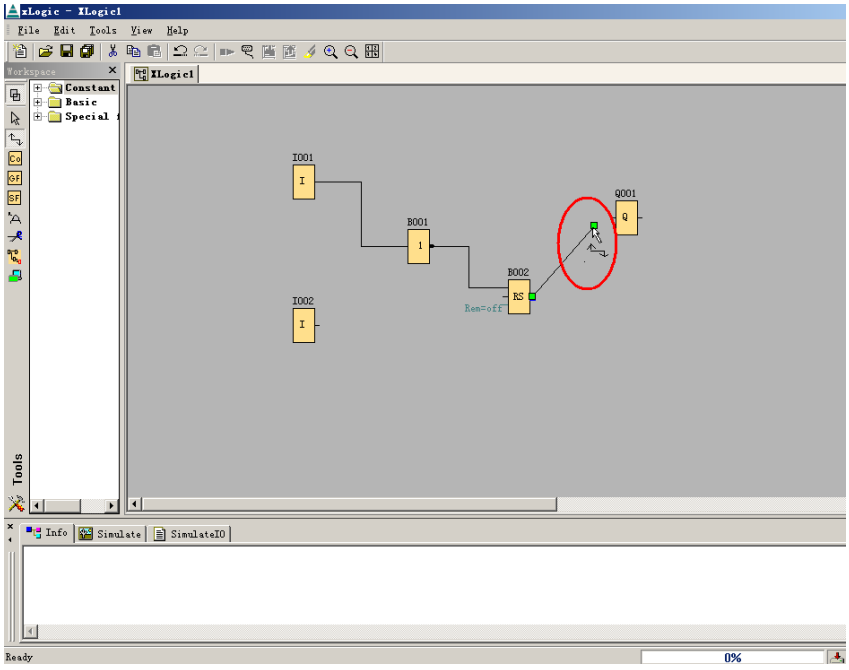
1. Select the SF tool if you want to place additional functions on the programming area.

2. A specific function block is selected using the symbol bar.

3. The following symbol now appears in the work area:

Now, move the cursor to the required position.
The function is inserted by clicking the left mouse button.

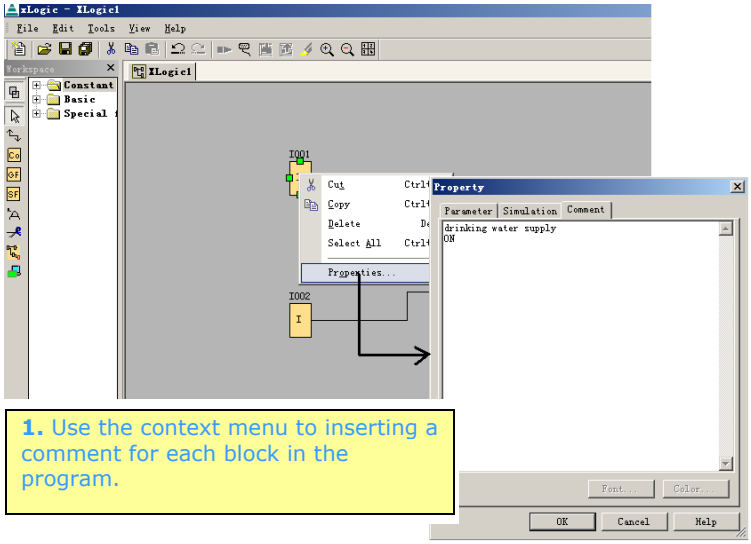
Step 4: Connecting



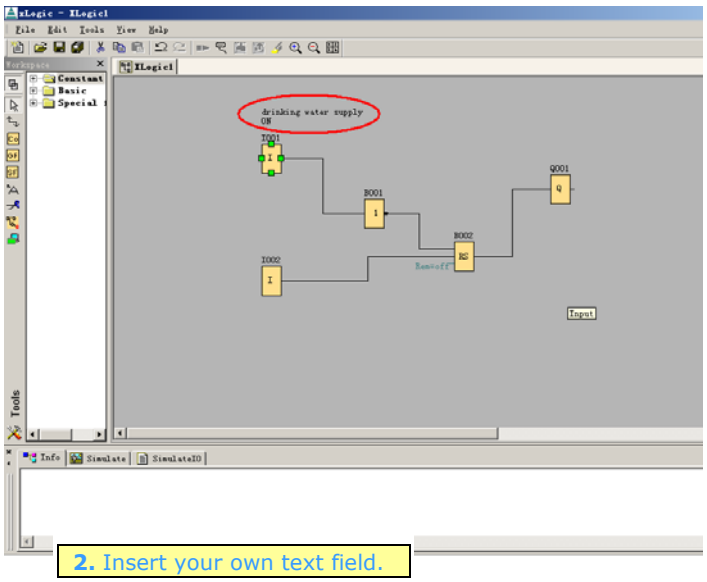
1. The  tool must be selected if you want to connect the inputs and outputs of the function blocks to one another. Now point the mouse to a connection pin of a block and click using the left mouse button. Keeping the mouse button pressed, move the cursor until it is pointing at the pin that you want to connect to and release the mouse button. xLogic then connects the two pins together. This results in a connection between two block pins. Use the same procedure for the other connections.

Step 5: Inserting Text Fields

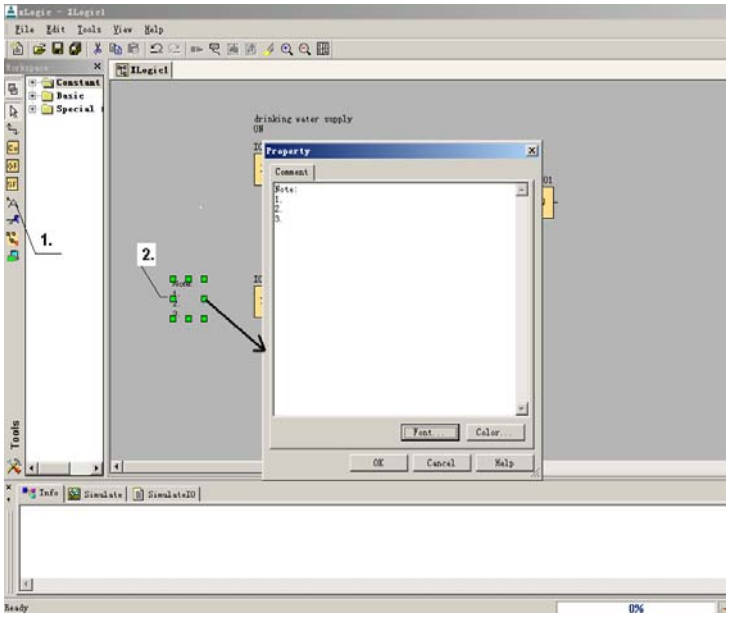
Inserting text fields makes the program easier to understand. xLogicsoft gives you several options for inserting text in the program structure:

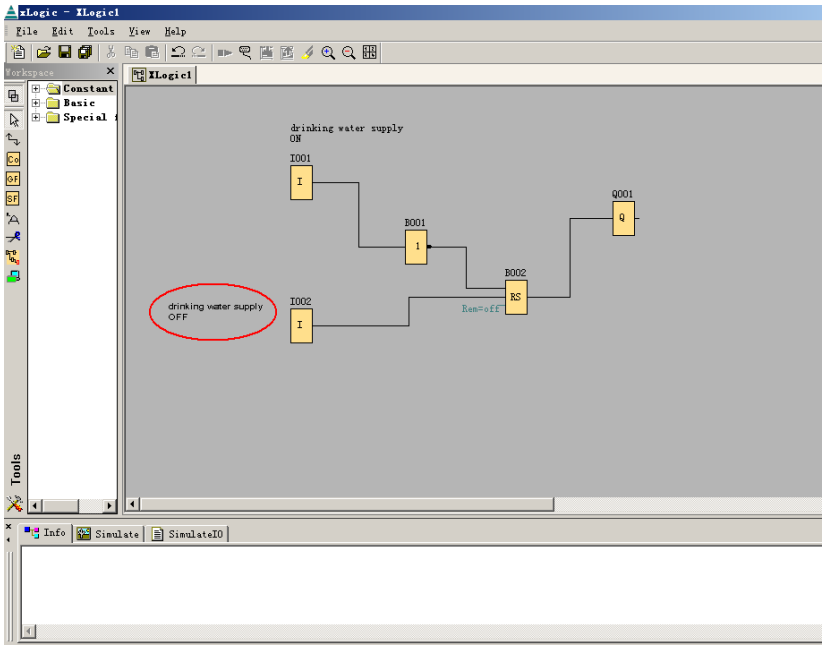


1. Use the context menu to inserting a comment for each block in the program.



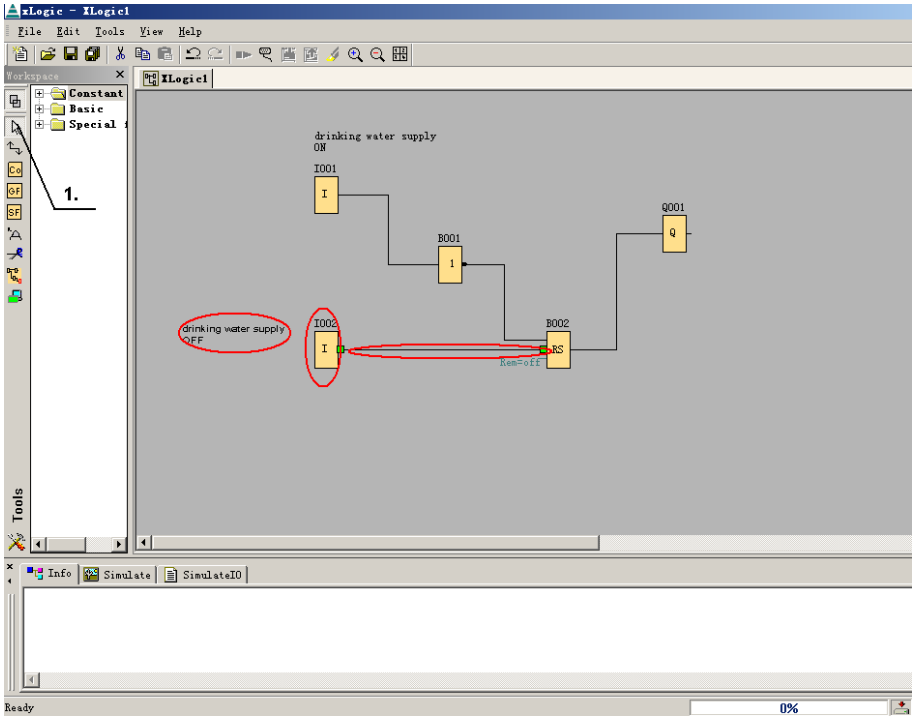
2. Insert your own text field.





Step 6: Moving

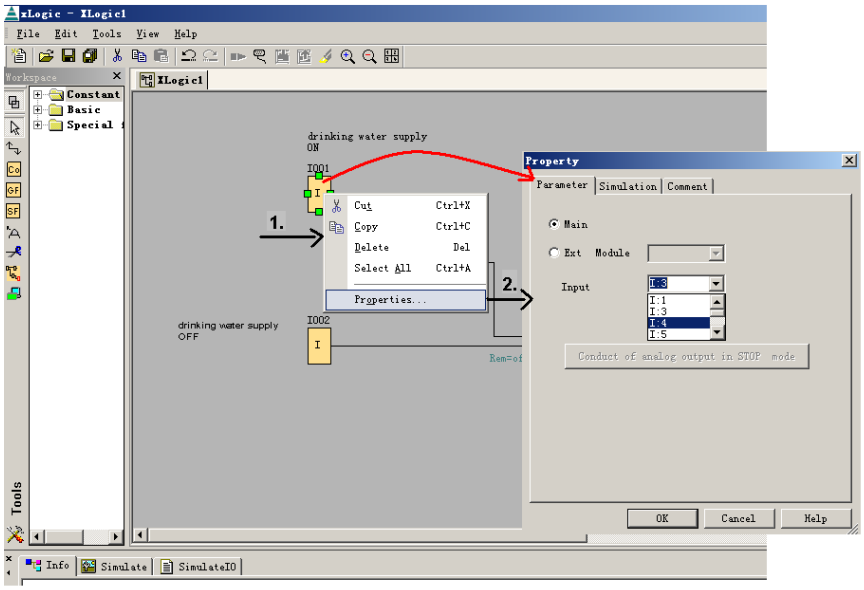
Once the function blocks are inserted and connected, the circuit program is done. Some follow-up work is needed, however, to obtain a view of the created circuit that is visible at a glance and easy to understand. Objects placed in the work area such as function blocks, lines, and text fields can be moved:



1. Select the tool if you want to move **function blocks**, **text fields,** or **connection lines**.

Step 8: Assigning Parameters to Function Blocks

In the case of special functions and basic functions, there is a tab for comments and one or more tabs for parameters. Here, you can specify values or settings that are to be adopted by the function block in its circuit.



1. Select function and click using the **right** mouse button.

or

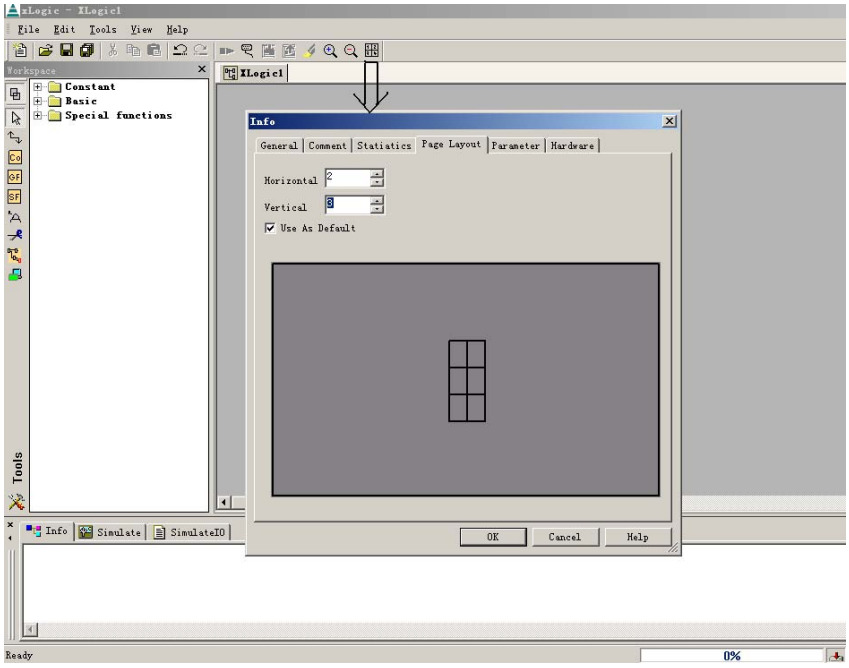
2. Select the *Block* ***Properties*** menu item.

Double-click the function using the **left** mouse button.

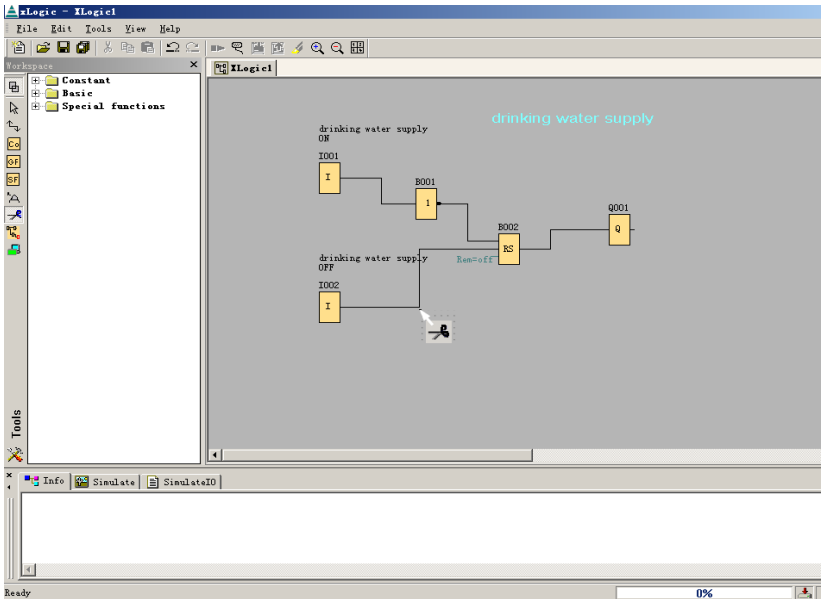
3. Another address can be assigned for each individual input and output block by means of block parameter assignment. However, only the inputs and outputs not already assigned in the circuit program are considered in the selection.

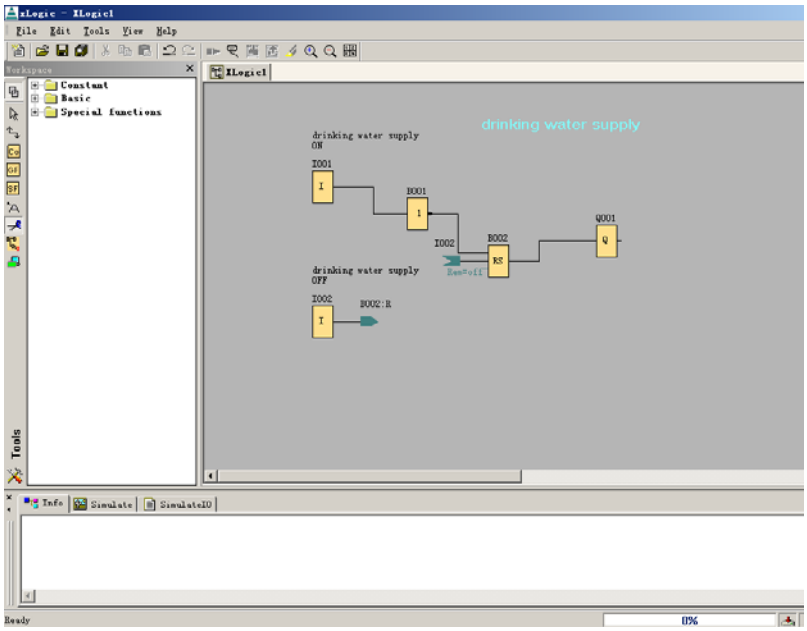
Step 9: Documentation

In the case of large or complex programs, it is often useful to divide up the circuit diagram onto several pages.



Various connection lines must be separated when the program is divided onto several pages.

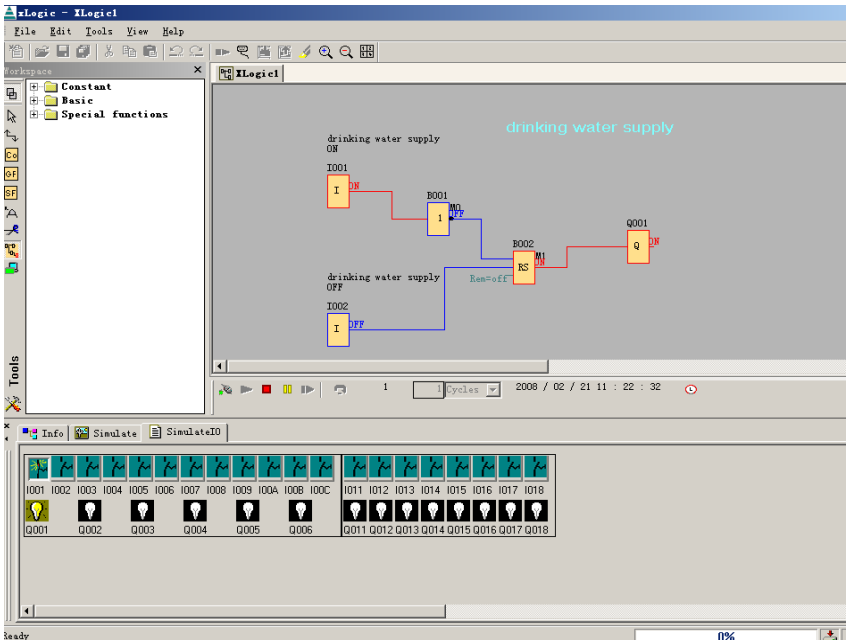




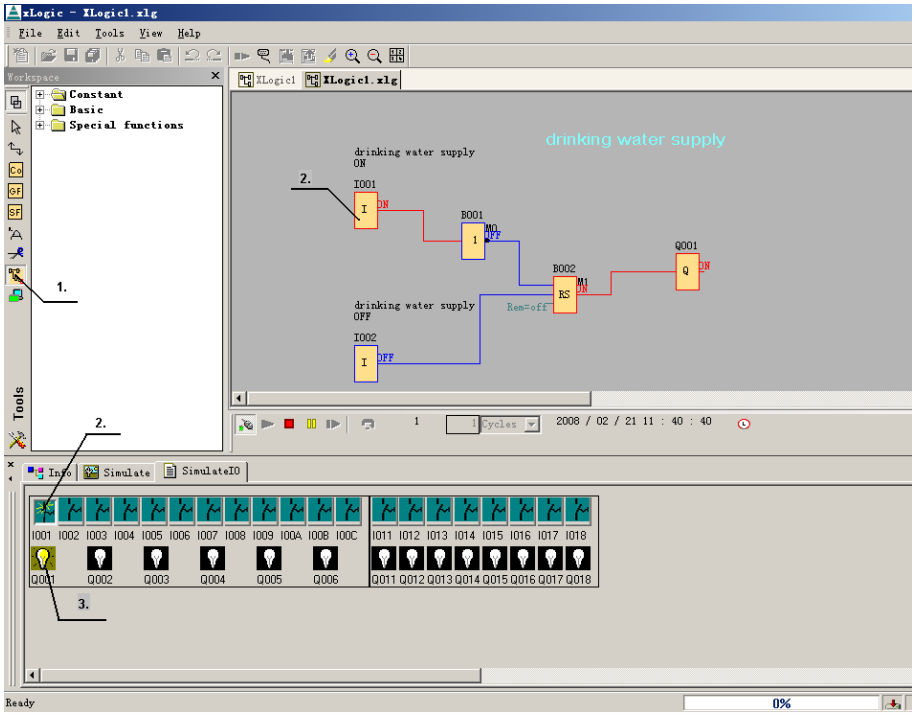
The connection lines can be easily separated using the selected cut tool. The interfaces are uniquely labeled by the page number, block number, and input pin. By clicking on one of the interface wildcards again using the cut tool, the separation is undone.

Step 10: Program Testing

Once the programming and documentation is completed, the program is tested.



You know that your program can run in, but you still have to check whether it functions as intended.
You may also want to modify some parameters .You can experimentally change the input values, test the response to a power failure, and compare your calculations or expectations with the actual output behavior.

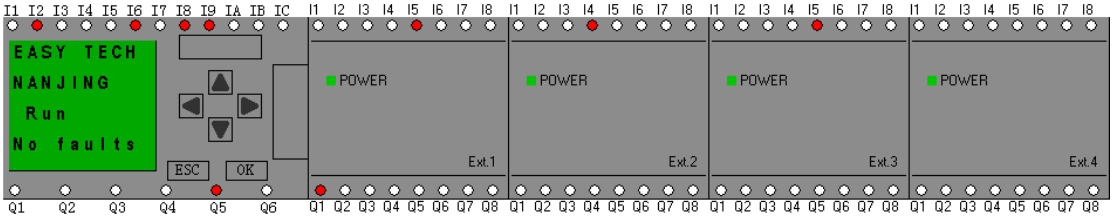


1. Select the  tool if you want to test your program.

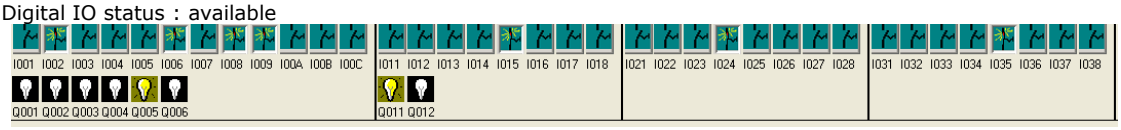
2. Once the simulation is activated, a symbol bar for operator control and monitoring of inputs and outputs is called. A software switch is used to simulate a power failure in order to test the retentive features of the circuit behavior.

3. The input states can be modified by clicking the button in the symbol bar (2) or clicking on the inputs in the display.

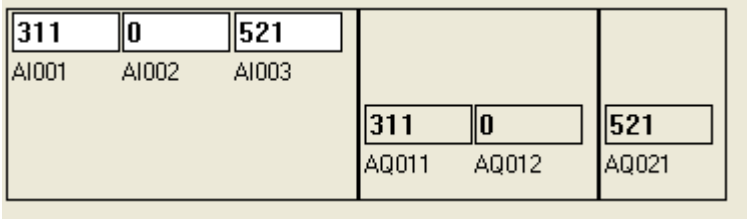
Vivid off-line simulation
LCD panel display: available



Digital IO status : available



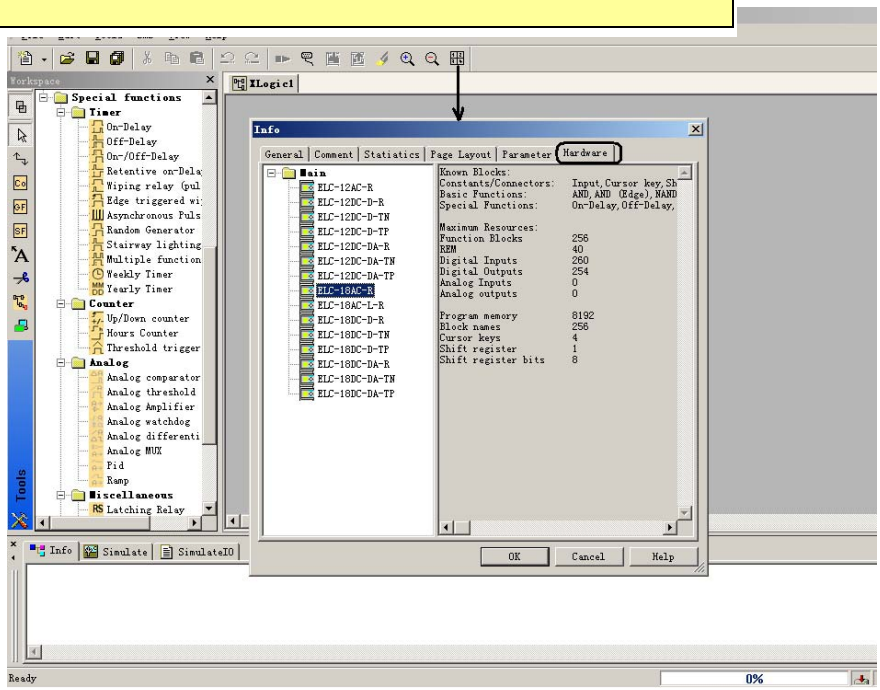
Analog IO value : available



The connection sequence can be tracked by the change in color of the connection lines from blue (low signal) to red (high signal). This facilitates the detection and removal of errors significantly.

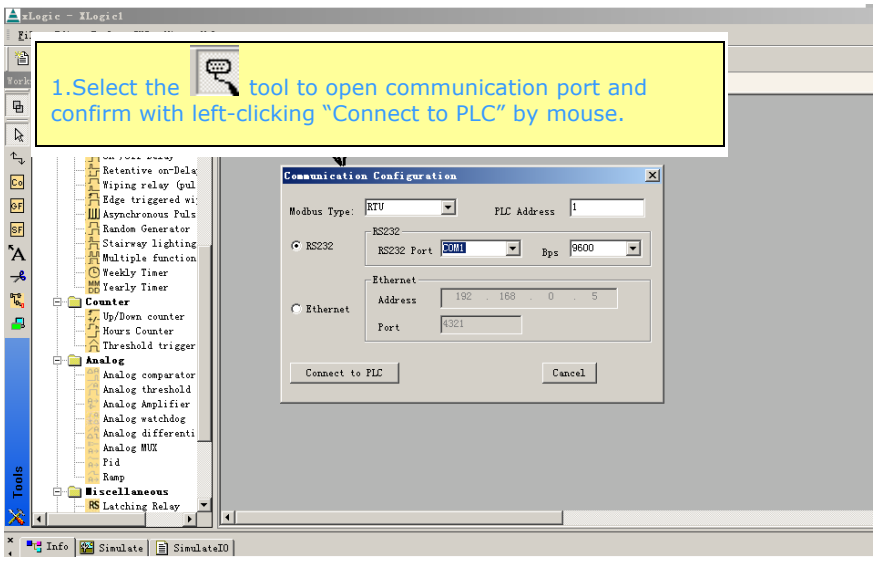
Step 11: Transferring Program to xLogic

When programming in xLogicsoftware, you have the option of first implementing your circuit program and then determining the required unit using the **Tools->Select Hardware** menu command, or you can use the  tool.



The xLogic unit must be connected with the PC cable for the transfer.

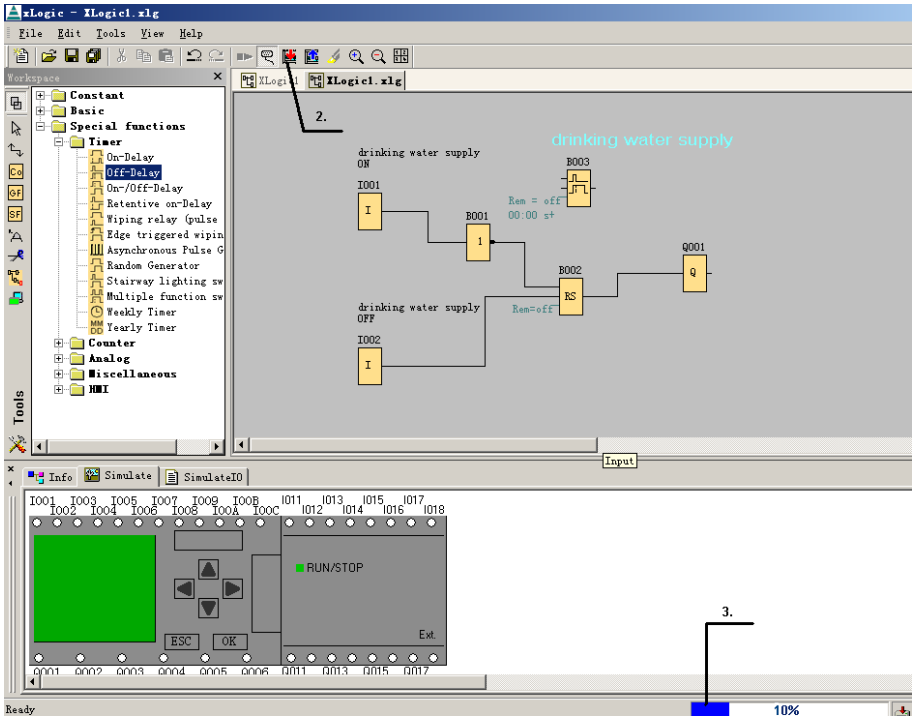
All preparations have been made. The program can now be transferred.



COM parameter:

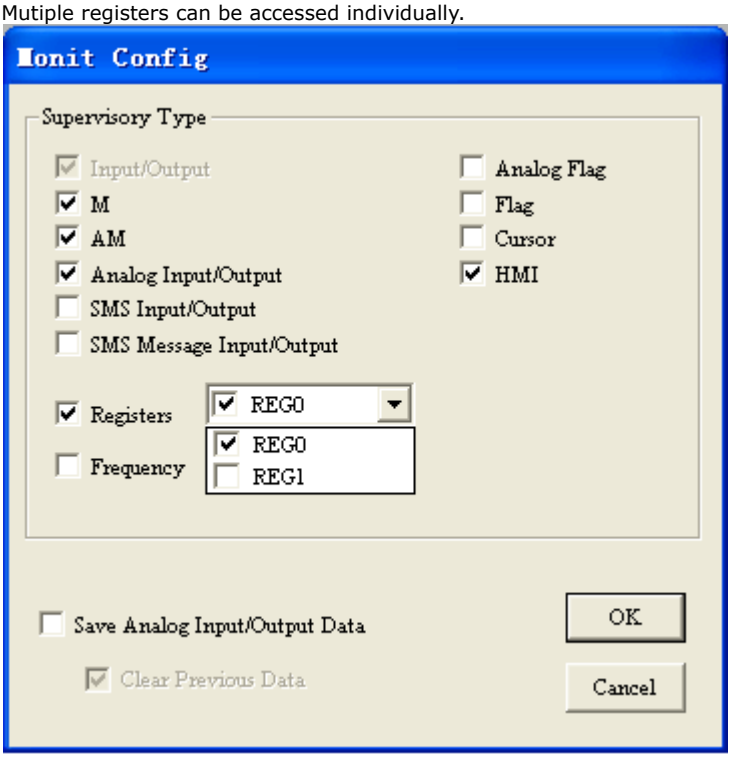
Modbus Type: RTU
PLC Address: 1
Bps: 9600
COM Port: which you connect

2.Select the function to transfer a circuit program created in the PC with xLogicsoftware to a xLogic unit.

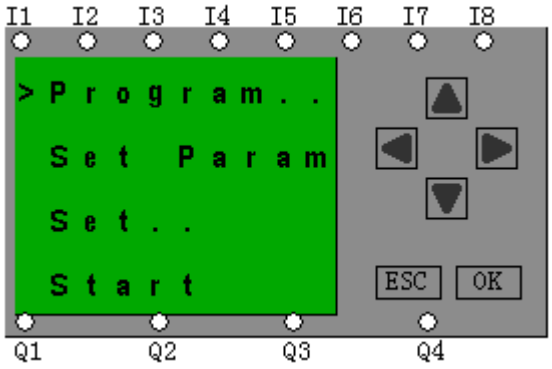


3.A message is displayed in the status bar indicating that whether the data transfer was successful or not.

On-line monitor/test
Mutiple registers can be accessed individually.



HMI also can be monitored/operated! (Only for ELC-12)



7.1 Connecting the xLogic to a PC

Connecting the PC cable
To connect the xLogic to a PC, you need the xLogic PC cable (ELC-RS232).
Remove the cap from your xLogic and connect the cable to the communication port. Connect the other end of the cable to the serial port of your PC.

Connecting the PC cable to the USB port

If your PC is only equipped with a USB interface (Universal Serial Bus), you will need a ELC-USB module to connect the xLogic cable to this port. For the ELC-USB module information, refer to Chapter 1.

Note

If the circuit program created with **xLogic** is password protected, both the circuit program and the password are downloaded to xLogic. The password prompt is enabled at the end of the data transfer. The upload of a password protected program created in xLogic is only possible after the correct password is entered in **xLogic.**

Appendix
A Technical data

A.1 General technical data

Criterion	Tested in accordance with	Values
ELC-18 Series Main Module Dimensions (W x H x D) Weight Installation		 95 x 90 x 55 mm Approx. 350 g on a 35 mm profile rail or wall mounting
ELC-12 Series Main Module Dimensions (W x H x D) Weight Installation		72 x 90 x 68 mm Approx.250g on a 35 mm profile rail or wall mounting
ELC12-E Series Expansion Module Dimensions (W x H x D) Weight Installation		48 x 90 x 64 mm Approx.180g on a 35 mm profile rail or wall mounting
ELC-E-16 Series Expansion Module Dimensions (W x H x D) Weight Installation		 72 x 90 x 53 mm Approx.250g on a 35 mm profile rail or wall mounting
Climatic conditions		
Ambient temperature Horizontal installation Vertical installation Storage/shipping	Low temperature to IEC 6006821 High temperature to IEC 60068-2-2	 0 ... 55 °C 0 ... 55 °C -40 °C ... +70 °C
Relative humidity	IEC 60068-2-30	From 10 to 95 % no condensation
Air pressure		795 ... 1080 hPa
Pollutants	IEC 60068-2-42 IEC 60068-2-43	SO_2 10 cm^3 /m^3, 4 days H2S 1 cm^3 /m^3, 4 days

Criterion	Tested in accordance with	Values
Ambient mechanical conditions		
Protection mode		IP20
Vibrations:	IEC 60068-2-6	5 ... 9 Hz (constant amplitude 3.5 mm) 9 ... 150 Hz (constant acceleration 1 g)
Shock	IEC 60068-2-27	18 shocks (half-sine wave 15g/11 ms)
Drop	IEC 60068-2-31	Drop height 50 mm
Free fall (packaged)	IEC 60068-2-32	1 m
Electro-magnetic compatibility (EMC)		
Emission(Conducted Emission)	EN 55022	Class B
Emission(Radiated Emission)	EN 55022	Class B
Harmonics(Current Harmonics)	EN 61000-3-2	
Flicker(Voltage Fluctuation)	EN 61000-3-3	
ESD(Electrostatic Discharge)	EN 61000-4-2 Severity 3	8 kV air discharge 6 kV contact discharge
RF-Field(Radiated Immunity)	EN 61000-4-3	3V/m
Burst(Electrical Fast Transients)	EN 61 000-4-4	1 kV (supply and signal lines)
Surge(Transients comm.&diff.mode) (applies only to ELC-AC types)	EN 51000-4-5	0.5kV
RF-com.mode(RF continues conducted)	EN 61000-4-6	
V-dips(Voltage dips and Interruption)	EN 61000-4-11	
Cycle time		
Cycle time per function		<0.01ms

A.2 Technical data: xLogic (ELC series)

Standard CPU Units(ELC-6 series)	ELC-6AC-R	ELC-6DC-D-R	ELC-6DC-D-TN
Inputs	4 digital	4 digital	4 digital
of which can be used in analog mode	none	none	none
Input/supply voltage	110-240V AC	12-24V DC	12-24V DC
Permissible range with signal "0" with signal "1" Input current	85 ... 265 V AC 100 ... 253 V DC max. 40 V AC 0.03 mA min. 79 V AC, 0.08 mA	10.8 V ... 28.8 V DC max. 3 V DC,1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA
Outputs	2 relays	2 relays	2 transistors(PNP)
Continuous current	10 A with resistive load; 2 A with inductive load	10 A with resistive load; 2 A with inductive load	0.3 A
Short-circuit protection	External fuse required	External fuse required	External fuse required
Switching frequency	2 Hz with resistive load; 0.5 Hz with inductive load	2 Hz with resistive load; 0.5 Hz with inductive load	10 Hz
Cycle time	< 0.1 ms/function	< 0.1 ms/function	< 0.1 ms/function
Integrated time switches/ power reserve	Yes / typ. 72 h	Yes / typ. 72 h	Yes / typ. 72 h
Connection cables	2 x 1.5 mm² or 1 x 2.5 mm²		
Ambient temperature	0 to + 55 ºC		
Storage temperature	– 40 ºC to + 70 ºC		
Degree of protection	IP20		
Certification	CE		
Mounting	On 35 mm standard mounting rail, 4 MW, or wall-mounting		
Dimensions	W x H x D (48*90*64 mm)		
Programming cable	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)
xLogic <=> xLogic communication (RS485)	No	No	No
xLogic <=> network (Ethernet)	No	No	No
Third party device(HMI) <=> xLogic	Yes (modbus)	Yes (modbus)	Yes (modbus)
Maximum program memory	64 blocks	64 blocks	64 blocks
External memory module	No	No	No
Data logging	Yes(ELC-MEMORY)	Yes(ELC-MEMORY)	Yes(ELC-MEMORY)
Extensions	No	No	No
High speed input	No	No	No
RTC	Yes	Yes	Yes
HMI	No	No	No

Note: The following function block cannot be used in ELC-6 series
Constant (Cursor key, Sms input/output,Sms message input/output)
Analog (Analog MUX, PI Controller,Analog Ramp,Analog Math,Analog Math error detection)
Miscellaneous(Message texts, Pwm,Modbus Read,Modbus Write)

Standard CPU Units(ELC-12 series)	ELC-12AC-R	ELC-12DC-DA-R	ELC-12DC-DA-TN	ELC-12DC-DA-TP
Inputs	8 digital	8 digital	8 digital	8 digital
of which can be used in analog mode	4 (0 to 10V)	4 (0 to 10V)	4 (0 to 10V)	4 (0 to 10V)
Input/supply voltage	110-240V AC	12-24V DC	12-24V DC	12-24V DC
Permissible range with signal "0" with signal "1" Input current	85 ... 265 V AC 100 ... 253 V DC max. 40 V AC 0.03 mA min. 79 V AC, 0.08 mA	10.8 V ... 28.8 V DC max. 3 V DC,1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA
Outputs	4 relays	4 relays	4 transistors(PNP)	4 transistors(NPN)
Continuous current	10 A with resistive load; 2 A with inductive load	10 A with resistive load; 2 A with inductive load	0.3 A	0.3 A
Short-circuit protection	External fuse required	External fuse required	External fuse required	External fuse required
Switching frequency	2 Hz with resistive load; 0.5 Hz with inductive load	2 Hz with resistive load; 0.5 Hz with inductive load	10 Hz	10 Hz
Cycle time	< 0.1 ms/function	< 0.1 ms/function	< 0.1 ms/function	< 0.1 ms/function
Integrated time switches/ power reserve	Yes / typ. 100 h	Yes / typ. 100 h	Yes / typ. 100 h	Yes / typ. 100 h
Connection cables	2 x 1.5 mm² or 1 x 2.5 mm²			
Ambient temperature	0 to + 55 ºC			
Storage temperature	– 40 ºC to + 70 ºC			
Degree of protection	IP20			
Certification	CE			
Mounting	On 35 mm standard mounting rail, 4 MW, or wall-mounting			
Dimensions	W x H x D (72 x 90 x 68 mm)			
Programming cable	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)
xLogic <=> xLogic communication (RS485)	Yes	Yes	Yes	Yes
xLogic <=> network (Ethernet)	Yes	Yes	Yes	Yes
Third party device(HMI) <=> xLogic	Yes (modbus)	Yes (modbus)	Yes (modbus)	Yes (modbus)
Maximum program memory	512 blocks	512 blocks	512 blocks	512 blocks
Data logging	Yes(ELC-MEMORY)	Yes(ELC-MEMORY)	Yes(ELC-MEMORY)	Yes(ELC-MEMORY)
Extensions	Yes	Yes	Yes	Yes
High speed input	No	I5,I6(14KHZ) I7,I8(60KHZ)	I5,I6(14KHZ) I7,I8(60KHZ)	I5,I6(14KHZ) I7,I8(60KHZ)
High speed output(PWM)	No	No	No	Q3,Q4(333Hz)
HMI	optional	optional	optional	optional

Model instruction
ELC-12AC-R-HMI = ELC-12AC-R+ELC-HMI , ELC-12AC-R-CAP = ELC-12AC-R+ELC-COVER
ELC-12DC-DA-R-HMI = ELC-12DC-DA-R+ELC-HMI , ELC-12DC-DA-R-CAP = ELC-12DC-DA-R+ELC-COVER
ELC-12DC-DA-TN-HMI = ELC-12DC-DA-TN+ELC-HMI, ELC-12DC-DA-TN-CAP = ELC-12DC-DA-TN+ELC-COVER
ELC-12DC-DA-TP-HMI = ELC-12DC-DA-TP+ELC-HMI, ELC-12DC-DA-TP-CAP = ELC-12DC-DA-TP+ELC-COVER

Ecnomy CPU Units(ELC-12 series)	ELC-12AC-R-E	ELC-12DC-D-R-E	ELC-12DC-D-TN-E	ELC-12DC-D-TP-E
Inputs	8 digital	8 digital	8 digital	8 digital
of which can be used in analog mode	none	none	none	none
Input/supply voltage	110-240V AC	12-24V DC	12-24V DC	12-24V DC
Permissible range with signal "0" with signal "1" Input current	85 ... 265 V AC 100 ... 253 V DC max. 40 V AC 0.03 mA min. 79 V AC, 0.08 mA	10.8 V ... 28.8 V DC max. 3 V DC,1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA
Outputs	4 relays	4 relays	4 transistors(PNP)	4 transistors(NPN)
Continuous current	10 A with resistive load; 2 A with inductive load	10 A with resistive load; 2 A with inductive load	0.3 A	0.3 A
Short-circuit protection	External fuse required	External fuse required	External fuse required	External fuse required
Switching frequency	2 Hz with resistive load; 0.5 Hz with inductive load	2 Hz with resistive load; 0.5 Hz with inductive load	10 Hz	10 Hz
Cycle time	< 0.1 ms/function	< 0.1 ms/function	< 0.1 ms/function	< 0.1 ms/function
Integrated time switches/ power reserve	Yes / typ. 72 h	Yes / typ. 72 h	Yes / typ. 72 h	Yes / typ. 72 h
Connection cables	2 x 1.5 mm² or 1 x 2.5 mm²			
Ambient temperature	0 to + 55 ºC			
Storage temperature	– 40 ºC to + 70 ºC			
Degree of protection	IP20			
Certification	CE			
Mounting	On 35 mm standard mounting rail, 4 MW, or wall-mounting			
Dimensions	W x H x D (72 x 90 x 68 mm)			
Programming cable	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)
xLogic <=> xLogic communication (RS485)	No	No	No	No
xLogic <=> network (Ethernet)	No	No	No	No
Third party device(HMI) <=> xLogic	Yes (modbus)	Yes (modbus)	Yes (modbus)	Yes (modbus)
Maximum program memory	64 blocks	64 blocks	64 blocks	64 blocks
Data logging	No	No	No	No
Extensions	No	No	No	No
High speed input	No	No	No	No
PWM	No	No	No	No
HMI	No	No	No	No

Note:

1.The following function block cannot be used in Ecnomy ELC-12 CPUS

Constant (Cursor key, Sms input/output,Sms message input/output)

Analog (Analog MUX, PI Controller,Analog Ramp,Analog Math,Analog Math error detection)

Miscellaneous(Message texts, Pwm,Modbus Read,Modbus Write)

2.Model instruction

ELC-12AC-R-E-CAP = ELC-12AC-R-E+ELC-COVER

ELC-12DC-D-R-E-CAP = ELC-12DC-D-R-E+ELC-COVER

ELC-12DC-D-TN-E-CAP = ELC-12DC-D-TN-E+ELC-COVER

ELC-12DC-D-TP-E-CAP = ELC-12DC-D-TP-E+ELC-COVER

Extensions Units(IO) for ELC-12 CPU	ELC12 -8AC-R	ELC12 -8DC-DA-R	ELC12 -8DC-DA-TN	ELC12 -8DC-DA-TP	ELC12 -8AC-DI	ELC12 -8DC-DI
Inputs	4 digital	4 digital	4 digital	4 digital	8 digital	8 digital
of which can be used in analog mode	none	4 (0 to 10V)	4 (0 to 10V)	4 (0 to 10V)	none	none
Input/supply voltage	110-240V AC	12-24V DC	12-24V DC	12-24V DC	110-240V AC	12-24V DC
Permissible range with signal "0" with signal "1" Input current	85 ... 265 V AC 100 ... 253 V DC max. 40 V AC 0.03 mA min. 79 V AC, 0.08 mA	10.8 V ... 28.8 V DC max. 3 V DC,1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA	85 ... 265 V AC 100 ... 253 V DC max. 40 V AC 0.03 mA min. 79 V AC, 0.08 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA
Outputs	4 relays	4 relays	4 transistors(PNP)	4 transistors(NPN)	none	none
Continuous current	10 A with resistive load; 2 A with inductive load	10 A with resistive load; 2 A with inductive load	0.3 A	0.3 A		
Short-circuit protection	External fuse required	External fuse required	External fuse required	External fuse required	External fuse required	External fuse required
Switching frequency	2 Hz with resistive load; 0.5 Hz with inductive load	2 Hz with resistive load; 0.5 Hz with inductive load	2 Hz	2 Hz	none	none
Connection cables	2 x 1.5 mm² or 1 x 2.5 mm²					
Ambient temperature	0 to + 55 ºC					
Storage temperature	– 40 ºC to + 70 ºC					
Degree of protection	IP20					
Certification	CE					
Mounting	On 35 mm standard mounting rail, 4 MW, or wall-mounting					
Dimensions	(W X H X D) 48 x 90 x 64 mm					

Extensions Units(Analog) for ELC-12 CPU	ELC12-E-PT 100	ELC12-E-AI(I)	ELC12-E-AQ-V	ELC12-E-AQ -I
Inputs	2 pt100 (–50 ºC ... +200 ºC)	4 (0/4….20mA)	No	No
Input/supply voltage	12-24V DC	12-24V DC	15-24V DC	12-24V DC
Outputs	No	No	2 (0 to 10V)	2 (0 to 20mA)
Resolution	0.25 °C	10 bit standardized to 0 – 1000	10 bit standardized to 0 – 1000	10 bit standardized to 0 – 1000
Connection cables	2 x 1.5 mm² or 1 x 2.5 mm²			
Ambient temperature	0 to + 55 ºC			
Storage temperature	– 40 ºC to + 70 ºC			
Degree of protection	IP20			
Certification	CE			
Mounting	On 35 mm standard mounting rail, 4 MW, or wall-mounting			
Dimensions	(W X H X D) 48 x 90 x 64 mm			

Communication module for ELC-12 CPU	ELC12-E-RS485	ELC12-E-Ethernet-DC	ELC12-E-Ethernet-AC
Input/supply voltage	12-24V DC	12-24V DC	110-240V AC
Description	isolated 485 converter,used to bring out the terminals of RS485 port built-in ELC-12 series CPU for connection with third party devices.	Ethernet module connecting to ELC-12 CPU units，DC type.	Ethernet module connecting to ELC-12 CPU units，AC type.
Short-circuit protection	External fuse required	External fuse required	External fuse required
Connection cables	2 x 1.5 mm² or 1 x 2.5 mm²		
Ambient temperature	0 to + 55 ºC		
Storage temperature	– 40 ºC to + 70 ºC		
Degree of protection	IP20		
Certification	CE		
Mounting	On 35 mm standard mounting rail, 4 MW, or wall-mounting		
Dimensions	(W X H X D) 48 x 90 x 64 mm		

Standard CPU Units(ELC-18 series)	ELC-18AC-R	ELC -18DC-D-R	ELC-18DC-DA-R	ELC-18DC-D-TN	ELC -18DC-DA-TN	ELC-18DC-D-TP	ELC -18DC-DA-TP
Inputs	12 digital	12 digital	12 digital	12 digital	12 digital	12 digital	12 digital
of which can be used in analog mode	none	none	8(0 to 10V)	none	8(0 to 10V)	none	8(0 to 10V)
Input/supply voltage	110-240V AC	12-24V DC	12-24V DC	12-24V DC	12-24V DC	12-24V DC	12-24V DC
Permissible range with signal "0" with signal "1" Input current	85 ... 265 V AC 100 ... 253 V DC max. 40 V AC 0.03 mA min. 79 V AC, 0.08 mA	10.8 V ... 28.8 V DC max. 3 V DC,1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA
Outputs	6 relays	6 relays	6 relays	6 transistors(PNP)	6 transistors(PNP)	6 transistors(NPN)	6 transistors(NPN)
Continuous current	10 A with resistive load; 2 A with inductive load	10 A with resistive load; 2 A with inductive load	10 A with resistive load; 2 A with inductive load	0.3 A	0.3 A	0.3 A	0.3 A
Short-circuit protection	External fuse required	External fuse required	External fuse required	External fuse required	External fuse required	External fuse required	External fuse required
Switching frequency	2 Hz with resistive load; 0.5 Hz with inductive load	2 Hz with resistive load; 0.5 Hz with inductive load	2 Hz with resistive load; 0.5 Hz with inductive load	10Hz	10Hz	10Hz	10Hz
Cycle time	< 0.1 ms/function	< 0.1 ms/function	< 0.1 ms/function	< 0.1 ms/function	< 0.1 ms/function	< 0.1 ms/function	< 0.1 ms/function
Integrated time switches/ power reserve	Yes / typ. 10 h	Yes / typ. 10 h	Yes / typ. 10 h	Yes / typ. 10 h	Yes / typ. 10 h	Yes / typ. 10 h	Yes / typ. 10 h
Connection cables	2 x 1.5 mm² or 1 x 2.5 mm²						
Ambient temperature	0 to + 55 ºC						
Storage temperature	– 40 ºC to + 70 ºC						
Degree of protection	IP20						
Certification	CE						
Mounting	On 35 mm standard mounting rail, 4 MW, or wall-mounting						
Dimensions	(W X H X D) 95 x 90 x 55 mm						
Programming cable	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)
xLogic <=> xLogic communication (RS485)	Yes	Yes	Yes	Yes	Yes	Yes	Yes
xLogic <=> network (Ethernet)	Yes	Yes	Yes	Yes	Yes	Yes	Yes
Third party device(HMI) <=> xLogic	Yes (modbus)	Yes (modbus)	Yes (modbus)	Yes (modbus)	Yes (modbus)	Yes (modbus)	Yes (modbus)
Maximum program memory	256 blocks	256 blocks	256 blocks	256 blocks	256 blocks	256 blocks	256 blocks
Data logging	No	No	No	No	No	No	No
Extensions	Yes	Yes	Yes	Yes	Yes	Yes	Yes
High speed input	No	IB,IC(14KHZ)	IB,IC(14KHZ)	IB,IC(14KHZ)	IB,IC(14KHZ)	IB,IC(14KHZ)	IB,IC(14KHZ)
RTC	Yes	Yes	Yes	Yes	Yes	Yes	Yes
HMI	Yes	Yes	Yes	Yes	Yes	Yes	Yes

Economy CPU Units(ELC-18 series)	ELC -18AC-R-E	ELC -18DC-D-R-E	ELC -18DC-DA-R-E	ELC -18DC-D-TN-E	ELC -18DC-DA-TN-E	ELC-18DC-D-TP-E	ELC-18DC-DA-TP-E
Inputs	12 digital	12 digital	12 digital	12 digital	12 digital	12 digital	12 digital
of which can be used in analog mode	none	none	8(0 to 10V)	none	8(0 to 10V)	none	8(0 to 10V)
Input/supply voltage	110-240V AC	12-24V DC	12-24V DC	12-24V DC	12-24V DC	12-24V DC	12-24V DC
Permissible range with signal "0" with signal "1" Input current	85 ... 265 V AC 100 ... 253 V DC max. 40 V AC 0.03 mA min. 79 V AC, 0.08 mA	10.8 V ... 28.8 V DC max. 3 V DC,1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA
Outputs	6 relays	6 relays	6 relays	6 transistors(PNP)	6 transistors(PNP)	6 transistors(NPN)	6 transistors(NPN)
Continuous current	10 A with resistive load; 2 A with inductive load	10 A with resistive load; 2 A with inductive load	10 A with resistive load; 2 A with inductive load	0.3 A	0.3 A	0.3 A	0.3 A
Short-circuit protection	External fuse required	External fuse required	External fuse required	External fuse required	External fuse required	External fuse required	External fuse required
Switching frequency	2 Hz with resistive load; 0.5 Hz with inductive load	2 Hz with resistive load; 0.5 Hz with inductive load	2 Hz with resistive load; 0.5 Hz with inductive load	10Hz	10Hz	10Hz	
Cycle time	< 0.1 ms/function	< 0.1 ms/function	< 0.1 ms/function	< 0.1 ms/function	< 0.1 ms/function	< 0.1 ms/function	< 0.1 ms/function
Integrated time switches/ power reserve	Yes / typ. 10 h	Yes / typ. 10 h	Yes / typ. 10 h	Yes / typ. 10 h	Yes / typ. 10 h	Yes / typ. 10 h	Yes / typ. 10 h
Connection cables	2 x 1.5 mm² or 1 x 2.5 mm²						
Ambient temperature	0 to + 55 ºC						
Storage temperature	– 40 ºC to + 70 ºC						
Degree of protection	IP20						
Certification	CE						
Mounting	On 35 mm standard mounting rail, 4 MW, or wall-mounting						
Dimensions	(W X H X D) 95 x 90 x 55 mm						
Programming cable	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)
xLogic <=> xLogic communication (RS485)	Yes	Yes	Yes	Yes	Yes	Yes	Yes
xLogic <=> network (Ethernet)	Yes	Yes	Yes	Yes	Yes	Yes	Yes
Third party device(HMI) <=> xLogic	Yes (modbus)	Yes (modbus)	Yes (modbus)	Yes (modbus)	Yes (modbus)	Yes (modbus)	Yes (modbus)
Maximum program memory	256 blocks	256 blocks	256 blocks	256 blocks	256 blocks	256 blocks	256 blocks
Data logging	No	No	No	No	No	No	No
Extensions	No	No	No	No	No	No	No
High speed input	No	IB,IC(14KHZ)	IB,IC(14KHZ)	IB,IC(14KHZ)	IB,IC(14KHZ)	IB,IC(14KHZ)	IB,IC(14KHZ)
RTC	Yes	Yes	Yes	Yes	Yes	Yes	Yes
HMI	Yes	Yes	Yes	Yes	Yes	Yes	Yes
PWM	No	No	No	No	No	No	No

Upgraded CPU Units(ELC-18 series)	ELC -18AC-R-U	ELC -18DC-D-R-U	ELC -18DC-DA-R-U	ELC -18DC-D-TN-U	ELC -18DC-DA-TN-U	ELC -18DC-D-TP-U	ELC -18DC-DA-TP-U
Inputs	12 digital	12 digital	12 digital	12 digital	12 digital	12 digital	12 digital
of which can be used in analog mode	none	none	8(0 to 10V)	none	8(0 to 10V)	none	8(0 to 10V)
Input/supply voltage	110-240V AC	12-24V DC	12-24V DC	12-24V DC	12-24V DC	12-24V DC	12-24V DC
Permissible range with signal "0" with signal "1" Input current	85 ... 265 V AC 100 ... 253 V DC max. 40 V AC 0.03 mA min. 79 V AC, 0.08 mA	10.8 V ... 28.8 V DC max. 3 V DC,1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA
Outputs	6 relays	6 relays	6 relays	6 transistors(PNP)	6 transistors(PNP)	6 transistors(NPN)	6 transistors(NPN)
Continuous current	10 A with resistive load; 2 A with inductive load	10 A with resistive load; 2 A with inductive load	10 A with resistive load; 2 A with inductive load	0.3 A	0.3 A	0.3 A	0.3 A
Short-circuit protection	External fuse required	External fuse required	External fuse required	External fuse required	External fuse required	External fuse required	External fuse required
Switching frequency	2 Hz with resistive load; 0.5 Hz with inductive load	2 Hz with resistive load; 0.5 Hz with inductive load	2 Hz with resistive load; 0.5 Hz with inductive load	10Hz	10Hz	10Hz	10Hz
Cycle time	< 0.1 ms/function	< 0.1 ms/function	< 0.1 ms/function	< 0.1 ms/function	< 0.1 ms/function	< 0.1 ms/function	< 0.1 ms/function
Integrated time switches/ power reserve	Yes / typ. 100 h	Yes / typ. 100 h	Yes / typ. 100 h	Yes / typ. 100 h	Yes / typ. 100 h	Yes / typ. 100 h	Yes / typ. 100 h
Connection cables	2 x 1.5 mm² or 1 x 2.5 mm²						
Ambient temperature	0 to + 55 ºC						
Storage temperature	– 40 ºC to + 70 ºC						
Degree of protection	IP20						
Certification	CE						
Mounting	On 35 mm standard mounting rail, 4 MW, or wall-mounting						
Dimensions	(W X H X D) 95 x 90 x 55 mm						
Programming cable	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)	PC cable, (RS232 or USB)
xLogic <=> xLogic communication (RS485)	Yes	Yes	Yes	Yes	Yes	Yes	Yes
xLogic <=> network (Ethernet)	Yes	Yes	Yes	Yes	Yes	Yes	Yes
Third party device(HMI) <=> xLogic	Yes (modbus)	Yes (modbus)	Yes (modbus)	Yes (modbus)	Yes (modbus)	Yes (modbus)	Yes (modbus)
Maximum program memory	512 blocks	512 blocks	512 blocks	512 blocks	512 blocks	512 blocks	512 blocks
Data logging	Yes (ELC-MEMORY)	Yes (ELC-MEMORY)	Yes (ELC-MEMORY)	Yes (ELC-MEMORY)	Yes (ELC-MEMORY)	Yes(ELC-MEMORY)	Yes(ELC-MEMORY)
Extensions	Yes	Yes	Yes	Yes	Yes	Yes	Yes
High speed input	No	IB,IC(60KHZ)	IB,IC(60KHZ)	IB,IC(60KHZ)	IB,IC(60KHZ)	IB,IC(60KHZ)	IB,IC(60KHZ)
RTC	Yes	Yes	Yes	Yes	Yes	Yes	Yes
HMI	Yes	Yes	Yes	Yes	Yes	Yes	Yes
PWM	No	No	No	Q5,Q6(333Hz)	Q5,Q6(333Hz)	Q5,Q6(333Hz)	Q5,Q6(333Hz)

Extensions Units(IO) for ELC-18 CPU	ELC-E-16AC-R	ELC-E-16DC-D-R	ELC-E-16DC-DA-R	ELC-E-16DC-D-TN	ELC-E-16DC-DA-TN
Inputs	8 digital	8 digital	8 digital	8 digital	8 digital
of which can be used in analog mode	none	none	2 (0 to 10V)	none	2 (0 to 10V)
Input/supply voltage	110-240V AC	12-24V DC	12-24V DC	12-24V DC	12-24V DC
Permissible range with signal "0" with signal "1" Input current	85 ... 265 V AC 100 ... 253 V DC max. 40 V AC 0.03 mA min. 79 V AC, 0.08 mA	10.8 V ... 28.8 V DC max. 3 V DC,1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA	10.8 V ... 28.8 V DC max. 3 V DC, 1mA min. 8 V DC,1.5 mA
Outputs	8relays（Q1-Q4,3A,Q5-Q8,10A）	8 relays（Q1-Q4,3A,Q5-Q8,10A）	8relay（Q1-Q4,3A,Q5-Q8,10A）	8 transistors(NPN)	8 transistors(NPN)
Continuous current	10 A with resistive load; 2 A with inductive load	10 A with resistive load; 2 A with inductive load	10 A with resistive load; 2 A with inductive load	0.3 A	0.3 A
Short-circuit protection	External fuse required	External fuse required	External fuse required	External fuse required	External fuse required
Switching frequency	2 Hz with resistive load; 0.5 Hz with inductive load	2 Hz with resistive load; 0.5 Hz with inductive load	2 Hz with resistive load; 0.5 Hz with inductive load	2 Hz	2 Hz
Connection cables	2 x 1.5 mm² or 1 x 2.5 mm²				
Ambient temperature	0 to + 55 ºC				
Storage temperature	– 40 ºC to + 70 ºC				
Degree of protection	IP20				
Certification	CE				
Mounting	On 35 mm standard mounting rail, 4 MW, or wall-mounting				
Dimensions	W x H x D (72 x 90 x 53 mm)				

Communication module for ELC-18 CPU	ELC-RS485	ELC-Ethernet-DC	ELC-Ethernet-AC	ELC-SMS-D-R
Input/supply voltage	12-24V DC	12-24V DC	110-240V AC	12-24V DC
Description	isolated 485 converter,used to bring out the terminals of RS485 port built-in ELC-12 series CPU for connection with third party devices.	Ethernet module connecting to ELC-12 CPU units，DC type.	Ethernet module connecting to ELC-12 CPU units，AC type.	GSM/SMS module connecting to ELC-18 CPU units(6I/4O+ 10 message IO)
Connection cables	2 x 1.5 mm² or 1 x 2.5 mm²			
Ambient temperature	0 to + 55 ºC			
Storage temperature	– 40 ºC to + 70 ºC			
Degree of protection	IP20			
Certification	CE			
Mounting	On 35 mm standard mounting rail, 4 MW, or wall-mounting			
Dimensions	W x H x D (72 x 90 x 53 mm)			

A.3 Switching capacity and service life of the relay outputs

Ohmic load

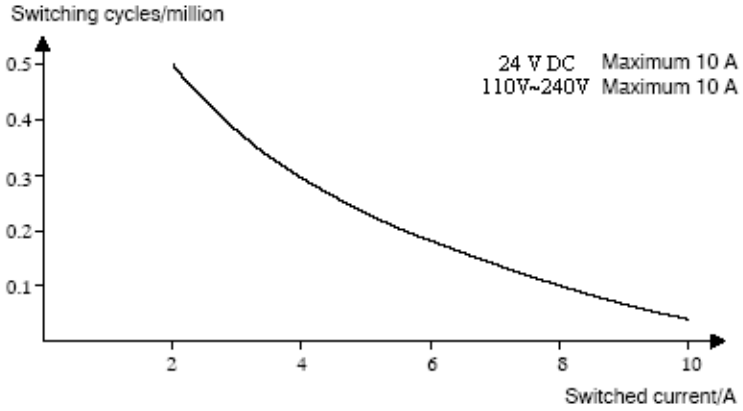


Figure A Switching capacity and service life of the contacts with ohmic load (heating)

Inductive load

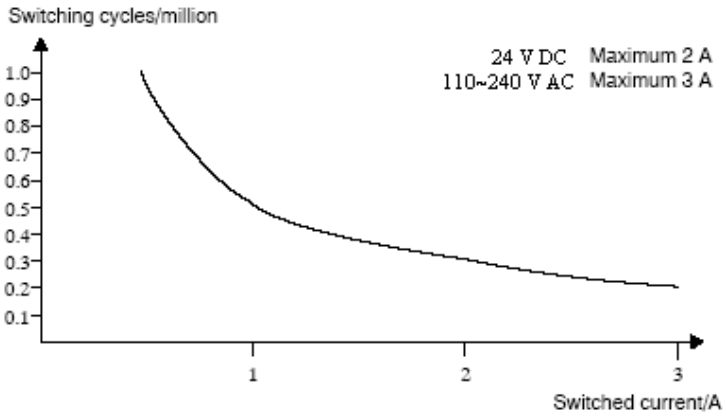
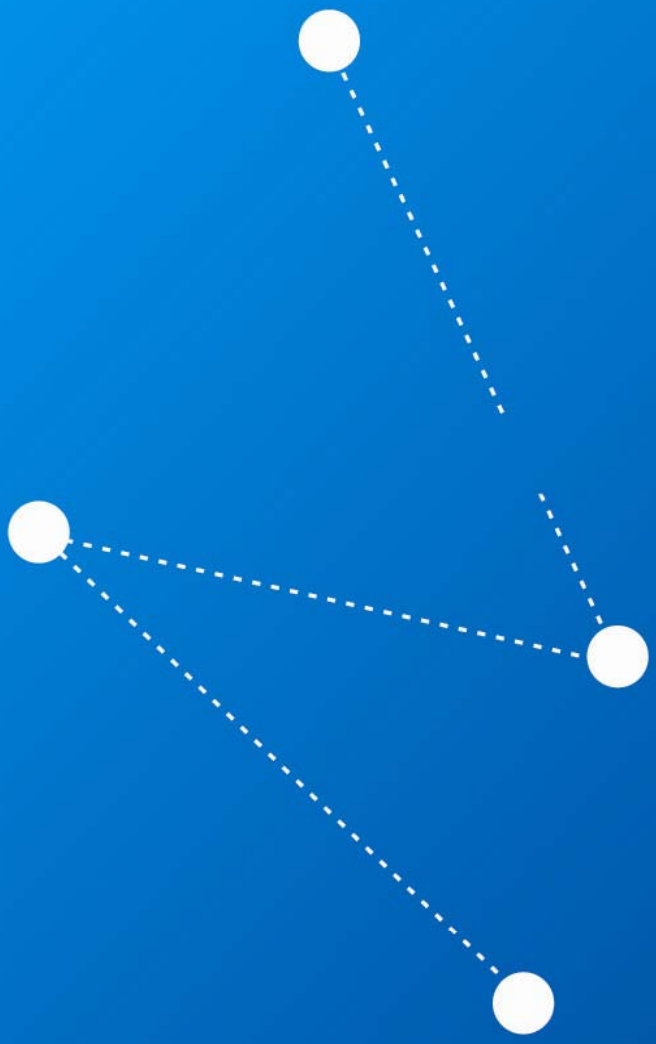


Figure B Switching capacity and service life of the contacts with high inductive load (contactors, solenoid coils, motors).



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